

## QPACE 4 project

- ▶ Aims to design an ARM SVE environment optimized for Lattice QCD
  - ▷ Prototype at the University of Regensburg, Germany, by 2020
- ▶ Pursues evaluation and enhancement of existing SVE software toolchain and upcoming SVE hardware technologies for Lattice QCD applications
- ▶ Enables Lattice QCD code optimized for SVE architectures, e.g., the Japanese flagship supercomputer "Post-K" announced for 2021