Design layout and expected performance of Inner Tracker for ATLAS Phase 2 Upgrade

Swagato Banerjee



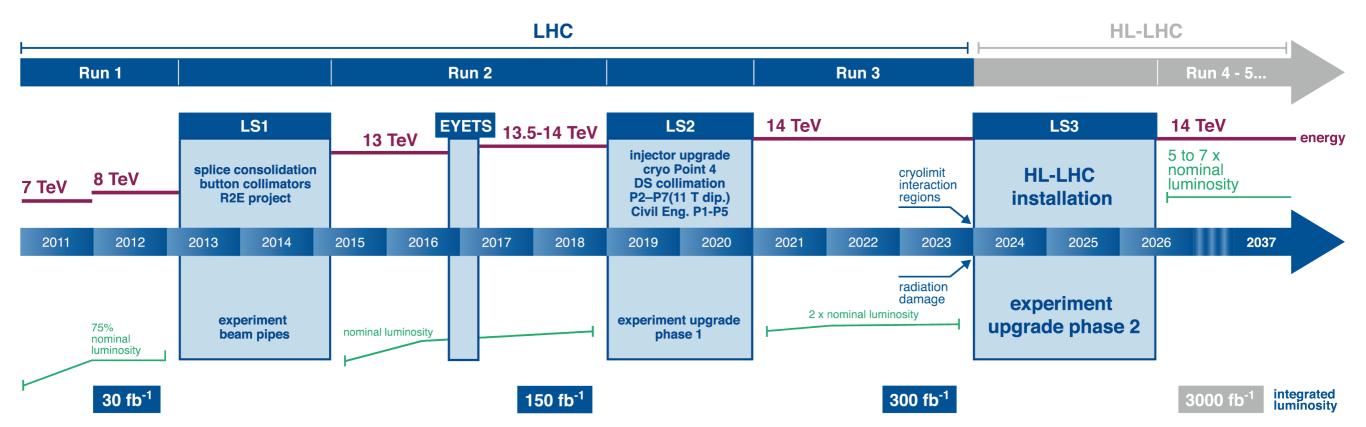
DPF Meeting 2017 APSIPARTICLES & FIELDS

Fermilab

High Luminosity LHC (HL-LHC) upgrade

LHC / HL-LHC Plan





Phase 2 Upgrade

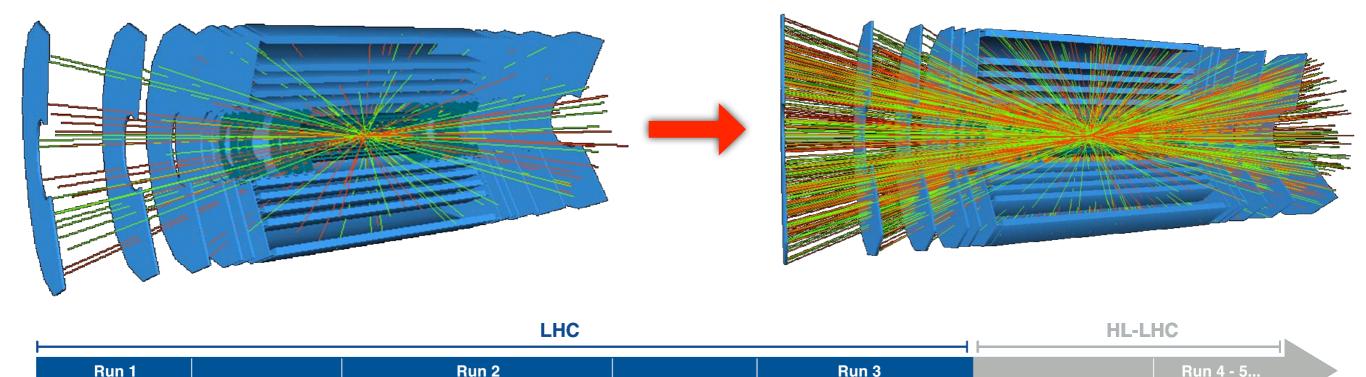
Installation: 2024 onwards, Preparation: now

10 times integrated luminosity: 3000 fb⁻¹ [radiation damage] 5-7 times instantaneous luminosity: 7.5x10³⁴ cm⁻²sec⁻¹ [particle density]

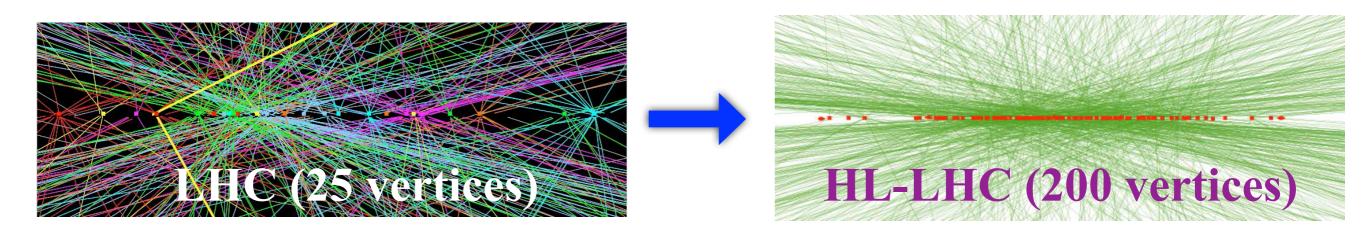




HL-LHC challenge



Factor of 10 increase in Pile-Up (# of interactions per bunch crossing)







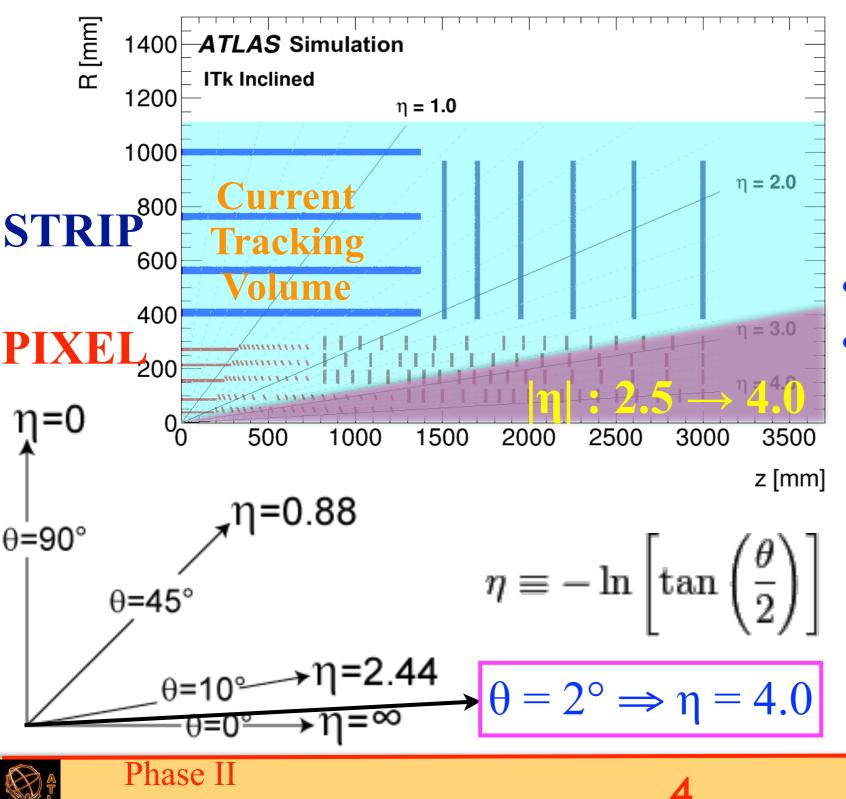






Very forward extension of Inner Tracker

Proposal: all silicon high granularity Inner Tracker (ITk) with \eta < 4



ITk Upgrade

Higher acceptance for tracks: e, μ , τ , jets

Critical improvements inpile-up/fake-jet rejectionvery forward jet tagging from hard scatter process

> Very forward coverage only with Pixel detector



Extending the physics reach

Very forward ITk has large physics gains, eg. rare and exotic Higgs decays, Vector-Boson scattering, SUSY, etc. via improvements in $e/\mu/b$ -tag acceptance & resolution, E_T^{miss} resolution, pileup rejection.

Detector system	Trigger–DAQ	Inner Tracker	Inner Tracker + Muon Spectrometer	Inner Tracker + Calorimeter		
Object Performance Physics Process	Efficiency/ Thresholds μ [±] e [±]	b-tagging	μ [±] Identification/ Resolution	Pile-up rejection	Jets	$E_{\mathrm{T}}^{\mathrm{miss}}$
$H \longrightarrow 4\mu$ VBF $H \rightarrow ZZ^{(*)} \rightarrow \ell\ell\ell\ell$ VBF $H \rightarrow WW^{(*)} \rightarrow \ell\nu\ell\nu$ SM VBS ssWW		~			\$ \$ \$	 ✓ ✓
SUSY, $\chi_1^{\pm}\chi_2^o \rightarrow \ell b \bar{b} + X$ BSM $HH \rightarrow b \bar{b} b \bar{b}$	 ✓ 		~	~	\ \	~

ATLAS Phase-II Upgrade Scoping Document, CERN-LHCC-2015-020. LHCC-G-166 (2015)

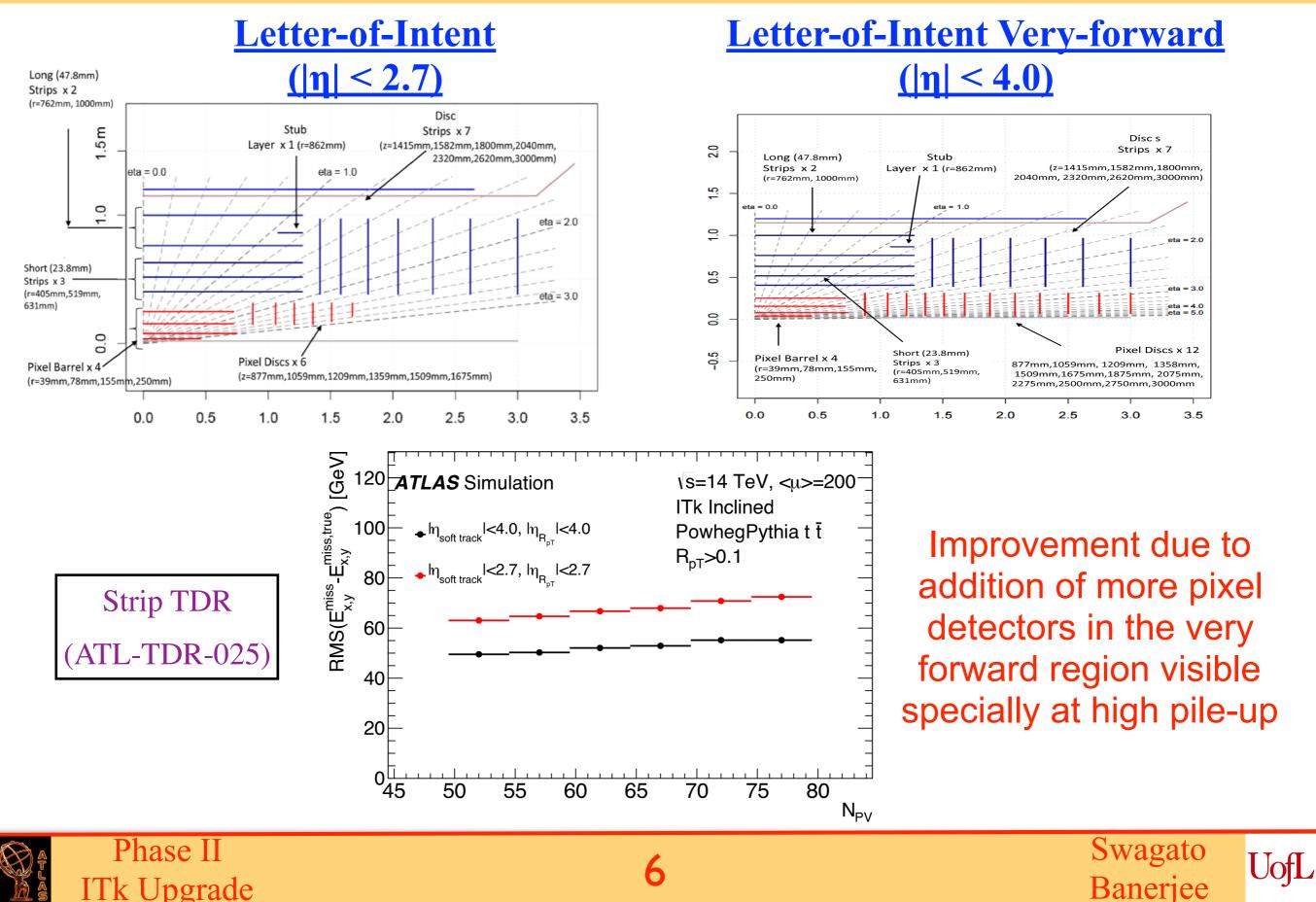


Phase II ITk Upgrade

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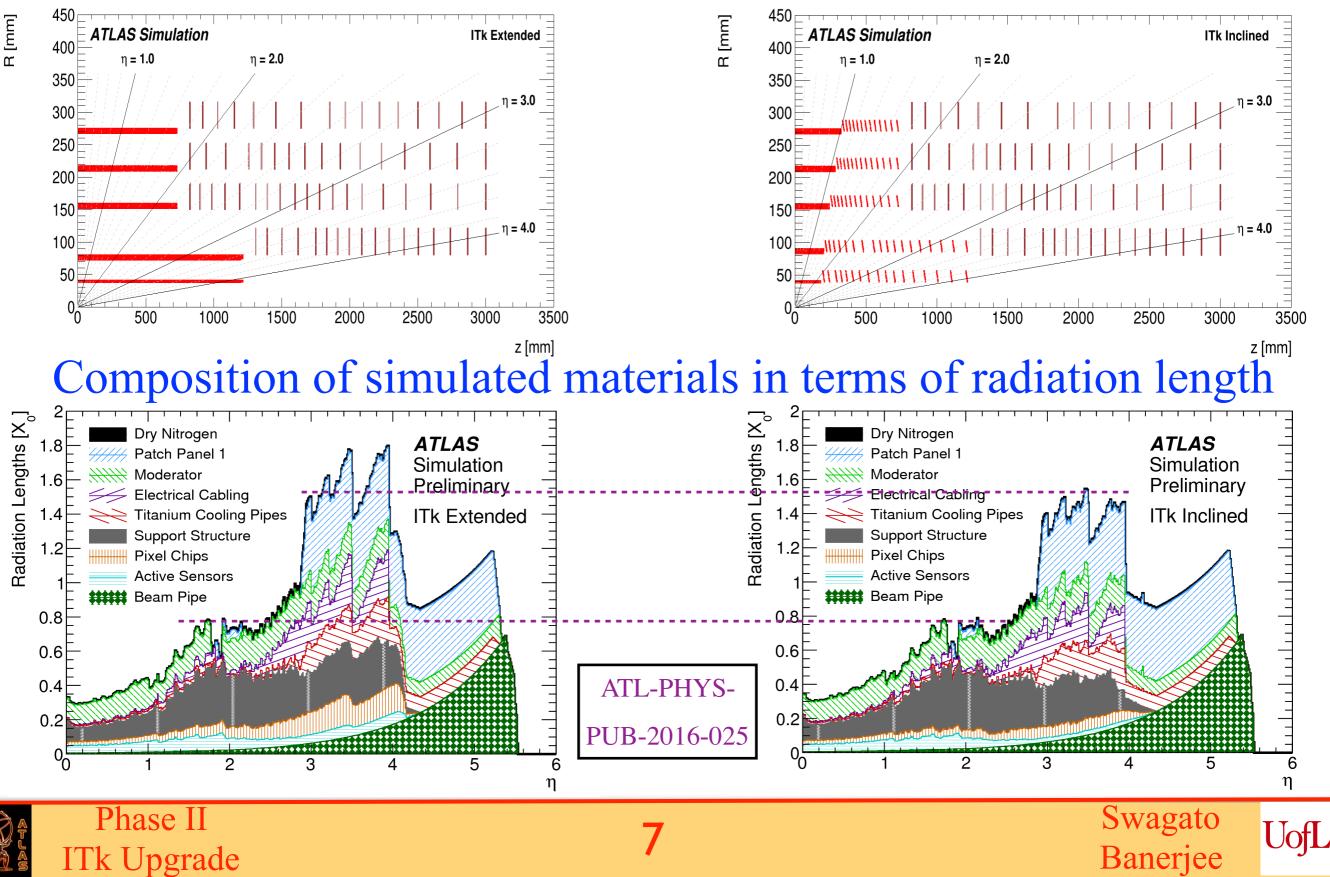
Evolution of ITk layouts (2012-2014)



Evolution of ITk layouts (2015-2016)



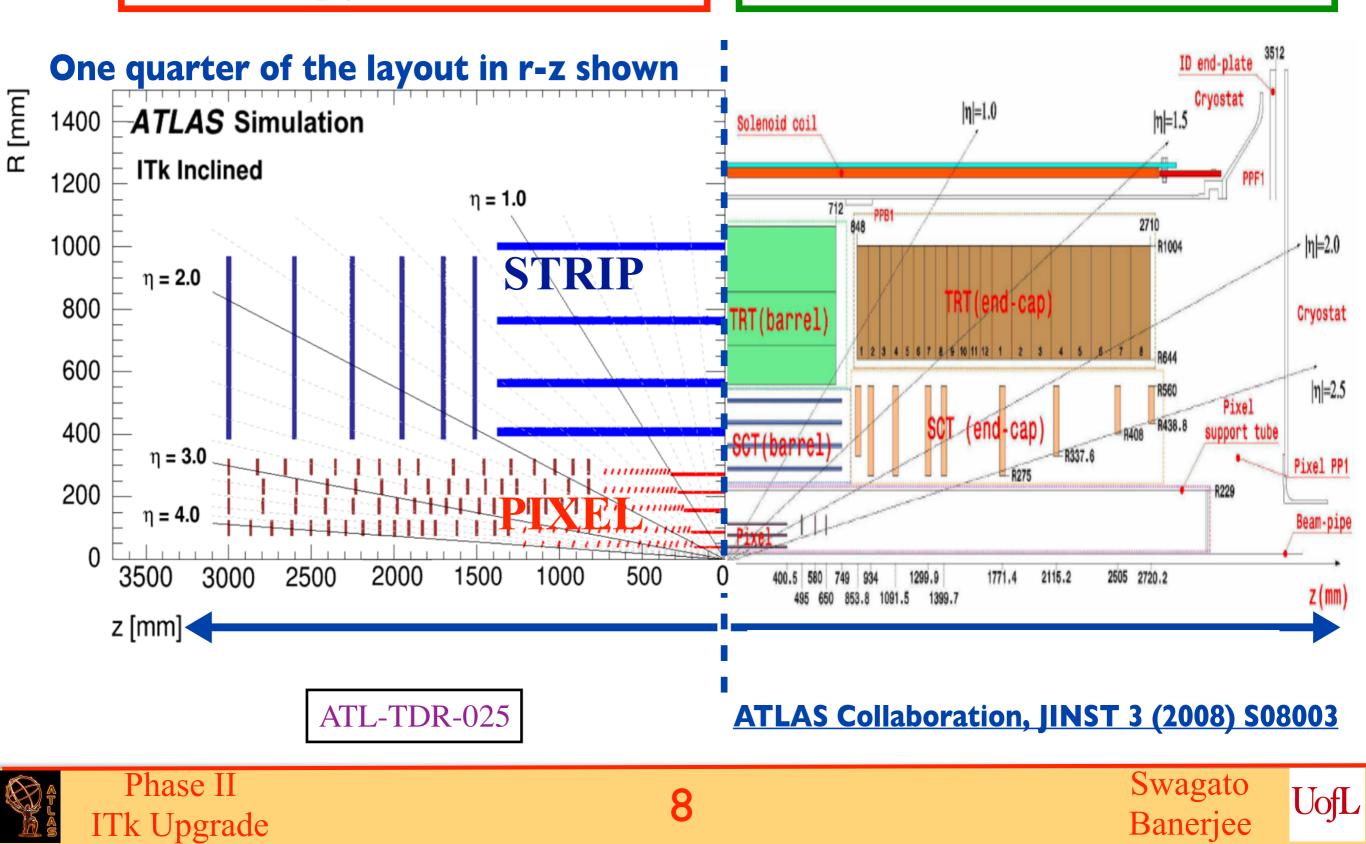




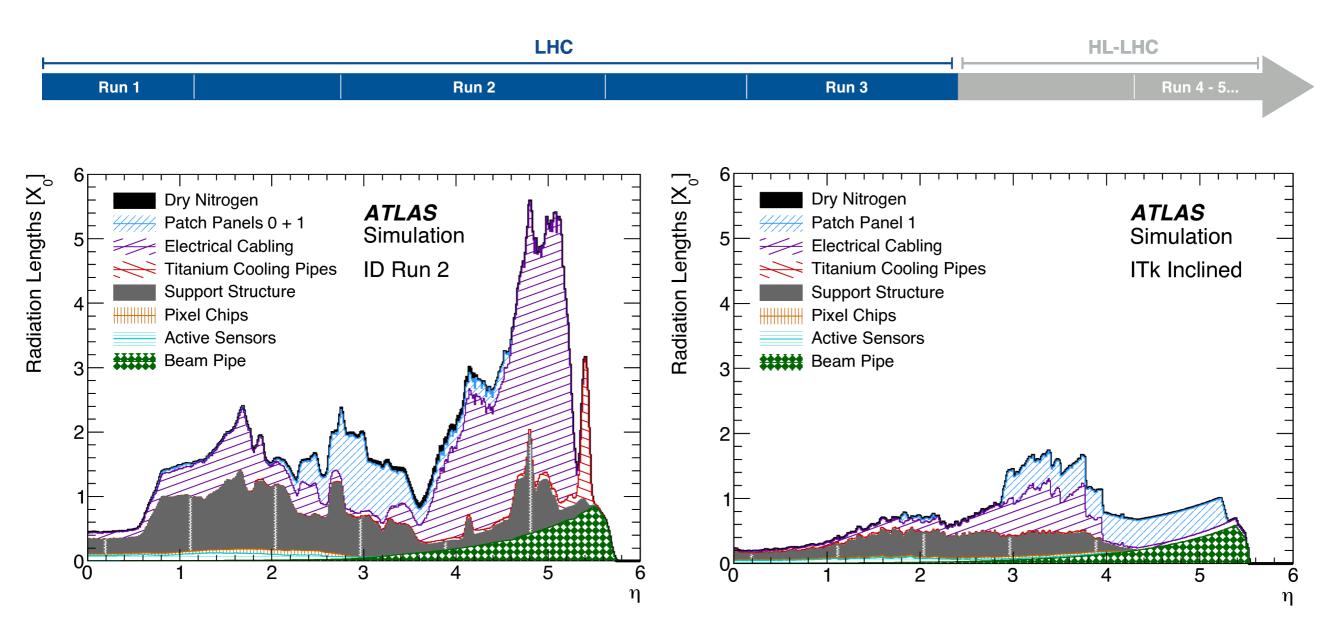
ITk layout for Strip TDR (April 2017)

Phase II Upgrade all-silicon ITk

Current silicon and straw tracker



Material description



Tracks travel transverse to inclined modules and thus require less materials to provide coverage up to $|\eta| < 4.0$ in the new ITk layout.

ATL-TDR-025

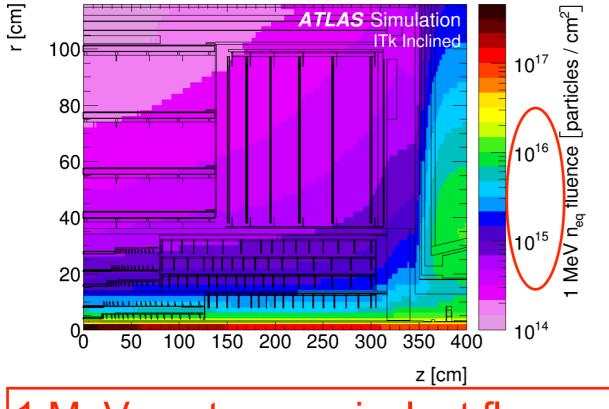


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Radiation damage

FLUKA simulation normalized to 3000 fb⁻¹ of pp collisions at 14 TeV:



1 MeV neutron equivalent fluence

- Possibility to extract & replace inner pixel layers if needed
- Newer technology for radiation hard sensors [hybrid / CMOS]

ATL-TDR-025

- Thinner silicon sensors
- Robust readout system

Layer	$\begin{array}{c} \mathbf{Radius} \\ [\mathrm{mm}] \end{array}$	$\begin{array}{c} \mathbf{Maximal \ Fluence} \\ [\mathrm{n_{eq}}/\mathrm{cm}^2 \] \end{array}$	Maximal Dose [MRad]
Strips			
Long Strips	762	3.8×10^{14}	9.8
Short Strips	405	7.2×10^{14}	32.5
End-cap	385	1.2×10^{15}	50.4
Pixels			
Jayer 0	39	1.87×10^{16}	1268
ayer 1	75	0.59×10^{16}	549
ayer 2	155	0.22×10^{16}	129
layer 3	213	0.15×10^{16}	87
Layer 4	271	0.11×10^{16}	53
End-cap	80	0.62×10^{16}	477



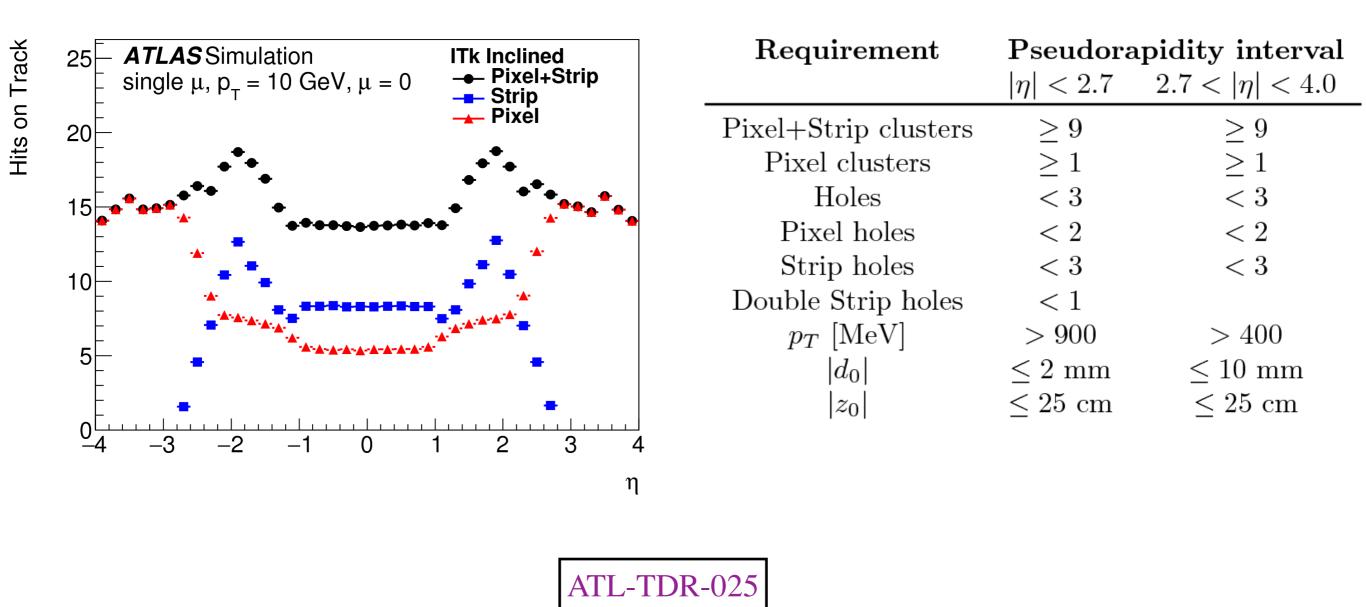
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Track reconstruction

Non-homogeneous detector and reduced magnetic field in forward regions:

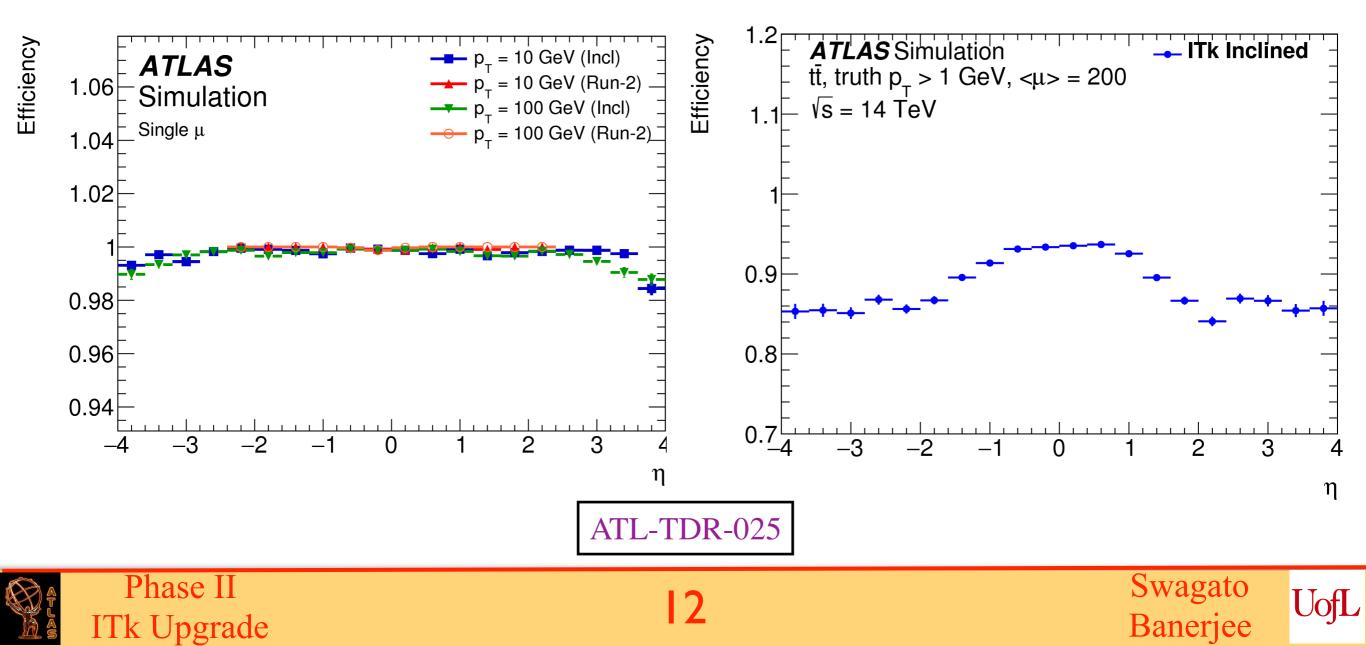




Phase II

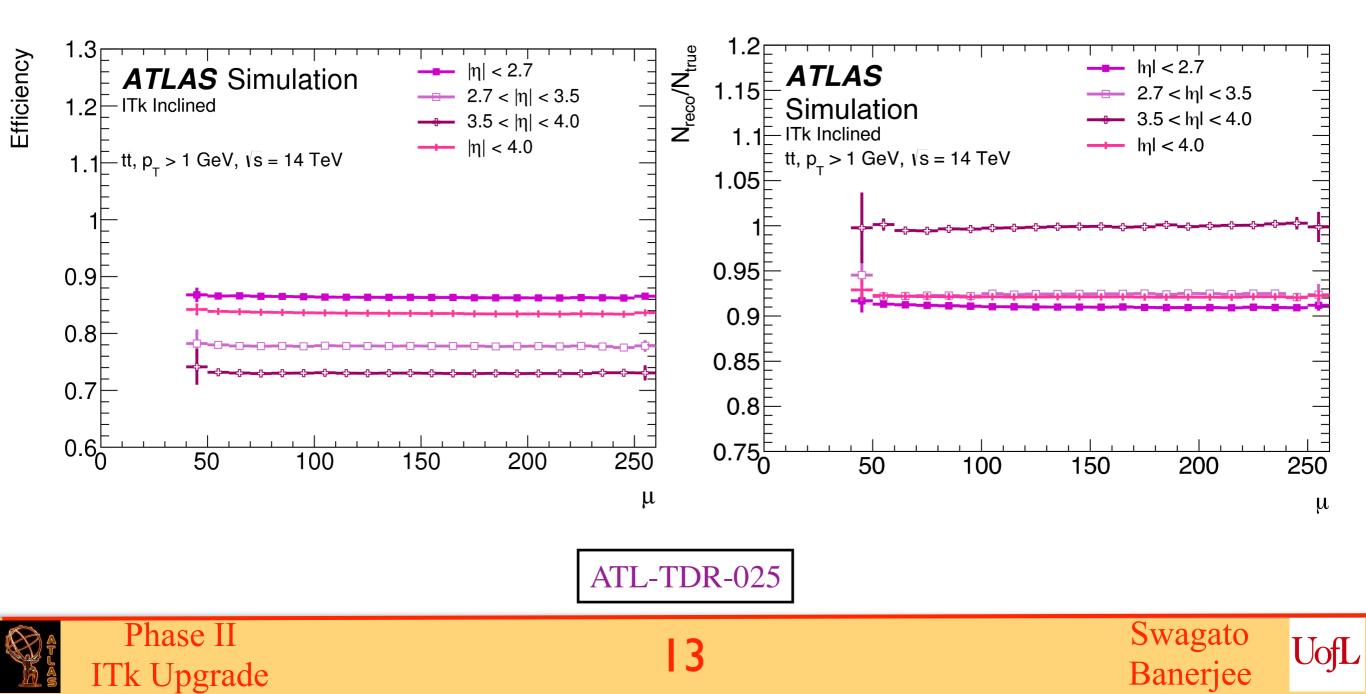
Tracking efficiency

- Good efficiency defined as fraction of stable, charged, primary particles ($p_T > 1$ GeV, $|\eta| < 4.0$) for a given reconstructed track
- Uniform efficiency of single-muon tracks versus η
- Efficiency ~ 85% (95%) in forward (central) region in tt samples due to high particle density



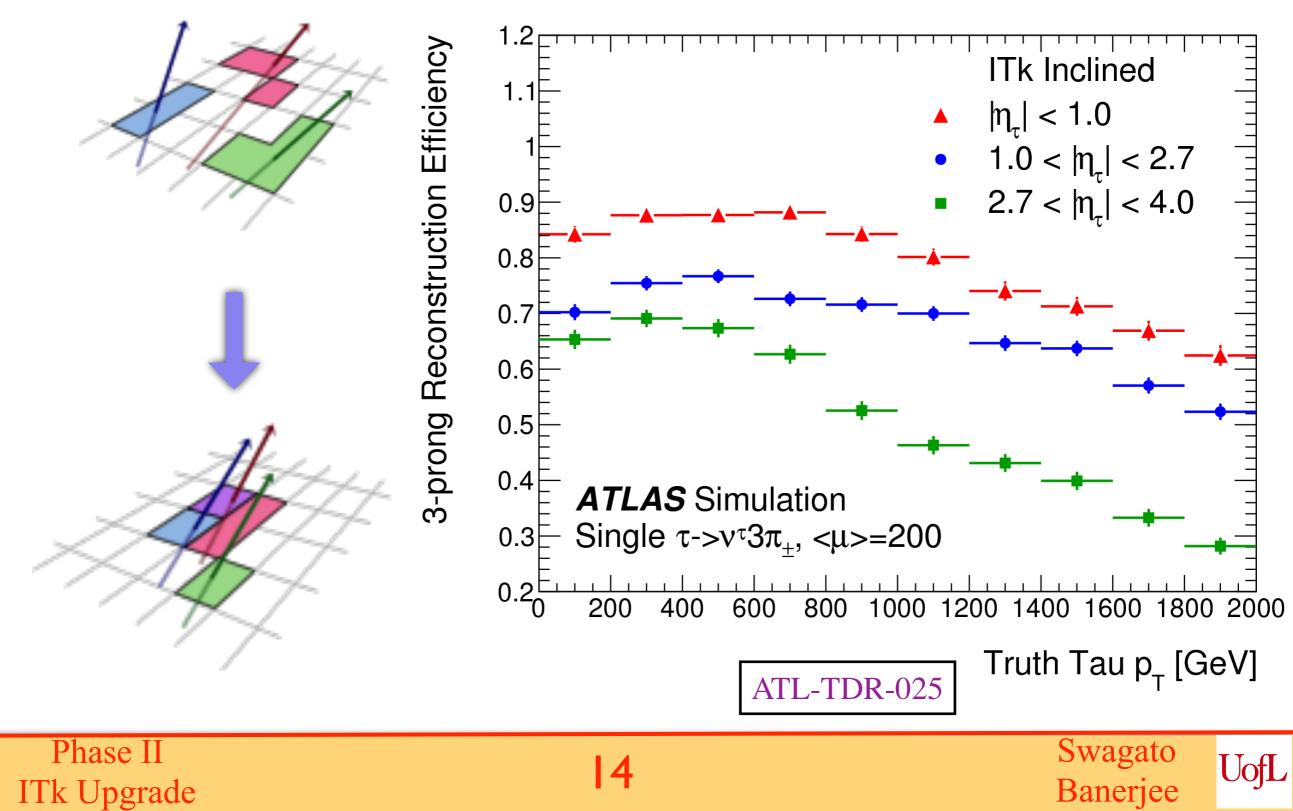
Pile-up robustness

- Track reconstruction efficiency is stable as fraction of pile-up
- Inclusive rate of number of reconstructed over number of generated as measure of non-fake tracks also stable vs pile-up

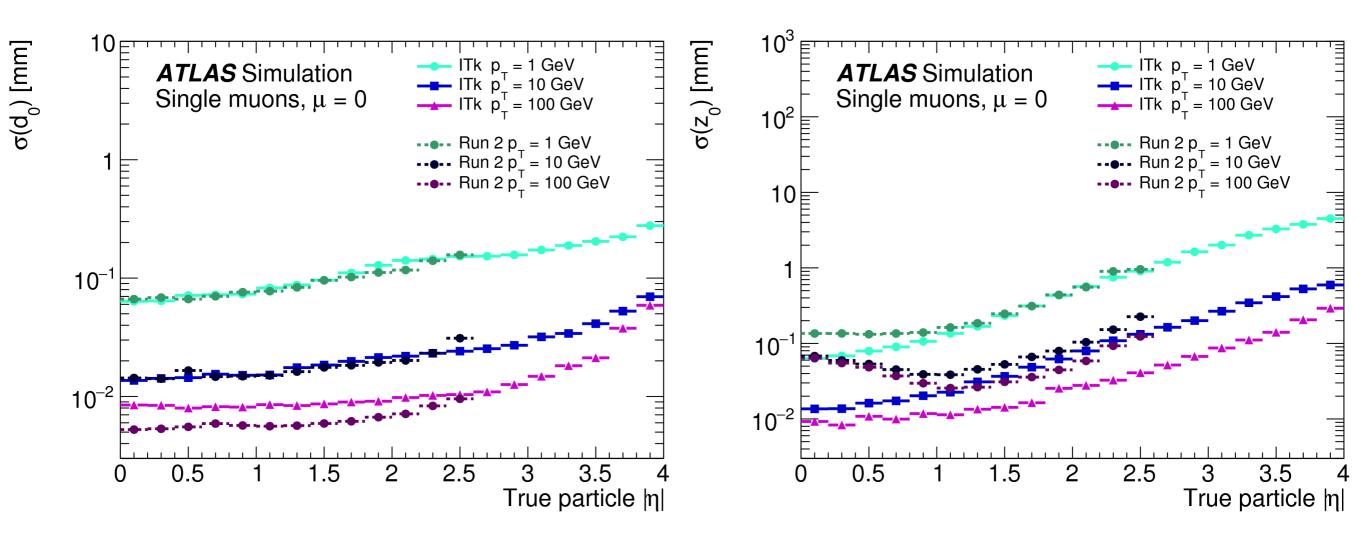


Tracking in dense environment

• Good efficiency to resolve all tracks in highly collimated boosted 3-prong τ decays in dense environment



Impact parameter resolutions



Excellent impact parameter resolution

Phase II

ITk Upgrade

muons with $p_T = 10 \text{ GeV}$	$\sigma(d_0)$	$\sigma(z_0)$	
$ \eta < 3.5$	40 µm	300 µm	



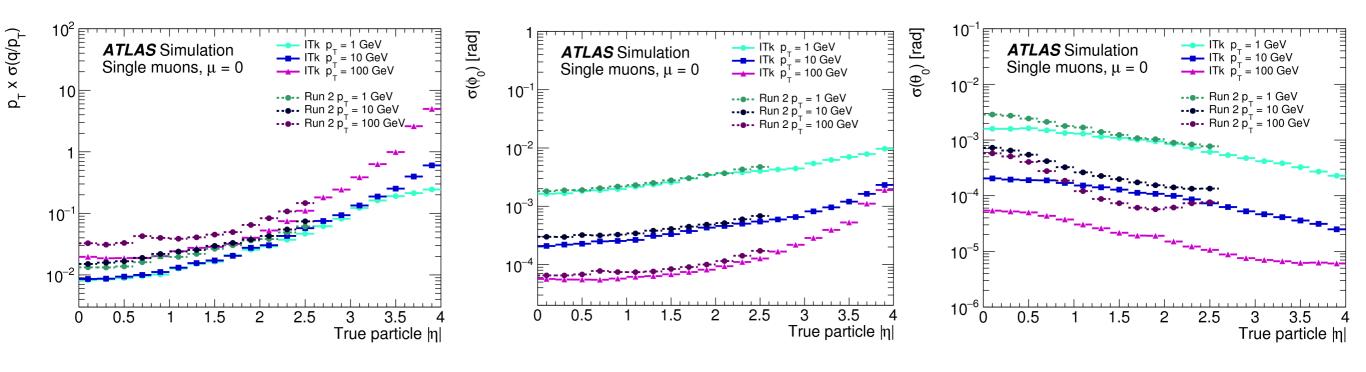


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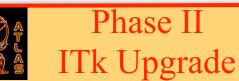


Impact parameter resolutions

Improved p_T resolution in central part w.r.t current detector, but degraded in forward due to reduced lever-arm in magnetic field



ATL-TDR-025

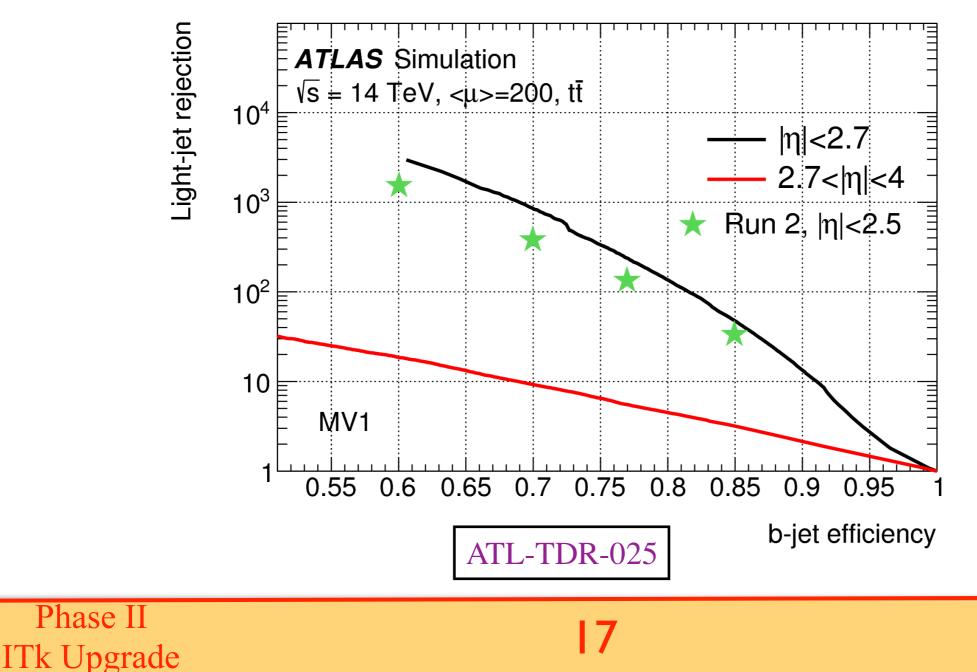


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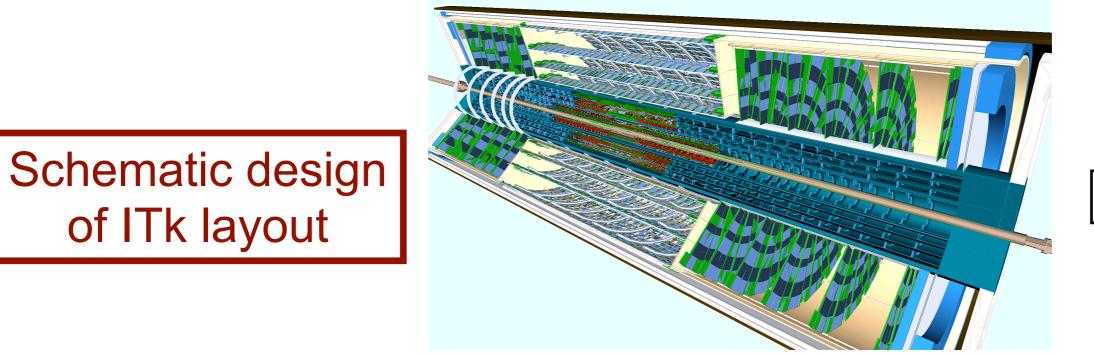
B-tagging performance

- Comparison w.r.t MV1 algorithm used in Run 2 shown
- B-tagging implemented all the way up to $|\eta| < 4.0$
- For efficiency ~ 70%:
 - rejection for ITk is ~ 1000 (10) for $|\eta| < 2.7$ (4.0)
 - factor of 2 better than Run 2





Summary



ATL-TDR-025

- ITk simulation helps to choose optimal detector layout
- New all silicon ITk planned for Phase II upgrade
- Strip TDR finalized in April 2017: 4 barrel, 2x6 disks endcap
- Pixel TDR timeline is end of 2017 : 5 layer barrel, endcap rings
- Inclined layout for Pixel
 - less material traversed
 - new developments for support structure
 - improvements w.r.t Run2 observed



