

ADC Study Group

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Purpose

- Choose an ADC architecture and IC process
- Maximize the probability that the device will have excellent performance and will be easy to use (minimize risk).

Status

- Meetings started in early August.
- Three candidate architectures have been reviewed
 - Sigma-Delta
 - SAR
 - Pipeline
- We have focused on two candidate IC processes
 - 180 nm CMOS
 - 65 nm CMOS
- I think the group will be able to make decisions in the next 2-4 weeks.