

# PD Electronics Meeting

Zelimir Djurnic, Giovanni Franchi, Deywis Moreno

November 9, 2017

## General Institutions list for electronics WG

- 1 Read-Out Electronics Modules: **CBPF, UFJF, Unicamp, ANL, UAN, University of Michigan**
- 2 SiPM Ganging/Mounting: **CBPF, UFJF, Unicamp, University of Michigan**
- 3 Cables:
- 4 PD Electronics Power Supplies: **ANL**
- 5 Grounding scheme for PD electronics/cables/power supplies: **ANL**
- 6 PDS Monitoring/Calibration System: **Univ. Warkwick, ANL**

# Important remarks

- ① WBS list is for post-TDR release
- ② TPR needed in April-2019
- ③ TP needs to be submitted in April 2018
- ④ R&D plans to have information needed to complete TDR  
See Dave's talk <https://indico.fnal.gov/event/15705/contribution/2/material/slides/0.pdf>
- ⑤ For R&D activities funding from National Agencies could be required.

## Initial List of R&D tasks for the electronics WG

- ① Ganging: passive vs active studies
- ② Choosing electronics architecture
- ③ Processing signals or charge
- ④ Overall electronic system simulation
- ⑤ Trigger strategy

Decisions based on Physics Simulations, Cost/Performance.

Some of the points listed above could be defined using simulations.

## How to proceed

- 1 You can send to us comments about the presented points or add new ones:

[zdjurcic@anl.gov](mailto:zdjurcic@anl.gov)

[gfranchi@agescientific.com](mailto:gfranchi@agescientific.com)

[deymoreno@uan.edu.co](mailto:deymoreno@uan.edu.co)