

BERKELEY LAB

LAWRENCE BERKELEY NATIONAL LABORATORY



Open Source LLRF Stack

C. Serrano LLRF'19 Workshop

Story





My first LLRF Workshop (2007)





Community



"A feeling of fellowship with others, as a result of sharing common attitudes, interests, and goals"

You and your research





Richard Hamming

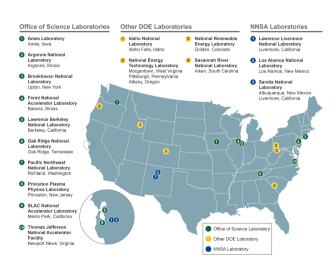
Why Open Source?



A statement on the **attitude** question. Letting you **see** what we do and hope we can share our **interests** and achieve our **goals**, together.

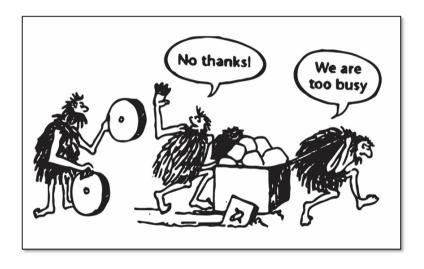
DOE Lab tour





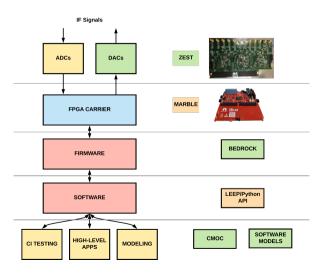
Very productive visits & discussions





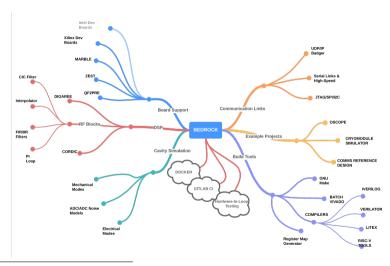
Open Source LLRF Stack





Bedrock

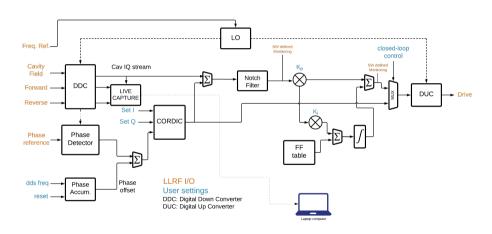




[&]quot;Honey, you are the rock, Upon which I stand" - Coldplay

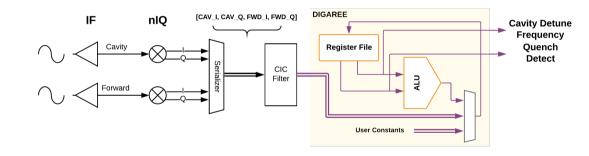
RF Controls





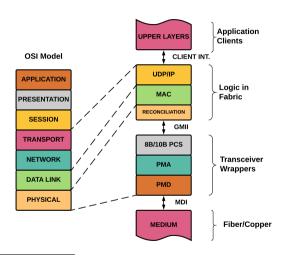
Real-time SRF Analyzer





Ethernet in fabric: Packet Badger

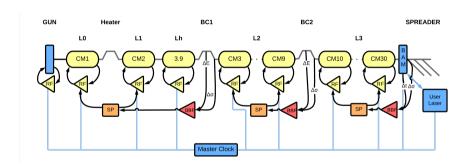




L. Doolittle et. al., "FPGA Communications based on GbE", ICALECPS'11

Software Simulation Engine

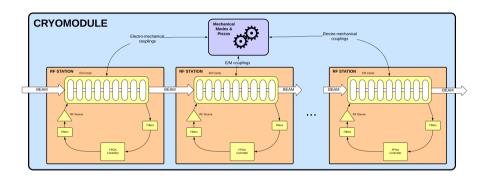




C. Serrano et. al., "End-to-end FEL Beam Stability Simulation Engine", IPAC'16

FPGA Simulation Engine (CMOC)





C. Serrano et. al., "CyroModule-On-Chip Simulation Engine", ICALCECPS'17

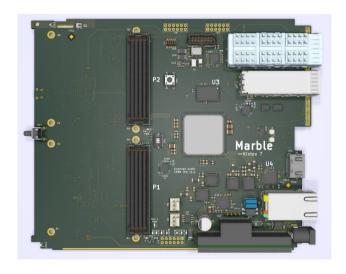
Marble-mini





Marble





Zest





G. Huang et. al., "Low Noise Digitizer Design for LCLS-II LLRF", NAPAC'16

Github



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Marble Marker Simple AMC FMC confer module with SFP, 2x FMC, PoE PMM. V3 *3 *0 *D12 *D1 Updated on Aug 21		People	3>
3lobal-Feedback-Simulator			
Pyrnon ¥2 ★2 ⊕0 №0 Updated on Aug L			
Zest zest is a PMC rezzanine board with 6 ADC channels and 2 DACs ₹○ ★○ ①□ ∏□ Updated on Jun 3			
CMOC systradur-On-Chip Simulation Engine			

Where is this all used?





How does it all come together?









Those who don't think about the **future**Resolve the present With tools from the past.