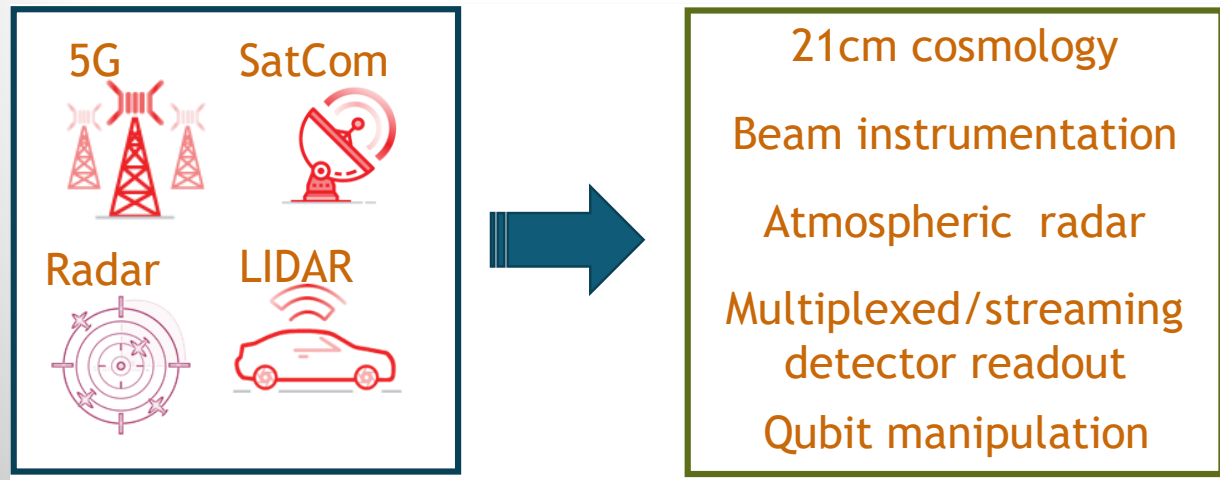


# RF Microelectronics

- New RF microelectronic platforms will catalyze new ways some HEP experiments are done
- Mass deployment of wireless devices in the commercial sector is driving rapid product development: datacomm (cellular, WiFi, IoT, satcomm), location services (GNSS), radar (defense, aviation, auto, weather)
- Drivers are low cost, power, volume, and efficient use of bandwidth

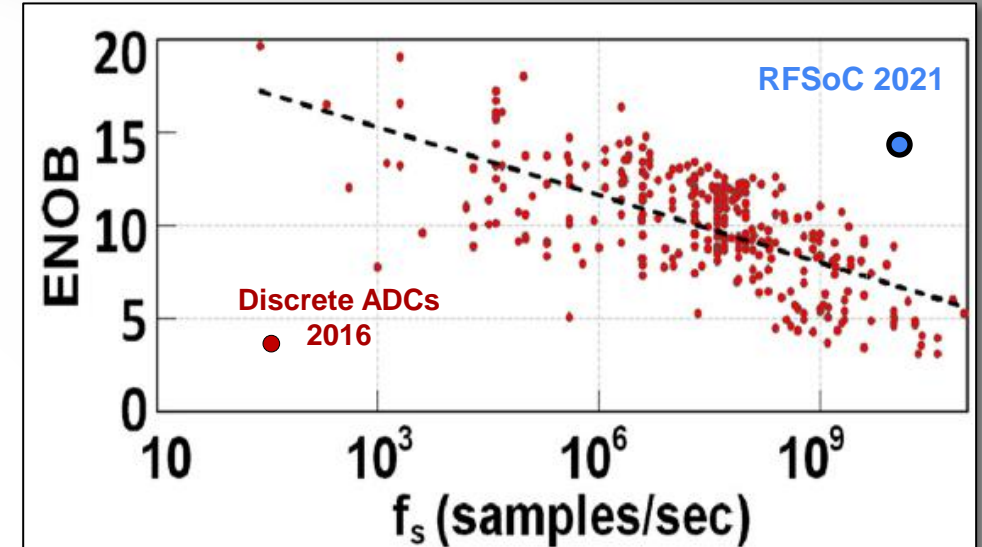


# Background

Prior to 2018, two bottlenecks stood in the way of fully-digital processing of high bandwidth, high dynamic range signals. First, ADC performance (as measured on the speed/resolution plane) was barely adequate to acquire multi-GHz signals at Nyquist rate, having only a few bits of resolution.

Therefore, heterodyne techniques involving costly, sensitive analog preconditioning circuitry (mixers, local oscillators, quadrature couplers, etc.) were used to translate signals down to intermediate frequencies for lower-rate digitization.

Second, the interface that transfers digitized data between the high-speed ADC and first-stage digital processor became complex and power-consuming, and required expert circuit layout techniques to avoid timing errors. The cost and design effort to realize high-bandwidth, high-resolution digital processors relegated them to specialty markets.



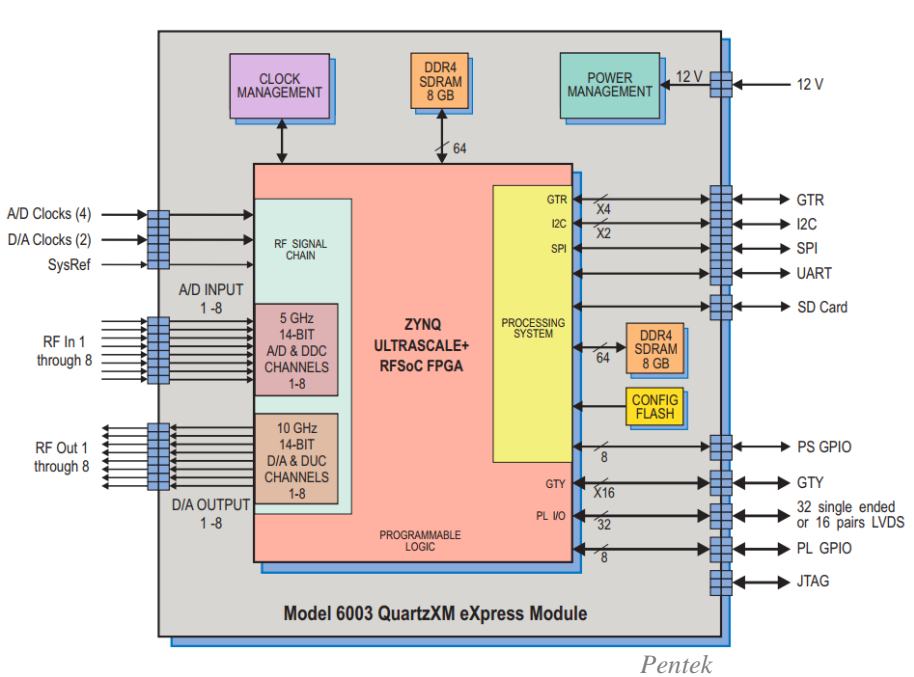
2018: introduction of Xilinx Ultrascale+ RFSoc. Multiple, high-rate ADCs & DACs in 14nm CMOS with rich fabric of fast programmable logic, memory, & quad-core ARM processors. ADCs sampling speed/resolution/power FOM exceeding state-of-the-art discretes.

→ Enabled 'software-defined radio' (digital processing at RF rates replacing analog conditioning)

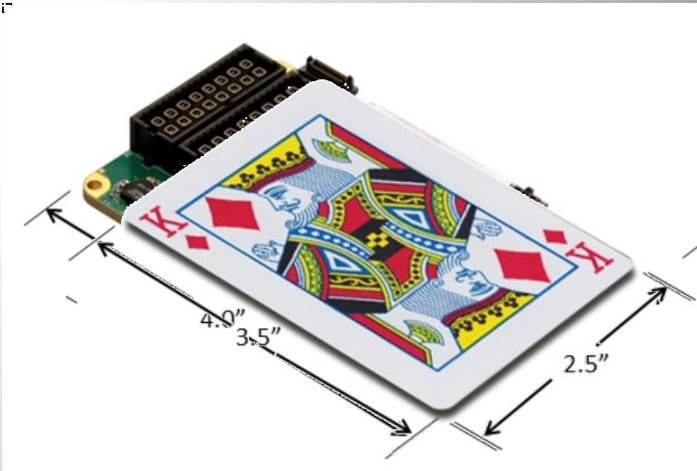
→ Order-of-magnitude reduction in PCB development time

# Platform characteristics

- Combination of GHz-bandwidth data converters, abundant FPGA resources, and streaming I/O make them attractive for research instrumentation.
- Several vendors now offer the RFSoc chips on compact form-factor modules with turnkey base firmware and a variety of custom IP for typical applications. Performance and number of resource blocks are available for a fraction of the cost and size of conventional implementations.
- Xilinx has introduced four generations of RFSoc between 2018-2022, each time increasing the maximum ADC sampling rate by >14%, and further improvements are expected as new sub-10nm CMOS process nodes become available. Versal AI RF ACAP 2023.



14b ADC	14b DAC	Input BW	Logic	RAM	DSP slices	Transceiver
6 @ 5.9	6 @ 10	7.13	930	38	4.3	2@100
GSPS	GSPS	GHz	Kcells	Mb	K	Gbps



# RF in HEP

- Legacy HEP instrumentation centers around sensors and readout electronics for ionizing radiation
- Recently-proposed experiments need to sense or control EM radiation in the RF range, acquiring and/or controlling information using the classical properties of the EM field (frequency, phase, polarization)

## 1. Sensing RF emission from naturally-occurring sources

- *Cosmological 21cm emission from neutral hydrogen*
- *Askaryan radiation from neutrino interactions in polar ice*

## 2. Probing resonant systems

- *Haloscope-based search for wavelike dark matter*
- *Manipulation of superconducting qubits*

## 3. Highly-multiplexed transmission of analog information on microwave carrier

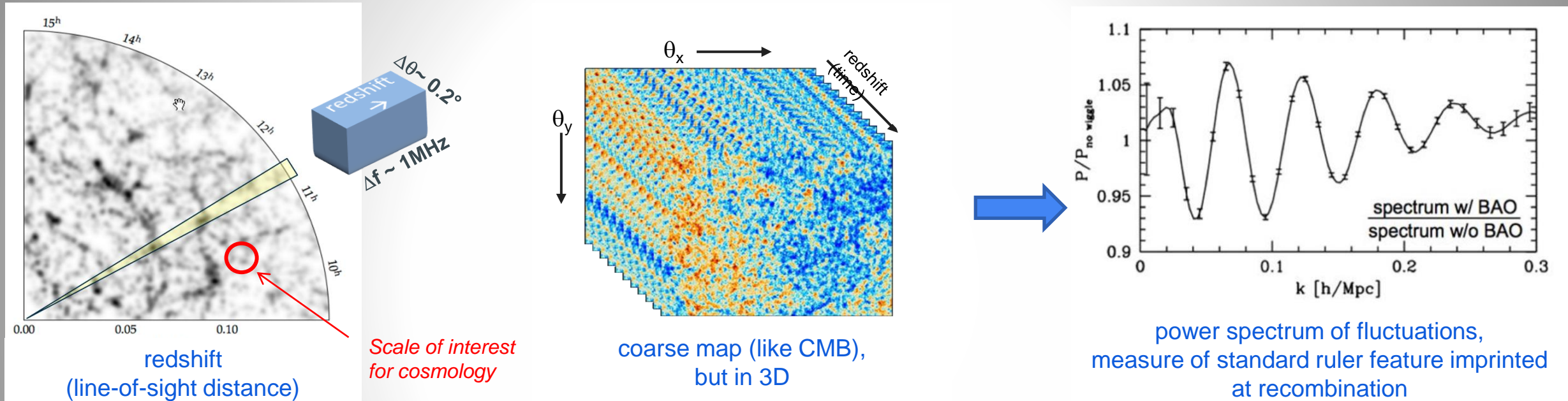
- *Highly-multiplexed readout of superconducting TES and MKID detectors*
- *Streaming readout*

## 4. Machine control and diagnostics

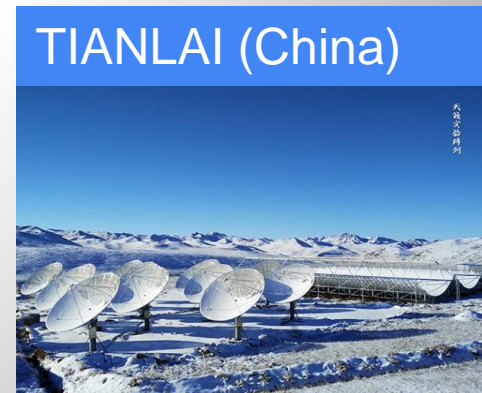
- *Beam position monitors*
- *Synchronization of accelerator and detector timing*

# 1. Sensing RF emission from naturally-occurring sources

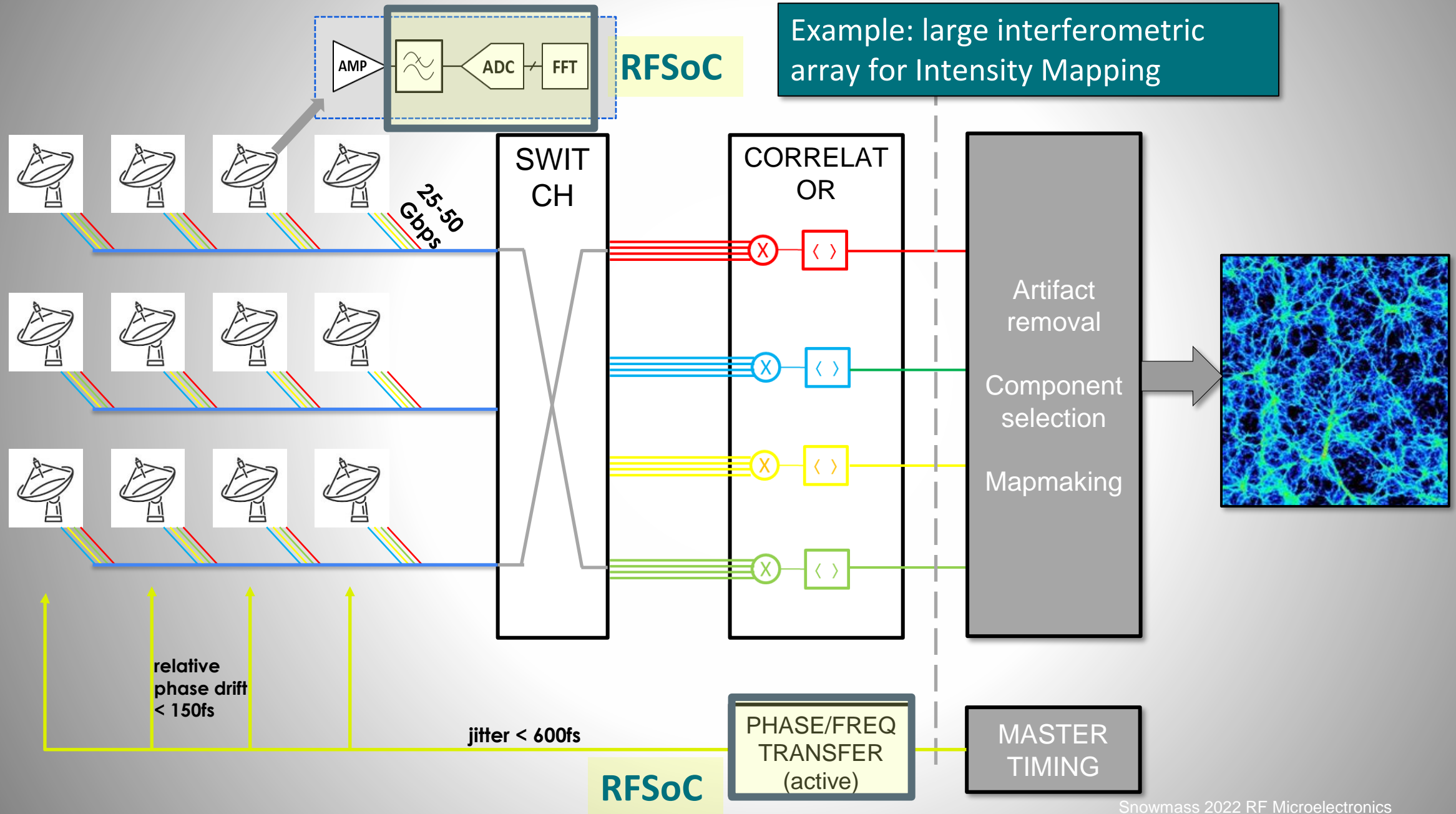
Large scale structure via **21cm Intensity Mapping**: tomographic reconstruction of the density field



Redshift range of most interest:  $z \sim 1$  (emergence of Dark Energy)

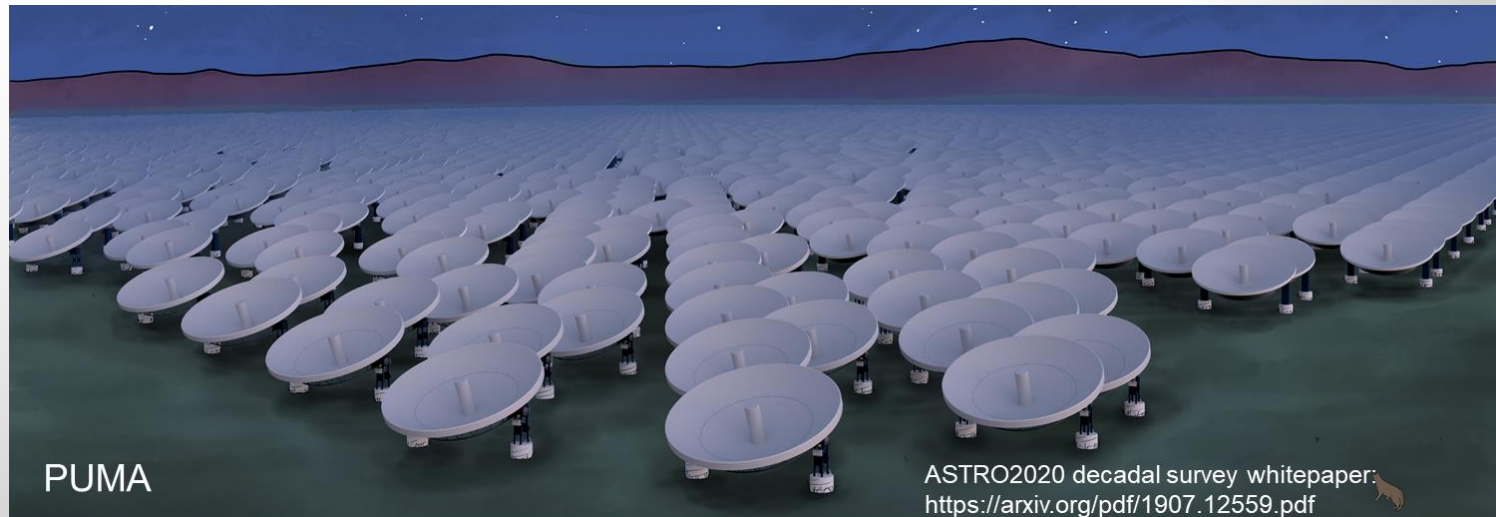


# 1. Sensing RF emission from naturally-occurring sources



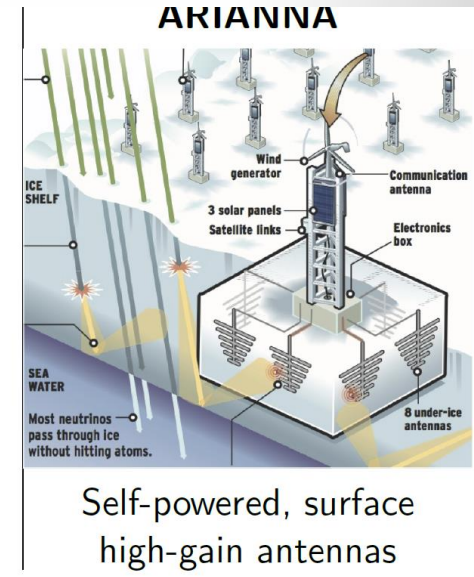
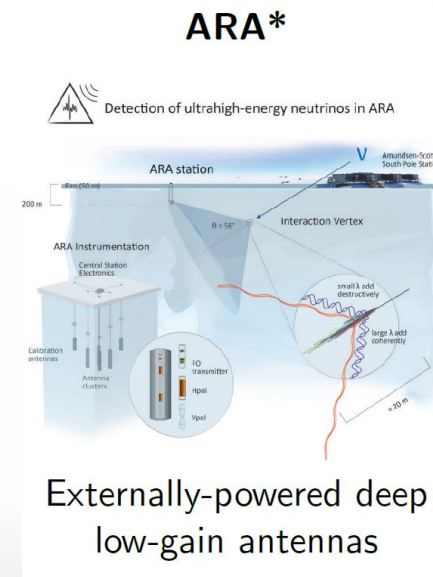
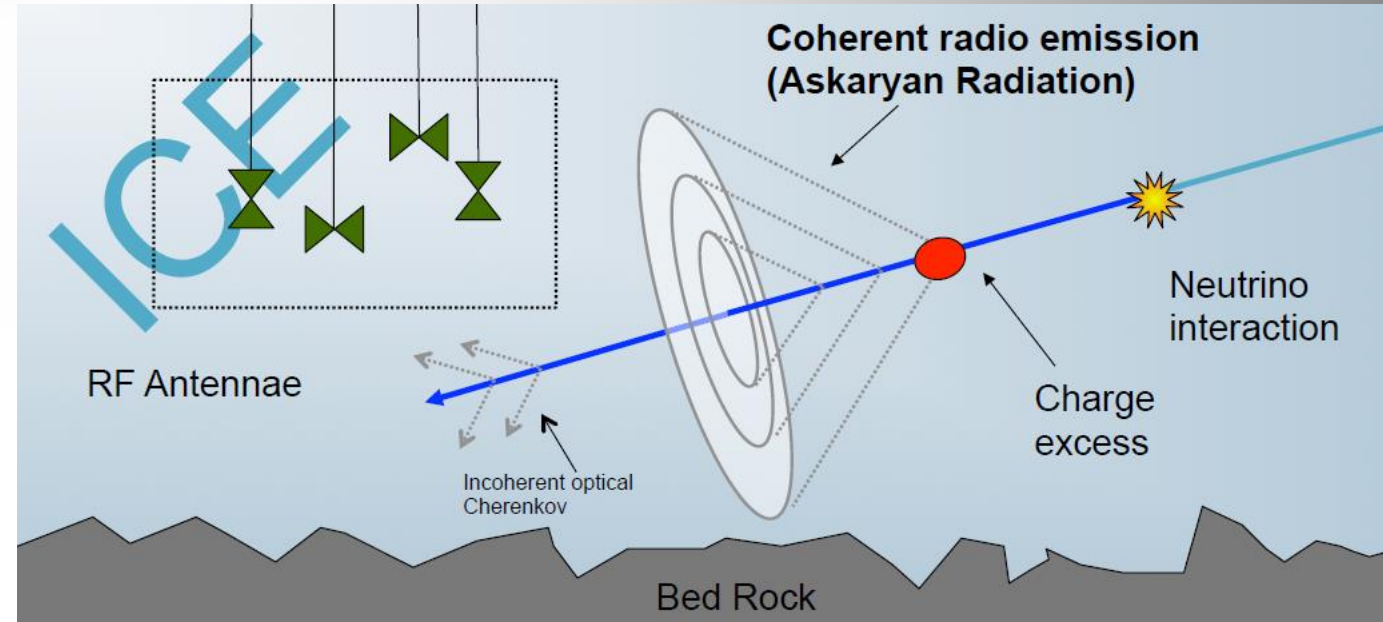
# A concrete example: Packed Ultrawideband Mapping Array (PUMA)

- A next-generation cosmic survey using intensity mapping of the 21-cm emission from neutral hydrogen
- Proposal submitted to the ASTRO2020 Decadal Survey and Snowmass LOI call
- Interferometric array of 32,000 (5,000) six-meter dishes closely packed
- Redshift range  $0.3 < z < 6$  corresponding to  $1100 < \nu < 200$  MHz
- Primary science goals:
  - **Probing physics of dark energy in the pre-acceleration era**
  - **Searching for signatures of inflation**
  - **Probing the transient radio sky (fast radio bursts and pulsars)**



# 1. Sensing RF emission from naturally-occurring sources

Radio detection of neutrinos is possible through the *Askaryan effect*, where particle showers in dense media (in this case polar ice) cause nanosecond-scale radio pulses in the frequency range between 30 MHz and 1 GHz. Consequently, fast, broad-band and low-noise receivers and systems are needed to efficiently detect the rare signals. Dozens to hundreds of such receivers in an interferometric phased array distributed over a few square km are being proposed, requiring cost- and power-efficient modules having exquisite (sub-nanosecond) timing precision to accurately reconstruct event topologies. Experiments such as ARA, ANITA, and ARIANNA have begun studying the diffuse flux of astrophysical neutrinos and candidate sources of extra-galactic neutrinos.



## 2. Probing resonant structures: qubit control and readout

Typical superconducting qubit is designed with its transition energy in the order of a few GHz, and requires arbitrary and precise microwave generation and detection for control and measurement.

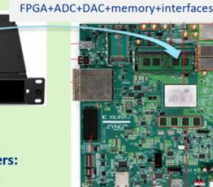
As the number of qubits increases, the number of microwave channels required increases linearly. Therefore, designing a qubit control system that is scalable, compact and cost-effective, while maintaining its precision, speed, and features, is imperative.

A basic qubit control system consists of DACs to generate the resonant probe pulses and ADCs to digitize the analog signals that travel out from the fridge.

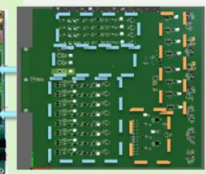
**Fermilab control and readout replaces expensive commercial equipment and messy cabling and discrete RF components.**

Gustavo Cancelo's group

Gen3 (RFSoc) : FNAL Readout and Control: Up to ~80 qubits/module (if FMUXed)  
>1000 qubits/system

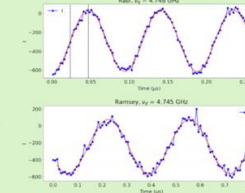


RF inputs, outputs, LO, fast flux control, high precision bias,



FNAL Gen3 electronics stakeholders:

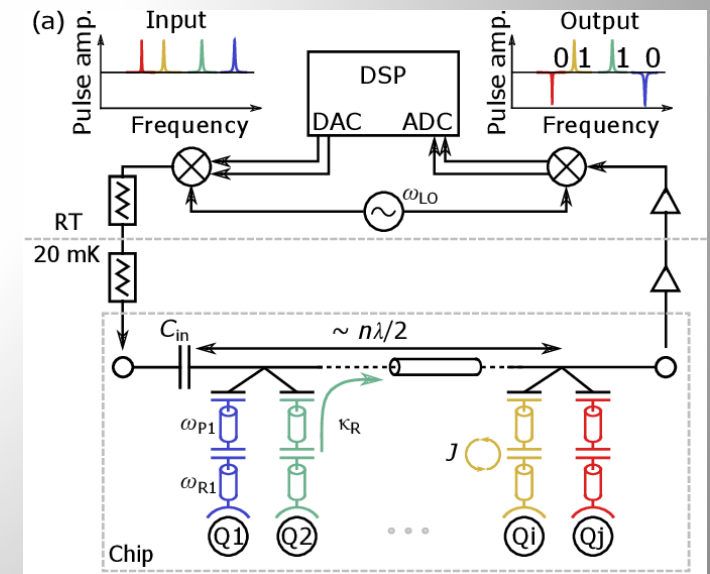
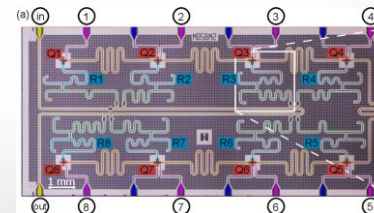
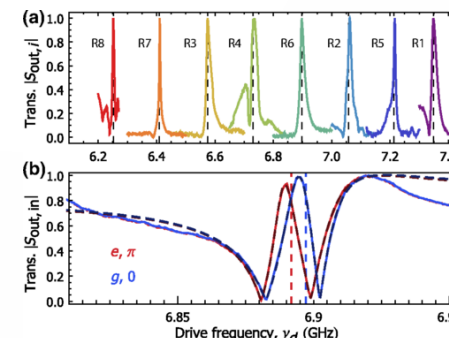
- U. Chicago: Davis Schuster lab.
- U Princeton: Andrew Hock lab.
- Fermilab: SQMS (A. Grassellino)
- Fermilab: QSC Thrust 3 (A. Chou)
- UCSB: Ben Mazin Lab.
- U. Purdue: Alex Ruichao Ma.
- IIT-FNAL: Rakshya Khatriwada.
- Fermilab CMB MKIDs (B. Benson).
- Fermilab DM MKIDs: Noah Kurimsky.
- Fermilab DE MKIDs: Juan Estrada.



Qubit measurements at U. Chicago. D. Schuster lab

Heinsoo et al. 2018, *Rapid High-fidelity Readout of Superconducting Qubits*  
<https://doi.org/10.1103/PhysRevApplied.10.034001>

Demonstration of high-fidelity, fast readout of multiplexed qubits on a single 1.2GHz



### 3. Highly-multiplexed transmission of analog information on a microwave carrier

Detector in an inaccessible location is constructed as a set of resonators on a periodically-loaded transmission line.

Each resonator is tuned to a discrete frequency. Resonators are continuously excited by a comb probe signal and the output spectrum is recorded.

Radiation event or flux causes a shift in the amplitude, frequency, and/or phase of the resonance.

→ *Simultaneous sensing of amplitude, position, and timing.*

→ *Single coax cable reads out  $O(1000)$  detector elements.*

Used with TES or MKID detector arrays, but can be extended to enable frequency-multiplexing of room-temperature streaming readout.


#### SLAC Space RFSoc board funded by NASA

(~\$2M)

SLAC

- With exception of RFSoc, all components flight qualified
  - Previous tests of RFSoc and similar technology Xilinx parts suggest good radiation hardness
- Firmware ported from ground-based evaluation board. Most building blocks already exist or are in development
- Power supplies tested, performing functionality and performance tests
- Signal Processing Bandwidth: ~120 MHz for Radio signals, optional 1.5GHz for Cherenkov transients
- Receive (ADC's), Transmit (DAC's) available for calibration

- RFSoc
  - 8X 4Gs/s 12-bit ADC, 8X 6.4 Gs/s 14-bit DAC
  - 4x ARM cores, 930K FPGA logic cells.

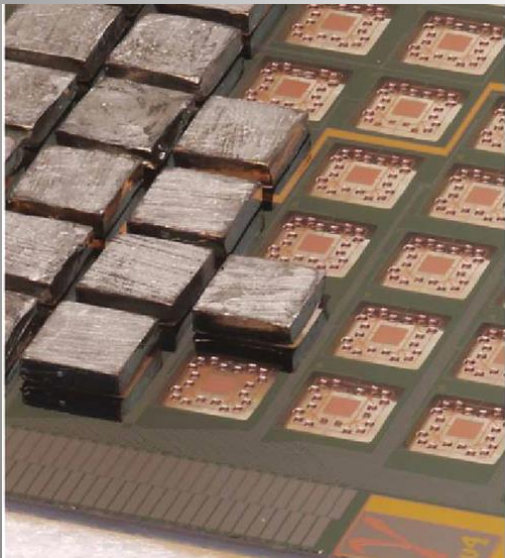
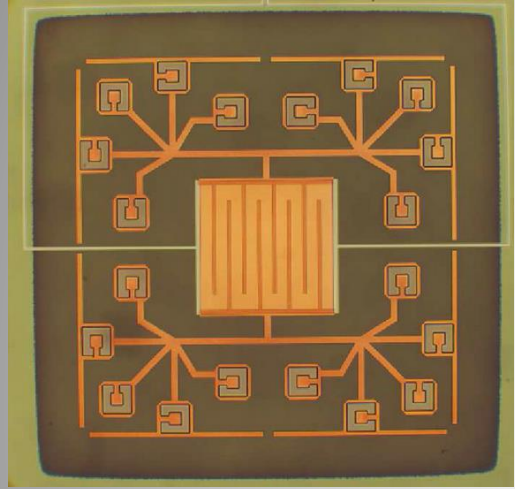


Labels in image:  
RFSOC  
Power supplies

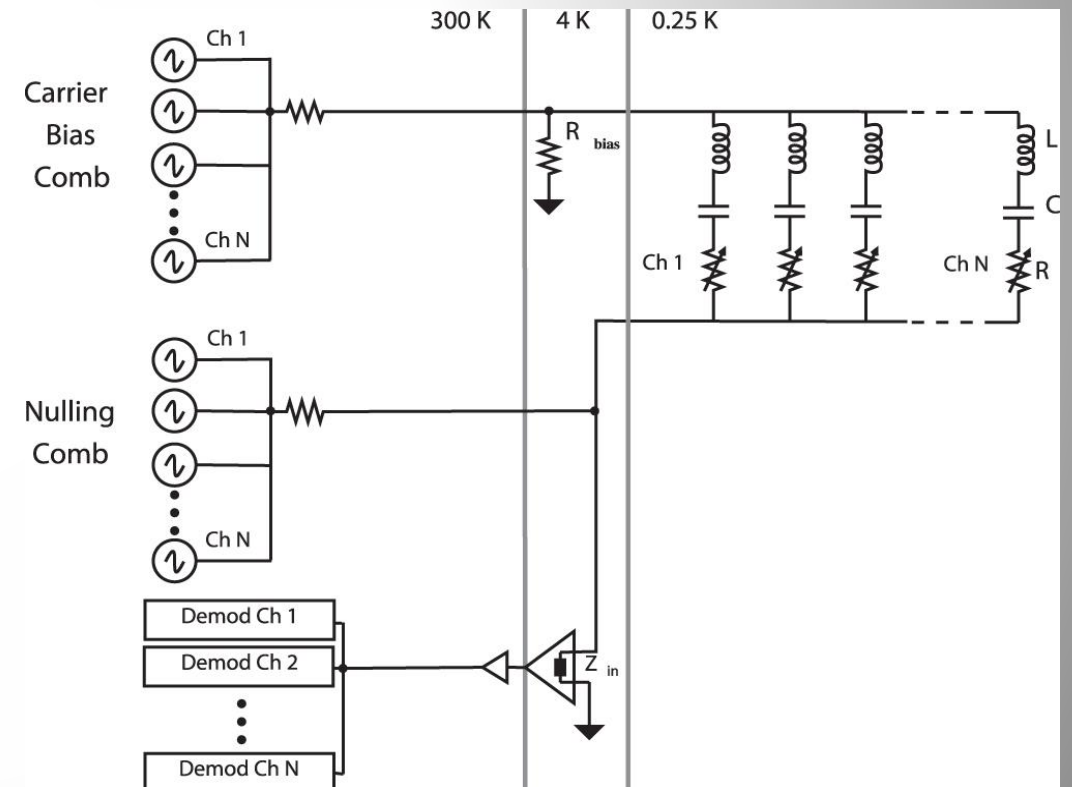
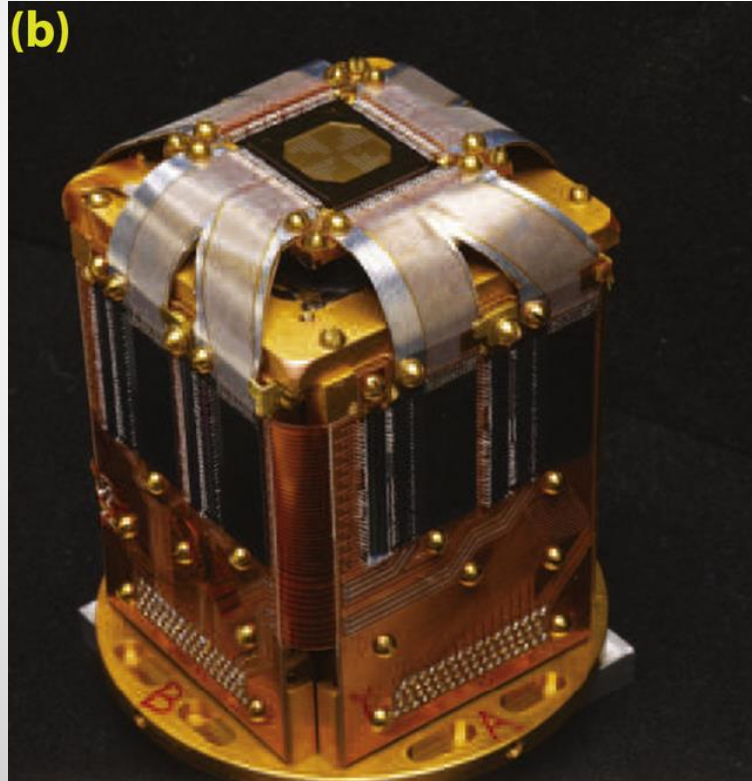
#### Fully integrated system

- Digital and ADC / DAC clocks,
- 4GB DDR3 memory
- 4GB boot and flash memory
- All power supplies, takes 5V in.
- Spacewire interface
- Supervisory system to restart in the event of upsets in the RFSoc
- Software in ARM cores for system loading and data streaming
- Software in C
  - Minimal libraries
  - No run-time memory allocation

### 3. Highly-multiplexed transmission of analog information on a microwave carrier



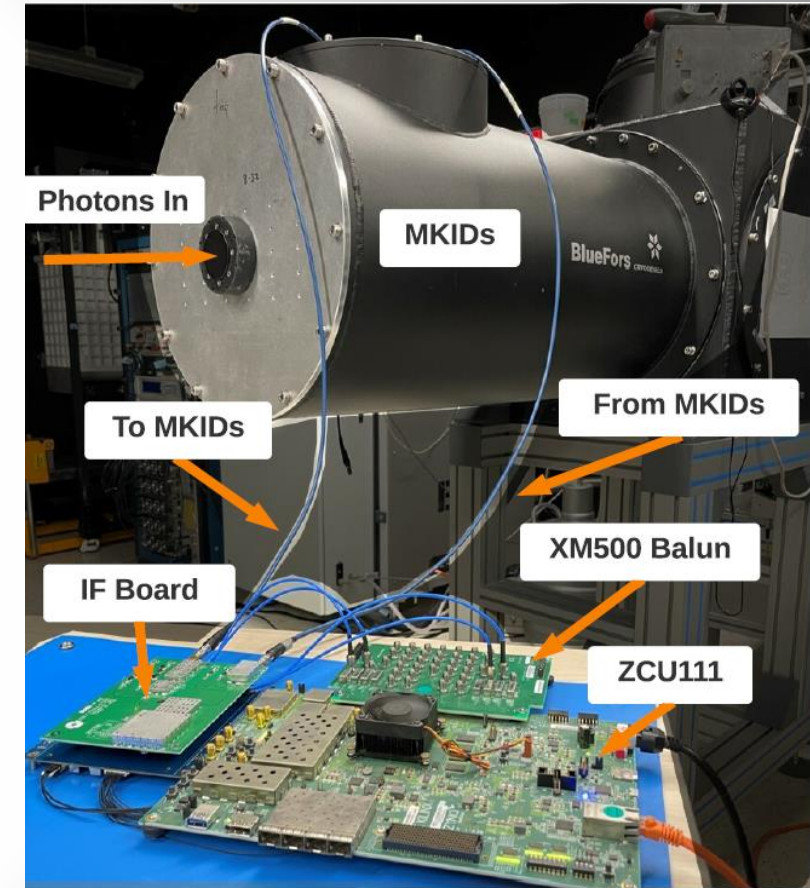
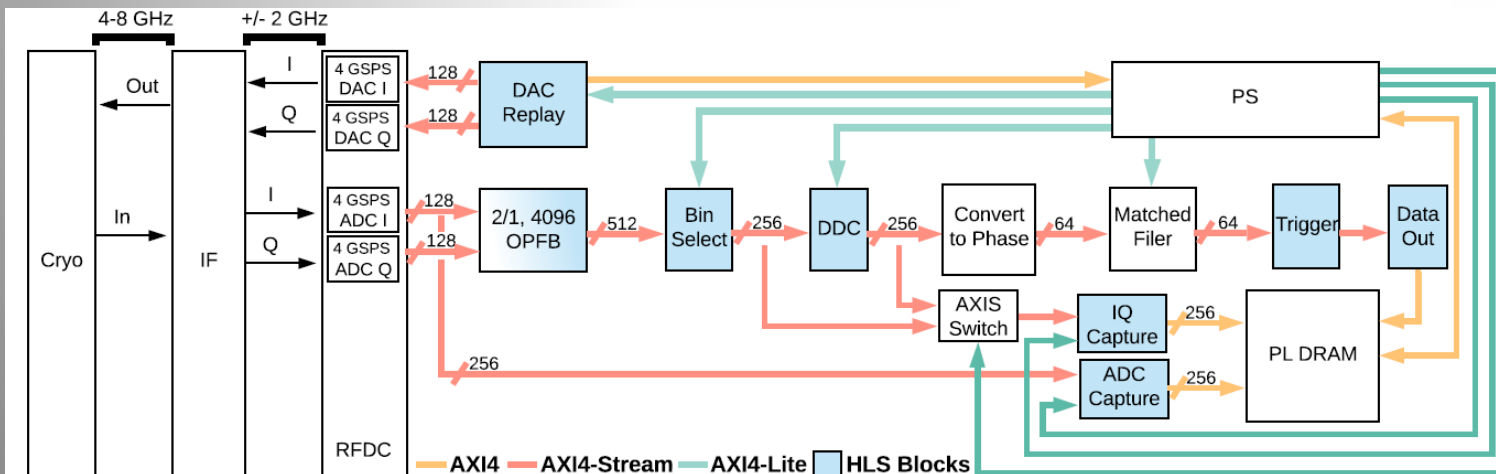
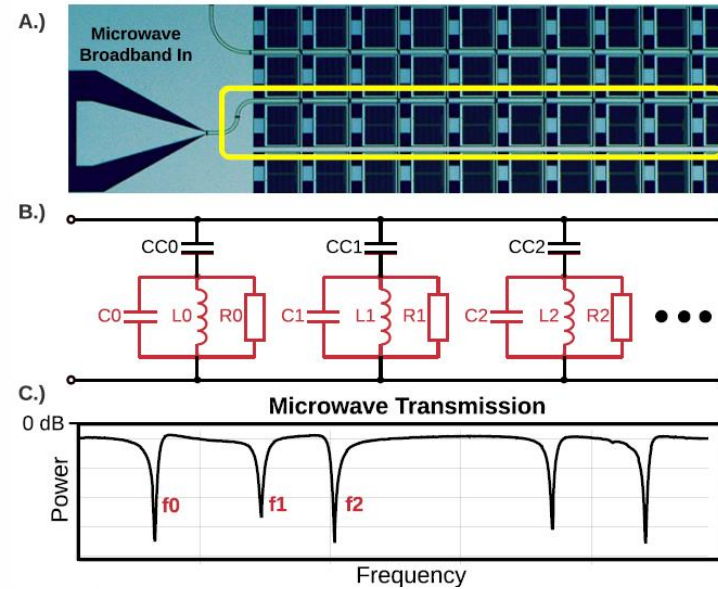
Example: TES array with frequency-division multiplexing for high-resolution x- and gamma-ray spectrometry (NIST).



Joel N Ullom and Douglas A Bennett 2015 *Supercond. Sci. Technol.* 28 084003

# 3. Highly-multiplexed transmission of analog information on a microwave carrier

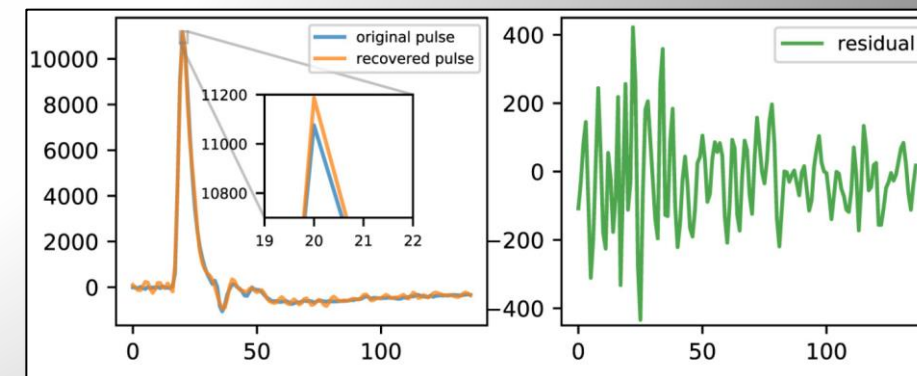
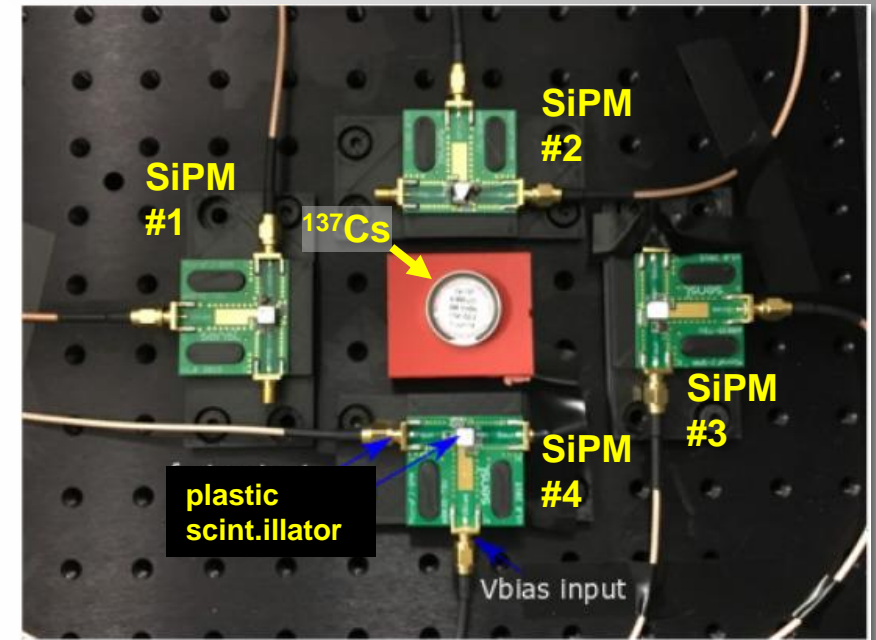
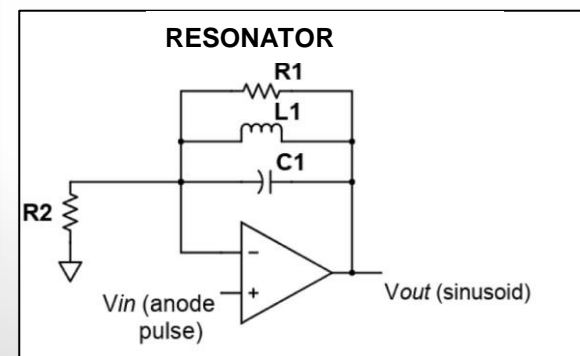
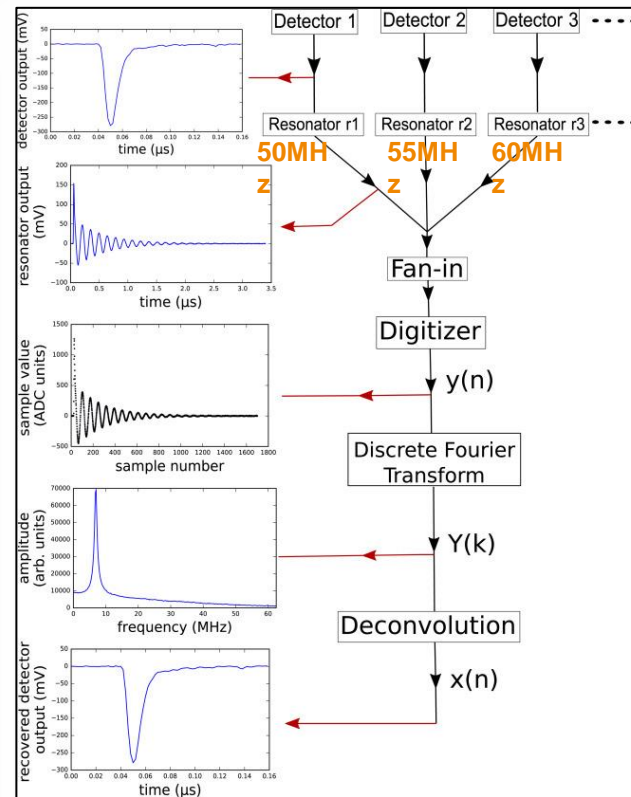
Example: MKID array with RFSoc frequency-division multiplexing for optical/NIR astronomy (2048 pixels on single feedline)



Smith et al., "Highly-Multiplexed Superconducting Detector Readout: Approachable High-Speed FPGA Design,"  
[https://ui.adsabs.harvard.edu/link\\_gateway/2022arXiv220316520S/arxiv:2203.16520](https://ui.adsabs.harvard.edu/link_gateway/2022arXiv220316520S/arxiv:2203.16520)

# 3. Highly-multiplexed transmission of analog information on a microwave carrier

Convolution-based frequency domain multiplexing (FDM) of pulse mode radiation detectors uses a resonator circuit to convert the detector pulse into a damped sinusoid with unique frequency which identifies the detector number. The frequency-encoded signal from each detector is combined into a single channel by a fan-in circuit, which is then read through a digitizer input channel. The original detector pulse is finally recovered from the digitized frequency-encoded signal by deconvolution. This method is dual of the modulation/demodulation technique that is used to read out, e.g., transition edge sensor bolometer arrays for satellite applications



## 4. Machine control and diagnostics

## RFSoc in HL- Beam Position Monitoring System

### Zynq RFSoc as prototype BPM processor [In Study]

- Evaluation board ZCU111 acquired as basis for prototype BPM processor
- RFSoc model XCZU28DR (Xilinx Zynq UltraScale+ architecture, gen. 1)

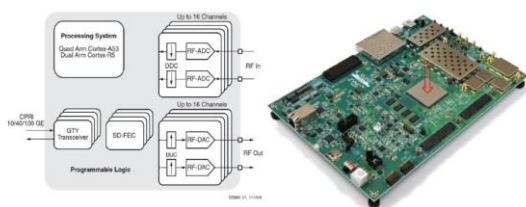


Figure 1: Zynq UltraScale+ RFSoC

Source: Zynq UltraScale+ BESoC Data Sheet: Overview (DS889)

Processing System Features	
Application Processing Unit	Quad-core ARM Cortex A33 up to 1.33 GHz
Real-Time Processing Unit	Dual-core ARM Cortex R5 up to 533 MHz

RF Features	
Max RF input Frequency (GHz)	4
Decimation / Interpolation	1x, 2x, 4x, 8x
12-bit RF DAC	8 of ADCs
	Max Rate (GSPS)
	4, 096
12-bit RF DAC	8 of DACs
	Max Rate (GSPS)
	6,554

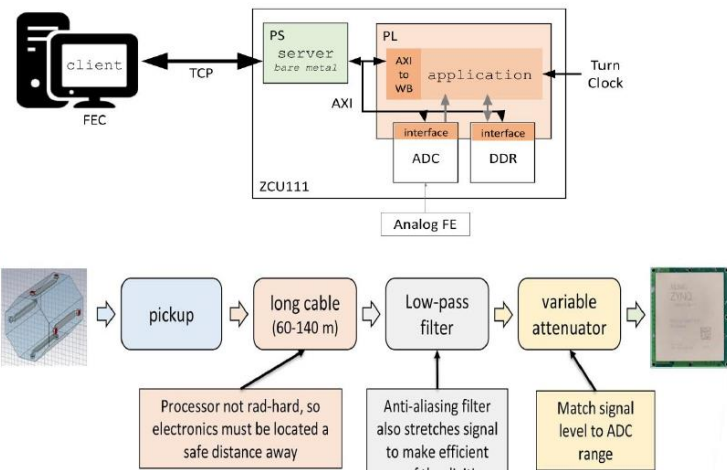
  

Programmable Logic	
System Logic Cells (k)	190
DSP Slices	4,372
Memory (Mb)	60.5
13 GHz Transceivers	16
Maximum I/O Pins	347

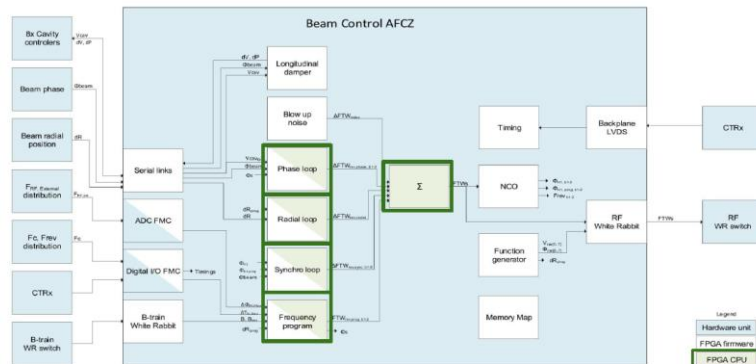
Courtesy: A. Boccardi, D. Bett, I. Degl'Innocenti

System On Chip Workshop 2021

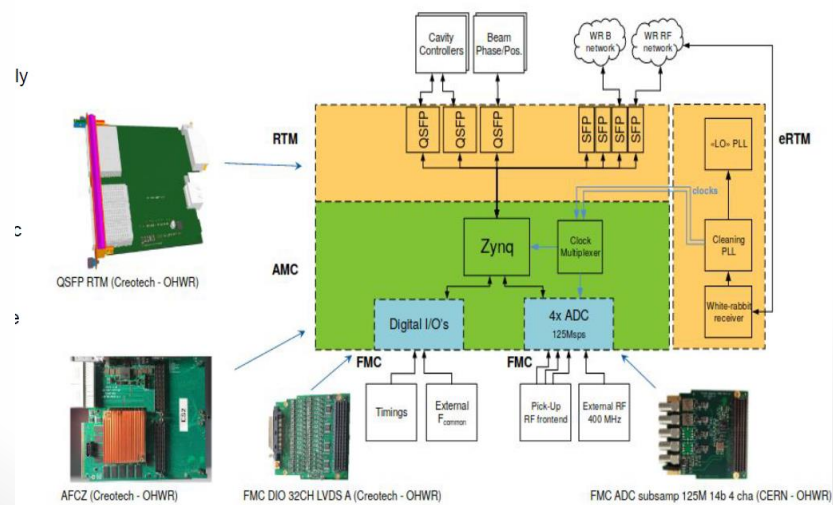
M. Shukla



## Beam Control AFCZ Card for SPS LLRF Upgrade



Upgrade of the SPS LLRF beam-based loops on the MicroTCA platform, A. Spierer, [LLRF-2019](#)



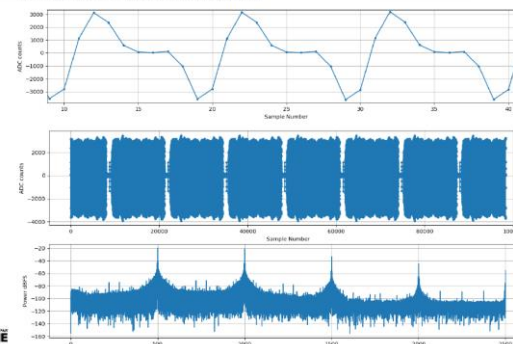
## National Synchrotron Light Source II

# rfSOC Development Status for BxB BPM System

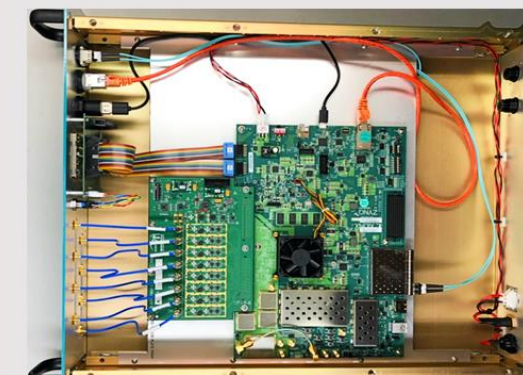
June 8, 2022

### RfSOC BxB Sampled Signal

- RFSOC sampled data (5 GSPS)
  - Output from Hybrid box with external 560MHz LPF
  - 10 Samples per RF bucket (200ps / sample)



### Advanced Light Source High Speed Digitizer



# Interested?

## 2nd System-on-Chip Workshop - CERN

Jun 7, 2021, 1:00 PM → Jun 11, 2021, 6:30 PM Europe/Zurich  
Zoom

Diana Scannicchio (University of California Irvine (US)), Frans Meijers (CERN)  
Revital Kopeliansky (Indiana University (US))

## SoC Interest Group Meeting

May 3, 2022, 3:00 PM → 6:00 PM Europe/Zurich  
Anderson (CERN)

## 16th xTCA Interest Group Meeting (online only)

Tuesday May 11, 2021, 3:00 PM → 6:15 PM Europe/Zurich  
online (Zoom)

### 1 Open-hardware MTCA modules for quantum physics

We will present a set of new open hardware modules in MTCA format developed in a large collaboration of Warsaw University of Technology, US ARL, University of Maryland, University of Oxford, QUARTIQ, M-Labs, and AMC FMC Carrier boards with several validated extensions. They are compatible with open-source ARTI Rabbit sub-nanosecond time synchronization. Alternatively, an extension card for N.A.T. MicroTCA carrier hardware using the ARTIQ distributed real-time IO (DRTIO) protocol.

Speaker: Grzegorz Kasprowicz (Warsaw University of Technology (PL))

## #rfsoc



Get Notifications for @ Mentions

Start a Call

About

Members 71

Topic

Add a topic

Description

RFSoc development

Created by

dave on April 16, 2019

Files



ADC-DAC Power.pdf

Shared by Leandro Stefanazzi on Jun 28th



20220608\_163351.jpg

Shared by Khanh Pham on Jun 9th

PYNQ

## Open Source RFSoc Overlays for the ZCU111 & RFSoc2x2 Platforms

Announcements  
davidnorthcote



1 May '21

## 2nd Workshop on System-on-Chip: Across LHC experiments, Accelerator&Technology Sector, and Radioprotection

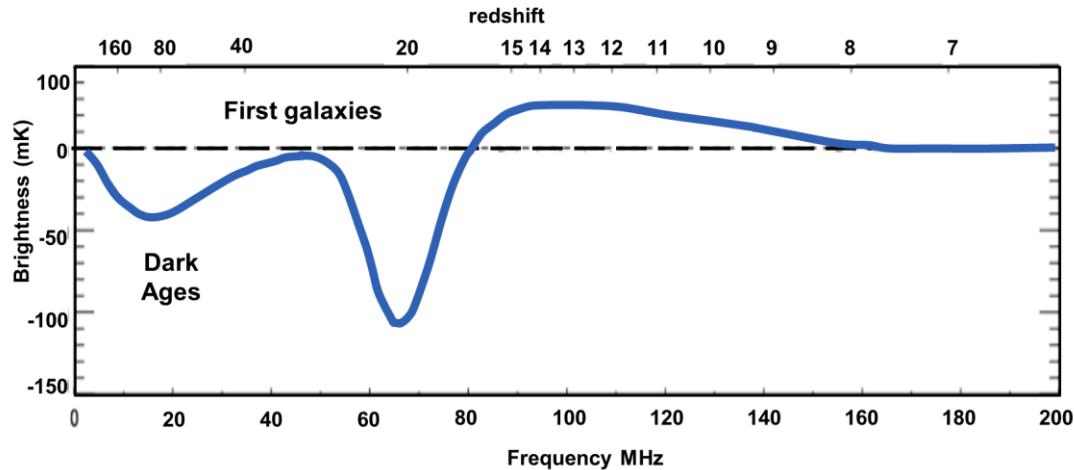
- 125 registrations
- Meeting fully virtual: zoom + mattermost
- Usually 40..70 participants connected to any session, with per
- 6 vendor presentations:  
Avnet/Silica: Xilinx and MicroChip, Intel/Altera, Enclust Scientific ... *special thanks!*
- 3 overview talks: ATLAS, CMS, Accelerator&Technology
- 14 presentations of different uses of SoC:  
CMS, ATLAS, and Radiation Protection
- 3 tutorials/demos: CI/CD for firmware, reliable boot
- 4 presentations on system-oriented aspects + discussion

# Thanks!

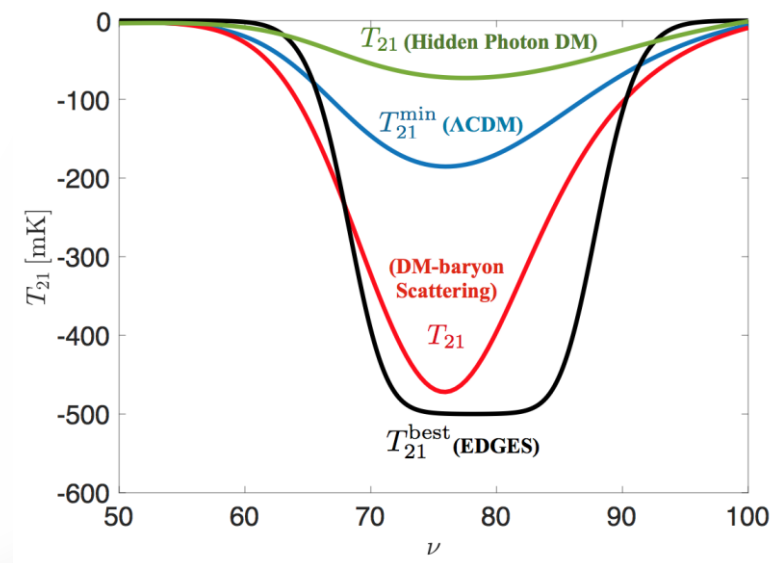


# Global spectrum at high redshift

- The global (i.e., sky-averaged) spectrum of the redshifted 21 cm line has the potential to be a direct probe of the epoch before the first stars and galaxies formed, when the absence of astrophysical processes simplifies the physics.
- Standard cosmological model provides robust prediction of the evolution of the spin temperature (emission/absorption of 21cm from neutral hydrogen) during this epoch.
- In 2018, anomalous 21cm absorption profile at  $z \sim 17$  was observed at  $3.8\sigma$  disagreement with maximum allowed by  $\Lambda$ CDM, hinting at new physics.
- Further robust 21 cm detection can constrain the proposed theoretical explanations.



Burns 2019



Kovetz 2018

Note: for both these 21cm techniques, one needs to contend with strong foreground emission from our galaxy. A large part of the program is centered on foreground characterization to allow accurate subtraction. At some wavelengths there is also significant transient RFI from anthropogenic sources. [Snowmass 2022 RF Microelectronics](#)