

PIP-II - BCRP218 Baseline 2020724 with July 2020 status			WBS PDF Print - Status Sort - PIP2IT										8/13/20 13:35																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																												
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Activity ID	Activity Name	Duration	BL Project Start	Start	BL Project Finish	Finish	BL1 Material Cost	BL1 Labor Units	Total Float	Re: 019	2020				2021				2022				2023				2024				2025				
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CNTRL75130	T6 MS - RQN Approved for Linac Clock (LCLK) Prototype Component	0			9/7/22	9/21/22	\$0.00	0h	164										0%																
CNTRL75140	Support Procurement for Linac Clock (LCLK) Prototype Components (43	9/8/22	9/22/22	11/7/22	11/21/22	\$0.00	40h	164	E									0%																
CNTRL75150	T6 MS - POAwarded for Linac Clock (LCLK) Prototype Components	0			11/7/22	11/21/22	\$0.00	0h	164										0%																
CNTRL75210	Interaction with Vendor for Linac Clock (LCLK) Prototype Components	51	11/8/22	11/22/22	1/26/23	2/9/23	\$0.00	40h	164	E									0%																
CNTRL75160	SVT Vendor Timing to Delivery of Linac Clock (LCLK) Prototype Comp	45	11/8/22	11/22/22	1/18/23	2/1/23	\$0.00	0h	164										0%																
CNTRL75170	T6 MS - Delivery of Linac Clock (LCLK) Prototype Components	0			1/18/23	2/1/23	\$0.00	0h	164										0%																
CNTRL75180	QC Inspect of Linac Clock (LCLK) Prototype Components (L)	5	1/19/23	2/2/23	1/25/23	2/8/23	\$0.00	8h	164	E									0%																
CNTRL75190	ACCEPT Delivery of Linac Clock (LCLK) Prototype Components (M&	1	1/26/23	2/9/23	1/26/23	2/9/23	\$60,000.00	0h	164	M									0%																
CNTRL75200	T6 MS - Accept Final Deliver of Linac Clock (LCLK) Prototype Compor	0			1/26/23	2/9/23	\$0.00	0h	164										0%																
121W-202009BD.03.07.03.01.02 AccS - Cntrl - PIP-II - Timing System - Accelerator Clc		1181	12/3/19	4/15/20 A	1/9/25	12/27/24	\$560,000.00	7120h	352																										
Project Sub-Phase: AssmTst Assemble and Test		493	1/25/23	1/13/23	1/9/25	12/27/24	\$0.00	3800h	352																										
No Components		493	1/25/23	1/13/23	1/9/25	12/27/24	\$0.00	3800h	352																										
CNTRL750570	Build Accelerator Clock (ACLK) Prototypes (L)	64	1/25/23	1/13/23	4/24/23	4/13/23	\$0.00	800h	183	E																									
CNTRL750580	Test Accelerator Clock (ACLK) Prototypes (L)	64	4/25/23	4/14/23	7/25/23	7/14/23	\$0.00	600h	183	E																									
CNTRL750720	Assemble and Test Accelerator Clock (ACLK) Clock Source (L)	128	7/5/24	6/25/24	1/9/25	12/27/24	\$0.00	800h	352	E																									
CNTRL750730	Assemble and Test Accelerator Clock (ACLK) Transmission Side (L)	128	7/5/24	6/25/24	1/9/25	12/27/24	\$0.00	800h	352	E																									
CNTRL750740	Assemble and Test Accelerator Clock (ACLK) Decoders (L)	128	7/5/24	6/25/24	1/9/25	12/27/24	\$0.00	800h	352	E																									
Project Sub-Phase: Design Design		891	12/3/19	4/15/20 A	11/8/23	10/30/23	\$0.00	3064h	352																										
No Components		891	12/3/19	4/15/20 A	11/8/23	10/30/23	\$0.00	3064h	352																										
CNTRL757040	Develop Functional Requirement and Specifications for Accelerator Clc	65	12/3/19	4/15/20 A	3/9/20	8/7/20	\$0.00	100h	183	E	75%																								
CNTRL750450	Develop Prelim Design for Accelerator Clock (ACLK) System (L)	80	3/9/20	8/10/20	7/8/20	11/16/20	\$0.00	260h	183	E																									
CNTRL750460	T6 MS - Preliminary Design Review (PDR) for Accelerator Clock (ACL	0			11/6/20	11/16/20	\$0.00	0h	183																										
CNTRL750470	Respond to recommendations from PDR for Accelerator Clock (ACLK)	30	11/9/20	11/17/20	12/22/20	1/5/21	\$0.00	120h	183	E																									
CNTRL750590	Develop Clock Source Prototype for Accelerator Clock (ACLK) System	150	12/23/20	1/6/21	7/29/21	8/6/21	\$0.00	670h	183	E																									
CNTRL750595	SVT: First 75 days of Develop Clock Source Prototype for Accelerator	75	12/23/20	1/6/21	4/13/21	4/21/21	\$0.00	0h	183																										
CNTRL750600	Develop Clock Fanout Prototype for Accelerator Clock (ACLK) System	150	4/14/21	4/22/21	11/12/21	11/22/21	\$0.00	672h	183	E																									
CNTRL750610	Develop Clock Decoders Prototype for Accelerator Clock (ACLK) Syste	150	11/15/21	11/23/21	6/22/22	6/30/22	\$0.00	672h	183	E																									
CNTRL750480	Develop Final Design after Testing Prototypes for Accelerator Clock (A	45	7/26/23	7/17/23	9/27/23	9/18/23	\$0.00	450h	352	E																									
CNTRL750490	T6 MS - Start FDR-PRR for Accelerator Clock (ACLK) System	0			9/27/23	9/18/23	\$0.00	0h	352																										
CNTRL750500	Respond to recommendations from FDR-PRR for Accelerator Clock (/	30	9/28/23	9/19/23	11/8/23	10/30/23	\$0.00	120h	352	E																									
CNTRL756900	T6 MS - Close FDR-PRR for Accelerator Clock (ACLK) System	0			11/8/23	10/30/23	\$0.00	0h	352																										
Project Sub-Phase: Procmnt Procurement		497	6/23/22	7/1/22	7/3/24	6/24/24	\$560,000.00	256h	352																										
Components: ACLKPdn ACLK Production		237	7/26/23	7/17/23	7/3/24	6/24/24	\$500,000.00	128h	352																										
CNTRL76120	Prepare Acquisition Plan for Accelerator Clock (ACLK) System Product	32	7/26/23	7/17/23	9/8/23	8/29/23	\$0.00	0h	363																										
CNTRL76110	T6 MS - Acquisiton Plan Started for Accelerator Clock (ACLK) System	0	7/26/23	7/17/23			\$0.00	0h	363																										
CNTRL76130	Review and Approve Acquisition Plan by PIP-II Procurement Manager	32	9/11/23	8/30/23	10/24/23	10/13/23	\$0.00	0h	363																										
CNTRL76140	T6 MS - Acquisition Plan Approved for Accelerator Clock (ACLK) Syste	0			10/24/23	10/13/23	\$0.00	0h	363																										
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■ Remaining Level of Effort
 ■ Actual Work
 ■ Critical Remaining Work
 ◆ Milestone
 ■ Project Baseline
 ■ Remaining Work
 ◆ Baseline Milestone

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