



Contribution ID: 101

Type: not specified

**3-bit amplitude resolution, 40MHz-rate compatible
and zero-suppressed readout integrated circuit for
pixel detectors – FCP130 developed for the HL-LHC
CMS Upgrade**

Friday, 14 September 2018 11:15 (30 minutes)

Presenter: FAHIM, Farah (FERMILAB)

Session Classification: Imaging - 4