# **Challenges in x-ray science**

### **Workshop on Innovative Devices and Systems**

Argonne National Laboratory, September 19th 2014



# **Challenges**



# **Specific setups for specific experiments**



# **Setups**



## **Typical solution scattering pump probe experiment**  Courtesy of Henrik Lemke<br>
SLAC



- (a) Diffuse scattering pattern of an aqueous solution (10 images average).
- (b) Azimuthally-averaged large dynamic range signal and measured difference signals (170 pulses average).

### *Challenge:*

The large dynamic range signal is changed by *<<1%* after excitation with an ultrashort laser pulse.

Inverse transformation of the signal requires *correct scaling of the small light induced changes throughout the entire intensity range of the scattering*  maabod onarged an oagnoat the onthe intensity range of the ceattering  $\frac{1}{5}$ 

## **Detector needs in x-ray science…**

Imaging/scattering:

- Good spatial resolution
- Dynamic range
- Fast and "smart"

Spectroscopy:

- Very high energy-resolution
- High speed

# **…other common needs**

- Efficiency and sensitivity
	- Good entrance window for soft x-rays
	- High stopping power material for hard x-rays
- Fast
	- Detector speed (temporal resolution)
	- Readout speed (frames/second)
- Large solid angle (large area) - Scalability
- Radiation tolerant
- Easy to use

SL AC

## **Source dependent constraints**





Photons arrive at once Information per pulse

### Storage Rings



Continuous source Information by accumulation



Yibin Bai et al, *Teledyne Imaging Sensors: Silicon CMOS imaging technologies for x-ray, UV, visible and near infrared*, SPIE Vol. 7021 (2008)

# **Photon absorption depth in Silicon**





*Beer-Lambert law:* 

**63% of the photons are absorbed -> hard x-ray cameras need tick sensors 37% of the photons are not yet absorbed**  *I(z) = e (-z/d)* 

**at the absorption depth** 

 **-> soft x-ray cameras need thin entrance windows**  10

## **Silicon limitation – other sensor materials SLAC**

 $1.0$  $0.8$ Quantum Efficiency<br>Particlency<br>Particlency **Detector Thickness**  $300 \mu m$ 500  $\mu$ m 1000  $\mu$ m 1500  $\mu$ m  $0.2$ 2000  $\mu$ m  $0.0$  $0.2$  $10$ 50  $\mathbf{1}$ Energy [keV]

#### Thicker silicon sensors Other semiconductors for direct detection



From: J. Treis, MPI, Pixel 2008 http://hasylab.desy.de/instrumentation/detectors/e74464/e106206/index\_eng.html

- *Small improvement*
- *High depletion voltage*
- *Parallax*
- *Germanium ('faster than Si')*
- *GaAs ('faster than Si')*
- *CdTe, CdZnTe*

# **Pixel detectors: two approaches**

## *Monolithic*

**CCD** 

## CMOS imagers

- CMOS Monolithic Active Pixel Sensors (MAPS).
- CMOS Silicon On Insulator (SOI).

## *Hybrid pixel detectors*

- Sensors in high resistivity silicon (i.e., Pixel Array Detectors (PAD), DEpleted P-channel Field Effect Transistor (DEPFET), Silicon Drift Detectors, X-ray Active Matrix Pixel Sensors (XAMPS)).
- Readout chip in low resistivity silicon standard IC technology.

*…and combination of the above* 

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## **CCDs changed the way we see the world**



# **CMOS imagers (under development)**

### **SOPHIAS** (Riken, Japan)

Fully-Depleted SOI, 5.5-7keV, 30 um x 30 um, 1.92 Mpixel,100 e- (~1 keV), 7 Me- maximum signal, 60 Hz (multiple gain)

T. Hatsui, International Image Sensor Workshop (IISW), Snowbird, Utah, USA, June 12-16, 2013.

### **PERCIVAL** (RAL, DESY and Elettra)

back-thinned to access its primary energy range of 0.25-1 keV, 25 um x 25 um, 16 Mpixel, single-photon, 1 to  $\sim$  10^5 (500 eV), 120 Hz (gain switching)

http://photon-science.desy.de/research/technical\_groups/detectors/projects/percival/ index\_eng.html

# **Pixel Array Detectors**

It consists of a segmented sensor bonded to a dedicated integrated circuit



Image from Dectris, LTD

History:

- First developed for High-Energy Physics experiments
	- ATLAS pixel array detector
- Then for applications at synchrotrons:
	- In Europe Detector group at Paul Scherrer Institute
	- In USA Sol Gruner's group at Cornell University

Today many groups are working on that

# **Application Specific Integrated Circuits**

### **Typical pixel architectures**  S&H  $FE$   $\rightarrow$  comp  $\rightarrow$  counter  $\rightarrow$  digital readout  $FE$   $\rightarrow$  analog  $\rightarrow$  analog readout memory  $FE$  S&H  $\rightarrow$  ADC  $\frac{digital}{mamp}$  digital readout digital memory **counting chip analog integrating – analog readout analog integrating – digital readout**

# **Good spatial resolution + low noise = better spatial resolution**

**At single photon rates interpolation may give 1 um position resolution Spectral information can be available by summing cluster charges** 

So far scientific CCDs (e.g. pnCCD-MPI/HLL, fCCD -LBNL)

Now low noise integrating pixel array detectors are being developed: ePix 100 (SLAC) and Moench (PSI)

#### **ePix 100**

50 um x 50 um pixel, low noise  $(< 50 e<sup>-</sup>)$  up to 1000 frames / s Fixed gain - 100 8 keV photon *Synchrotron Radiation News, 27 (4), 14, 2014* 

#### **MOENCH**

25 um x 25 um pixel, low noise  $($  < 40  $e$ <sup>-</sup> $)$ Different implementation with fixed gain and gain switching - from 15 12 keV photons up to 600 12 keV photons *Synchrotron Radiation News, 27 (4), 3, 2014* 

# **Good spatial resolution + low noise = better spatial resolution**

**At single photon rates interpolation may give 1 um position resolution Spectral information can be available by summing cluster charges** 



Figure 4: Image of a kidney stone acquired using GOTTHARD in scanning geometry using a 2 um slit to define the vertical pixel size: (a) analog image acquired with 25 um pitch strips; (b) digital image with  $0.5$  um virtual pixel obtained after interpolation. Details can be found in [14].

*Synchrotron Radiation News, 27 (4), 3, 2014* 

# **ePix 100p**

![](_page_18_Picture_1.jpeg)

![](_page_18_Picture_2.jpeg)

![](_page_18_Figure_3.jpeg)

![](_page_18_Picture_4.jpeg)

- $\cdot$  T= ca -8 C
- $\cdot$  Tint = 260 us
- Measured at LCLS
- Cu target and Fe foil (Ka: 8.0 keV and 6.4 keV)
- gain corrected
- 0.85 keV FWHM -> **99** e- ENC

# **ePix 100p**

![](_page_19_Picture_1.jpeg)

![](_page_19_Picture_2.jpeg)

![](_page_19_Figure_3.jpeg)

**SSRL - Ag L line and primary 7.5 keV**

![](_page_19_Picture_5.jpeg)

- $\cdot$  T= ca -8 C
- $\cdot$  Tint = 260 us
- Measured at LCLS
- Cu target and Fe foil (Ka: 8.0 keV and 6.4 keV)
- gain corrected
- 0.85 keV FWHM -> **99** e- ENC

# **Dynamic range: the gain game?**

![](_page_20_Picture_1.jpeg)

MMPAD: range extension

AGIPD, JUNGFRAU, ePix 10k: gain switching

LPD: multiple gains

DSSC: gain compression

## **Mixed-Mode Pixel Array Detector (MMPAD) SLAC**

### Cornell University and Area Detector System Corporation

![](_page_21_Figure_2.jpeg)

150 um x 150 um, up to 200 photons arriving simultaneously, up to 4.7 10  $^7$  8 keV full well, 0. 86 ms read time,  $>$  400 e $\cdot$  noise, up to 1100 Hz (computer memory) and 200 Hz for continuous storage to disk.

# **Gain compression: DSSC**

### Matteo Porro IWORDID14

**SLAC** 

![](_page_22_Picture_2.jpeg)

## **DSSC - Design Parameters**

![](_page_22_Picture_66.jpeg)

![](_page_22_Picture_5.jpeg)

DEPFET (G. Lutz, P. Lechner, PNSensor)

- $\geq$  2x poly, 2x Al, 1x Cu
- $\geq 11$  implantations

![](_page_22_Picture_9.jpeg)

Mini-SDD (R. Richter, MPG HLL)  $\triangleright$  1x Al, 1x Cu  $\geq$  2(3) implantations

# **Gain compression: DSSC**

### Matteo Porro IWORDID14

**SLAC** 

![](_page_23_Picture_2.jpeg)

## **DSSC - Design Parameters**

![](_page_23_Figure_4.jpeg)

![](_page_23_Picture_5.jpeg)

DEPFET (G. Lutz, P. Lechner, PNSensor)  $\geq 2x$  poly, 2x Al, 1x Cu

 $\triangleright$  11 implantations

![](_page_23_Picture_8.jpeg)

Mini-SDD (R. Richter, MPG HLL)  $> 1x$  Al, 1x Cu  $\geq$  2(3) implantations

# **ePix hybrid**

ePIX10k module: 135k-pixel ePIX100 module: 540k-pixel

![](_page_24_Figure_2.jpeg)

- front detector with fixed (large) hole to simplify cooling and metrology
- could use edge sensitive detectors in the center to minimize dead area
- potential use of a attenuator section in front of back detector
- front and back detector are a single camera, mounted on a single optical bench to simplify alignment and metrology

## **data rates and link speeds**

## ePIX one with 0.5Mpixel

- @120Hz 1.0Gbit/sec
- @360Hz 3.1Gbit/sec
- @500Hz 4.3Gbit/sec
- @1kHz 8.7Gbit/sec

## ePIX four with 2Mpixel

- @120Hz 4.2Gbit/sec
- @360Hz 12.5Gbit/sec
- @500Hz 17.4Gbit/sec
- @1kHz 34.8Gbit/sec

![](_page_25_Picture_11.jpeg)

![](_page_25_Picture_13.jpeg)

# **ROI, read during capture**

![](_page_26_Figure_1.jpeg)

- small region of interest (ROI) is readout at high frame rate
- In every frame of the ROI another small section of the full matrix is readout

- after a number of n frames we collected n ROI frames at high frame rate
- and one full frame at low frame rate
- supporting the acquisition of a slower full frame readout is useful for to observe in "real-time" if alignment or sample properties are drifting.

# **triggering and read during capture**

![](_page_27_Figure_1.jpeg)

- In a hybrid pixel detector the (fully parallel) pixel circuitry can operate faster than the ASIC backend (readout) interface
- We could run the front end at higher rates than the backend and readout only selected (triggered) frames

# **Camera PCB level electronics and DAQ integration**

![](_page_28_Picture_1.jpeg)

- To limit the complexity of the cameras PCB level electronics one could aggregate the detector module data and hand it over via next generation high speed serial links to an Advanced Telecommunication Computing Architecture (ATCA) crates and Reconfigurable Computing Elements (RCEs) which can be located up to 20 m away from the camera head.
- The RCE nodes could perform data processing on raw data for the purpose of calibration, online visualization, event selection, binning, averaging and triggering before the final data stream is send to the DAQ system.

## **Sources/Moore's law**

1950 1960 1970 1980 1990 2000 2010  $10<sup>1</sup>$  $10<sup>1</sup>$ Computer Speed, Mflop/s (millions of operations per second)  $10<sup>1</sup>$  $10<sup>1</sup>$  $10<sup>1</sup>$  $10<sup>7</sup>$  $10<sup>1</sup>$ DARPA(?) **Blue Gene**  $10<sup>6</sup>$  $10<sup>8</sup>$ **NEC Earth**  $10$ Intel ASCI-Red  $10<sup>6</sup>$ **Cray T90**  $10$ 12 orders Cray 2  $10<sup>′</sup>$ of magnitude Cray X-MP In 6 decades  $10<sup>3</sup>$ Cray 1  $10<sup>2</sup>$ **CDC 6600**  $10<sup>1</sup>$ **IBM 7090**  $10<sup>6</sup>$  $10<sup>°</sup>$ Eniac  $10<sup>1</sup>$ 1950 1960 1970 1980 1990 2000 2010 Year

Courtesy of Oleg Shpyrko

### Moore's law for CPU chips

## **Sources/Moore's law**

![](_page_30_Picture_1.jpeg)

### **Brilliance is Coherence**

![](_page_30_Figure_3.jpeg)

Courtesy of Oleg Shpyrko

## **Gap with Moore's law? More than Moore and 3D SLAC**

![](_page_31_Figure_1.jpeg)

**Fig. 5** – 3D integration of a "More-than-Moore" photodetector with "More Moore" read-out and digital signal processing ICs (courtesy of Piet de Moor, IMEC)

# **Industry is already implementing 3D (and 2.5D)**

![](_page_32_Picture_1.jpeg)

Research 3D chip stack (concept). It is alternate approaches like this, rather than brute-forcing smaller transistors, / future of computer.

![](_page_32_Figure_3.jpeg)

Figure 1: Xilinx continues to expand its leadership in all three areas.

![](_page_32_Picture_5.jpeg)

![](_page_32_Picture_6.jpeg)

![](_page_32_Picture_7.jpeg)

![](_page_32_Picture_8.jpeg)

# **Vertical Integrated Photon Imaging Chip**

![](_page_33_Picture_1.jpeg)

### **High-speed spectroscopic detectors: Maia (BNL - CSIRO)**  *P. Siddons, DOE – BES Neutron and X-Ray Detectors Workshop, 2012*

![](_page_34_Picture_1.jpeg)

### **Large solid angle (1.2sr) and real-time processor**

Photon event (Peak height, Time-over-Threshold, detector number)

FPGA performs high-speed computation (Dynamic Analysis for deconvolution) and generates elemental maps (photon-by-photon) in real time

Obvious future path with SDDs

### **Can hybrid pixel array detectors play a role?**

HEXID (Hyperspectral Energy-resolving X-ray Imaging Detector, BNL) - Other low noise detectors (e.g. ePix100, Moench)

![](_page_34_Picture_8.jpeg)

**SLAC** 

![](_page_34_Picture_9.jpeg)

Rehak *et al*., Nucl. Inst. Meth. A624 (2010) 260

#### Courtesy of Kent Irwin

## **TES x-ray spectrometer collaborators**

![](_page_35_Picture_2.jpeg)

#### **SLAC / Stanford**

Saptarshi Chaudhuri Hsiao-Mei Cho Kelly Gaffney Kent Irwin Chris Kenney Dale Li Michael Minitti Dennis Nordlund Tsu-Chien Weng Christopher Williams

#### **Lund Kemicentrum (Lund, Sweden)**

Jens Uhlig

#### **Illinois**

Peter Abbamonte Yizhi Fang

#### **NSLS**

Daniel Fischer Cherno Jaye

#### **NIST**

Doug Bennett Randy Doriese Dan Swetz Galen O'Neil Joel Ullom Joe Fowler Carl Reintsema Gene Hilton Dan Schmidt

#### **ANL**

Antonino Miceli Tom Cecil Clarence Chang Lisa Gades Tim Madden Gensheng Wang Daikang Yan Jessica McChesney Fanny Simones Hongping Yan

### **Sensitivity of TES based X-ray Spectrometers at SSRL (in development) Enabling Ultra-low Concentrations (ppm)**

![](_page_36_Figure_1.jpeg)

**New Science Opportunities in Material Science, Chemistry, and Biology** 

Courtesy of Kent Irwin

![](_page_36_Picture_3.jpeg)

![](_page_36_Figure_4.jpeg)

![](_page_36_Figure_5.jpeg)

![](_page_37_Figure_0.jpeg)

\*Can be significantly better, but with much lower efficiency and smaller solid angle

#### Courtesy of Kent Irwin

**SLAC** 

## **TES Spectrometer Arrays**

![](_page_38_Picture_2.jpeg)

We are now deploying a 240 pixel soft x-ray spectrometer array on SSRL BL-10-1

### Courtesy of Kent Irwin

**SLAC** 

### **Beamline hardware**

![](_page_39_Picture_2.jpeg)

![](_page_39_Picture_3.jpeg)

#### APS 29ID IEX beamline

- 400-2500 eV
- Installed August, 2014

#### NSLS U7A beamline

- 200-1400 eV
- Prototype installed Dec., 2011

### Studying charge order in superconductors using transition edge sensor (TES) detectors, P. Abbamonte (UIUC), D. Swetz (NIST), et al.

### Short-ranged, glassy charge order - how pervasive is it?

![](_page_40_Figure_2.jpeg)

There is circumstantial but widespread evidence that local charge order is pervasive in unconventional superconductors, such as copper-oxides and ironarsenides, and therefore may be integrally related to the mechanism of superconductivity. To understand this phenomenon, direct measurements of glassy charge order are needed.

#### **Background** problem in resonant soft x-ray scattering (RSXS)

![](_page_40_Figure_5.jpeg)

The most direct way to measure charge order is RSXS, which has previously shown that that valence band order exists in both 214 cuprates, such as La<sub>2</sub>  $_{x}Ba_{x}CuO_{4}$  and 123 materials such as YBa<sub>2</sub>Cu<sub>3</sub>O<sub>zs</sub>. Unfortunately, inelastic background problems prevent detection/study of short-ranged order.

![](_page_40_Figure_7.jpeg)

![](_page_40_Picture_8.jpeg)

Office of Science

![](_page_40_Picture_10.jpeg)

![](_page_40_Picture_11.jpeg)

![](_page_40_Picture_12.jpeg)

### Update, Sept. 2014

![](_page_41_Picture_1.jpeg)

#### Courtesy of Kent Irwin

### **TES spectrometer Moore's Law: ~2 years doubling SLAC**

1 pixel 1996

![](_page_42_Figure_3.jpeg)

![](_page_42_Figure_4.jpeg)

![](_page_42_Figure_5.jpeg)

24 pixel 2004

![](_page_42_Figure_7.jpeg)

![](_page_42_Picture_8.jpeg)

Solid Angle x Efficiency (sr.%) **0** 

Resolving Power

Resolving Power

**1**

**10**

**1000** 

45 pixel 2008

240 pixel 2014

Long-term program will double solid angle & count rate every  $\sim$  two years

# **Challenges**

Detector comprises

Sensors

Material / technology challenges

Design / technology challenges

Electronics (Front/back end)

Functionalities, power, IGR challenges

Detector produces

More and more data

Links and data reduction challenges

**Methods** 

Setups and analysis challenges

**Convergence** 

![](_page_44_Picture_0.jpeg)

# **Thanks !**

## **Other contributors**

SLAC detector group SLAC DAQ, controls and data group LCLS instrument scientists SSRL beamline scientists and support Etc. etc.