

Implementing a Time-to-Digital Converter in a FPGA

Wednesday, 5 August 2009 14:30 (20 minutes)

A Time-to Digital converter (TDC) implemented in a field-programmable gate array (FPGA) as high-resolution time measurement device is presented. This TDC FPGA is innovative in that it solved many problems prevalent in the previously developed firmware. Its new flexible firmware enables it to be utilized in many practical applications.

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