



Slow Control, Timing and Data Interfaces specified by: https://edms.cern.ch/ui/file/2088726/2/2088726_DID_DAGSC_PDS_V2_docx_cpdf.pdf

Cold-Warm Electronics interface specification: https://edms.cern.ch/ui/file/2342785/1/Cold_Warm_PDS_Interface_4_27_2020.pdf (connector definition to be updated)

DAPHNE-ColdElec interface connector: 5 D-SUB connectors, 30 pins (dual 15 pins), 8 channels/1 AFE each one (currently under analysis), 3 options to connect to the chassis (where the Hirose connectors are located)

Power supply connector: MOLEX 430450610 (under consideration), required a locked connector, redundancy, return, remote sensing features