

#### Liquid Argon TPC Trigger Development with MicroBooNE & SBND

Daisy Kalra on behalf of MicroBooNE & SBND collaborations

**Columbia University** 

CPAD-2021 March 18, 2021



Daisy Kalra, Columbia University









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- Motivation
- Introduction to MicroBooNE and SBND
- TPC Readout Electronics
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- Trigger Approaches
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**TDAQ Session, CPAD-2021** 

**DUNE: World's largest LArTPC neutrino experiment** (once constructed), will start taking data ~2026.

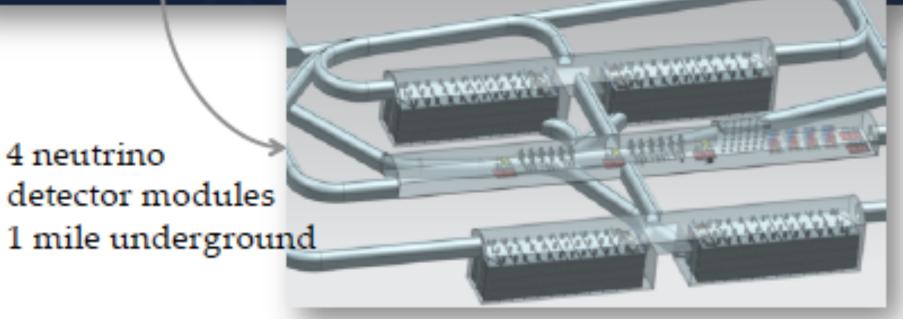
> Data rates in DUNE: up to >1 million readout channels 2 MHz x 12 bit ADC digitization >5 TB/s data rate!

One of the DUNE physics goals is to search • for non-beam events (rare events) such as neutrinos from Supernova burst, (up to ~few thousand interactions over first 10 seconds, *but ~once per century)* 

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#### Sanford Underground Research Facility, South Dakota

Fermi National Accelerator Laboratory, Illinois





[https://www.dunescience.org/]



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proton decay (baryon number violation process) (<1 interaction per year).

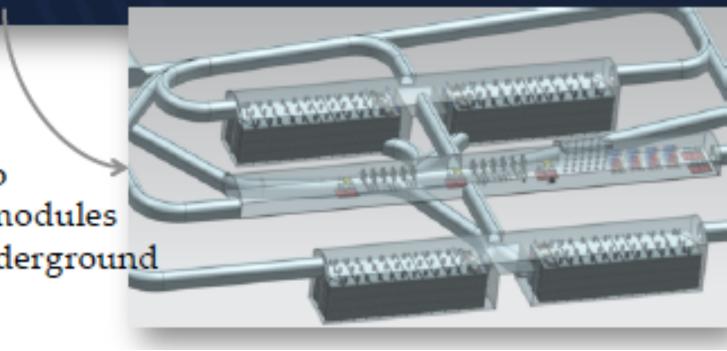
#### Sanford Underground Research Facility, South Dakota

Fermi National Accelerator Laboratory, Illinois

4 neutrino detector modules 1 mile underground

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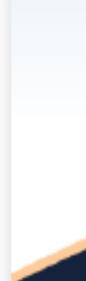
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**Requires continuous readout with ~100% live time and self-triggering.** 

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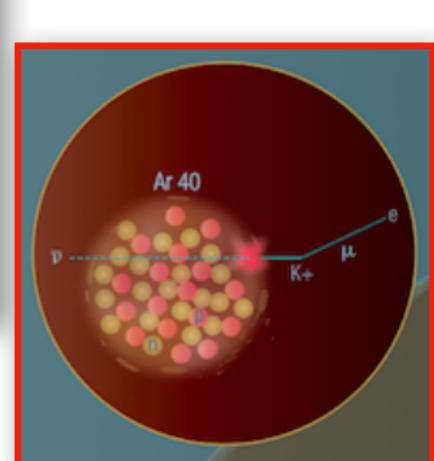
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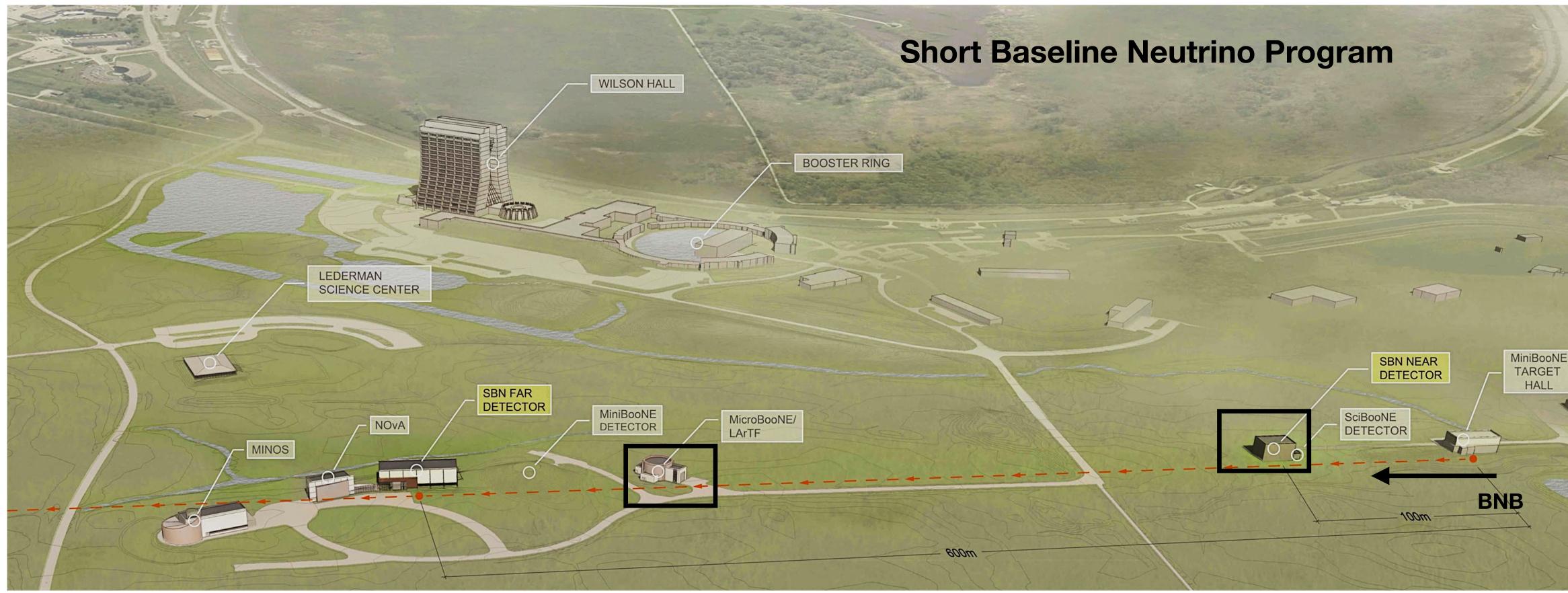
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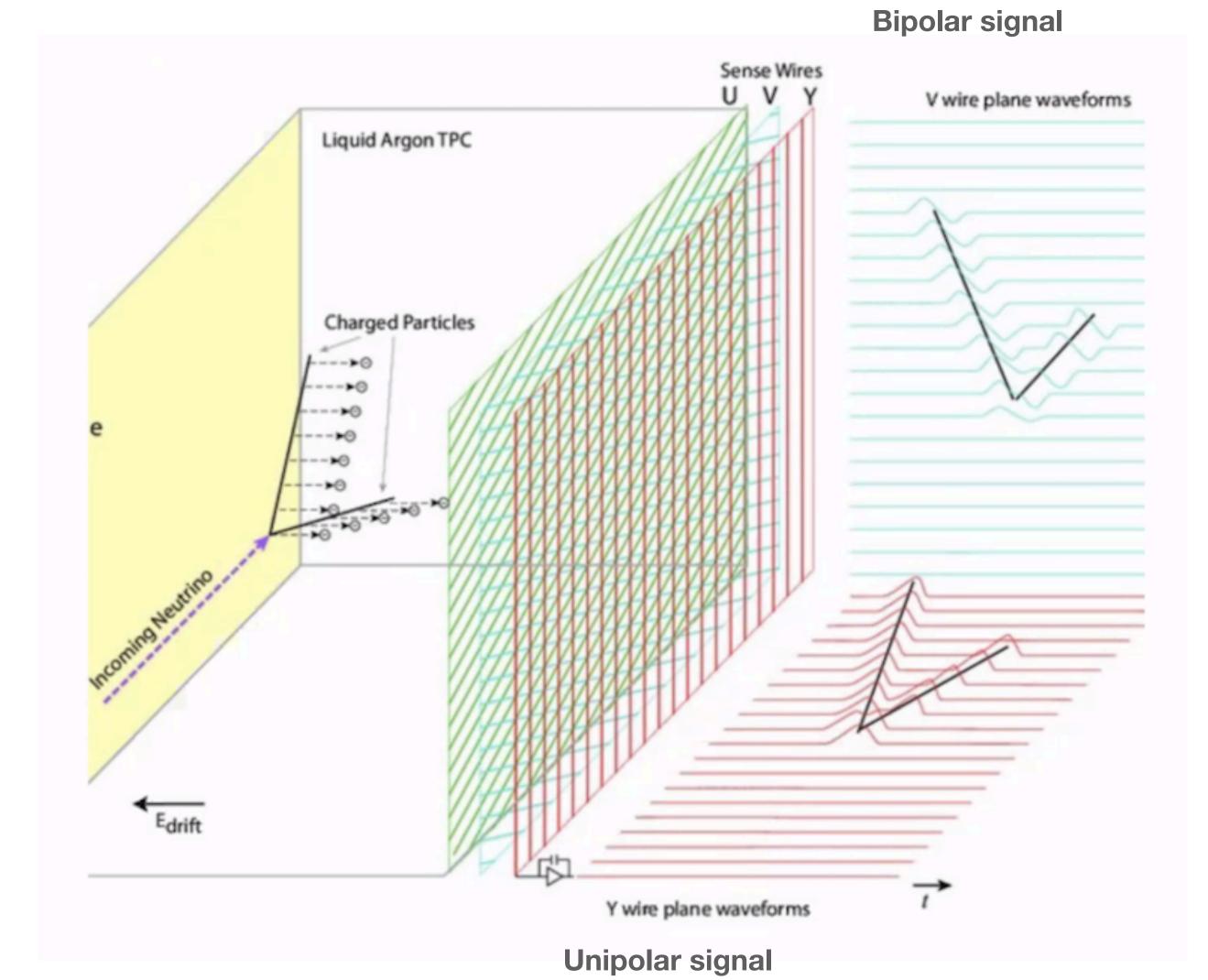
#### Current LArTPC detectors such as MicroBooNE and SBND, which share functionally identical back-end readout electronics, can be exploited to demonstrate and develop TPC-based trigger.

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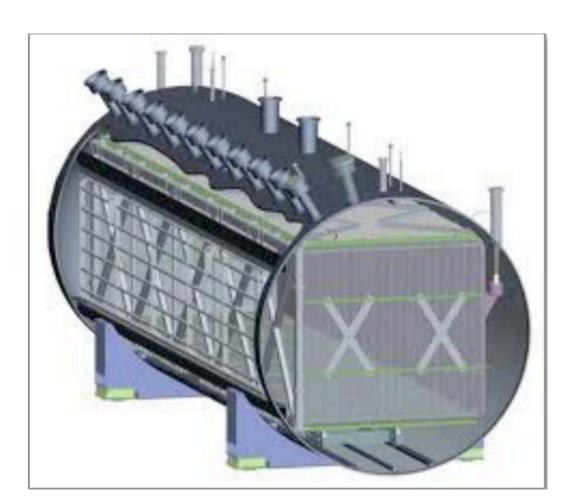
### Liquid Argon Time Projection Chamber (LArTPC)

- Neutrino interactions with Ar nuclei leave a trail of ionization electrons, drift towards anode under uniform electric field.
- Two induction planes (U & V) wires: orientation: @ 60° w.r.t vertical
  Collection plane (Y) wires: vertically oriented
  to enable 3D reconstruction of collected
  ionization tracks.
- PMTs located behind the anode planes capture prompt scintillation light and hence achieve 3D reconstruction along with the wire signal information.
- High spatial resolution and calorimetry for excellent particle identification.



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#### **MicroBooNE** and **SBND** Part of Short Baseline Neutrino Program at Fermilab



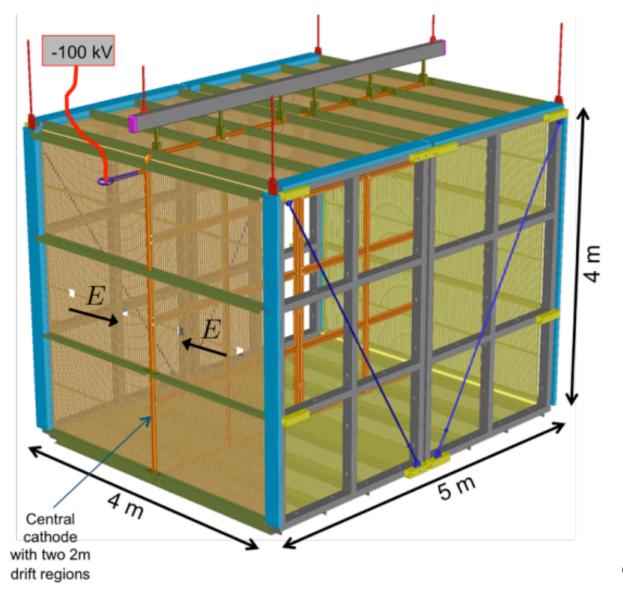
#### **MicroBooNE**

- 89 tons active\* LAr volume
- 8256 TPC wires (2MHz)
- 32 8" PMTs (64 MHz digitization)
- Data Rates: 33 GB/s

\*Maximum volume that can be used for physics analysis.

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in both experiments.



#### Functionally identical back-end readout electronics (digital processing electronics)

#### **SBND**

- 112 tons active\* LAr volume
- 11264 TPC wires (2MHz)
- 120 8" PMTs (500 MHz ulletdigitization)
- Data Rates: 45 GB/s

#### **MicroBooNE and SBND** Part of Short Baseline Neutrino Program at Fermilab

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- 8256 TPC wires (2MHz)
- 32 8" PMTs (64 MHz digitization)

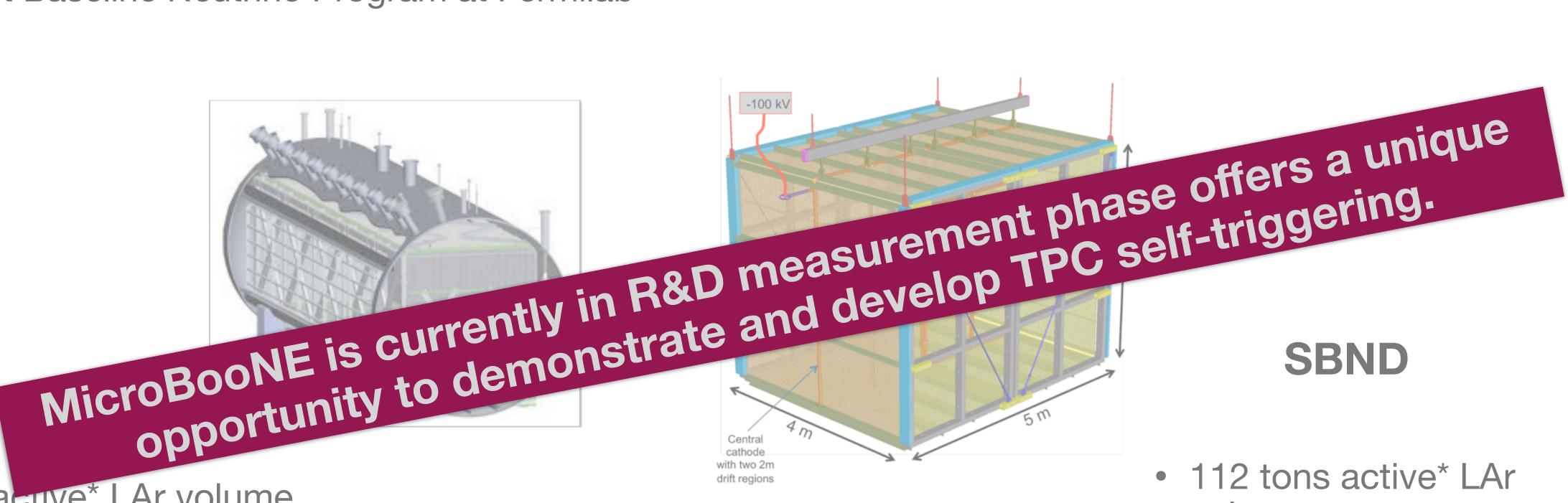
**MicroB** 

 Functionally identical back-end readout electronics (digital processing electronics) in both experiments.

Data Rates: 33 GB/s

\*Maximum volume that can be used for physics analysis.

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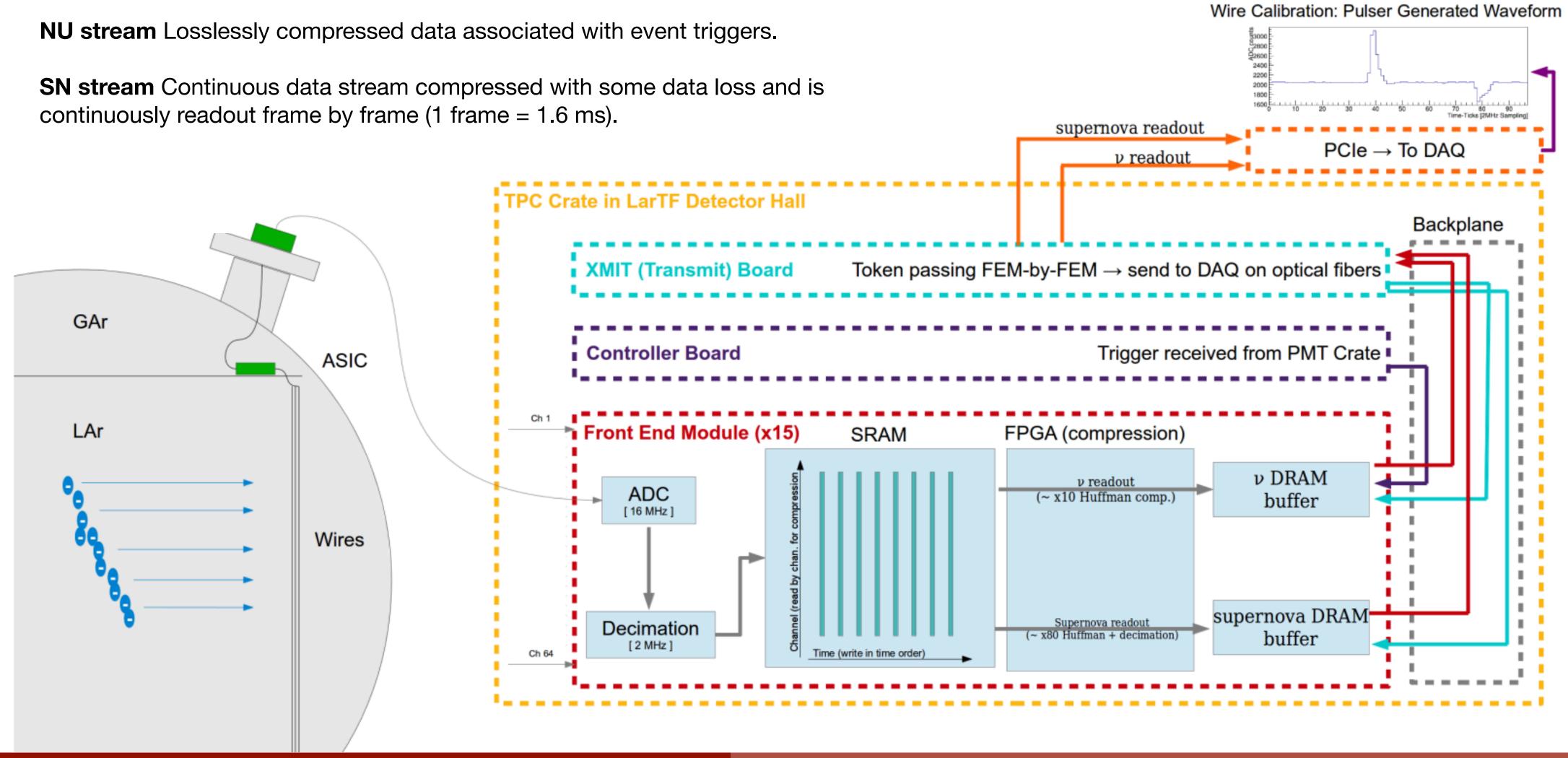
- volume
- 11264 TPC wires (2MHz)
- 144 8" PMTs (500 MHz digitization)
- Data Rates: 45 GB/s





## **TPC Readout electronics (MicroBooNE)**

The TPC readout electronics shapes, amplifies, digitizes and records the signal induced on anode wire planes and pass it to downstream data acquisition (DAQ) system.

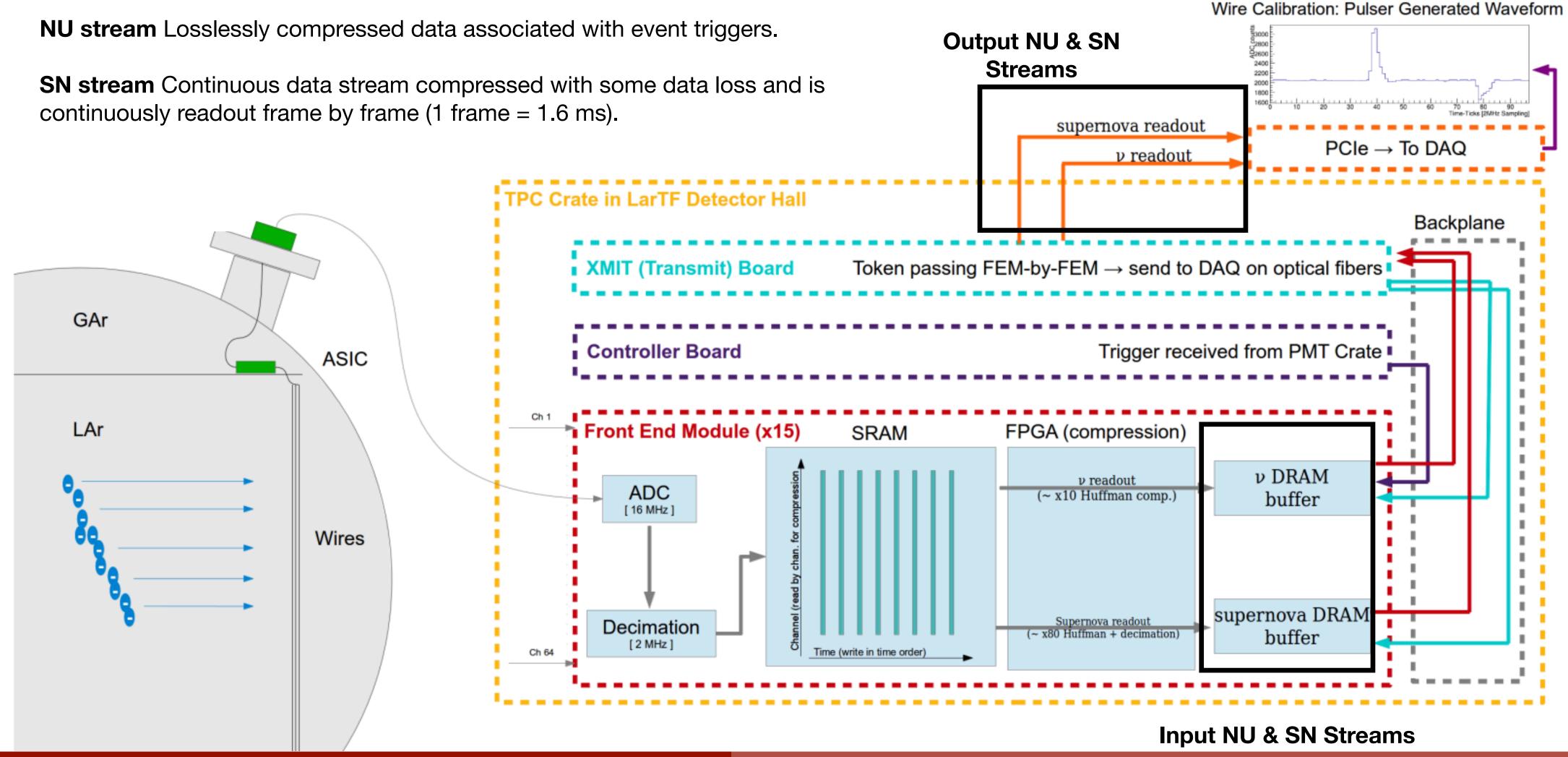


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## **TPC Readout electronics (MicroBooNE)**

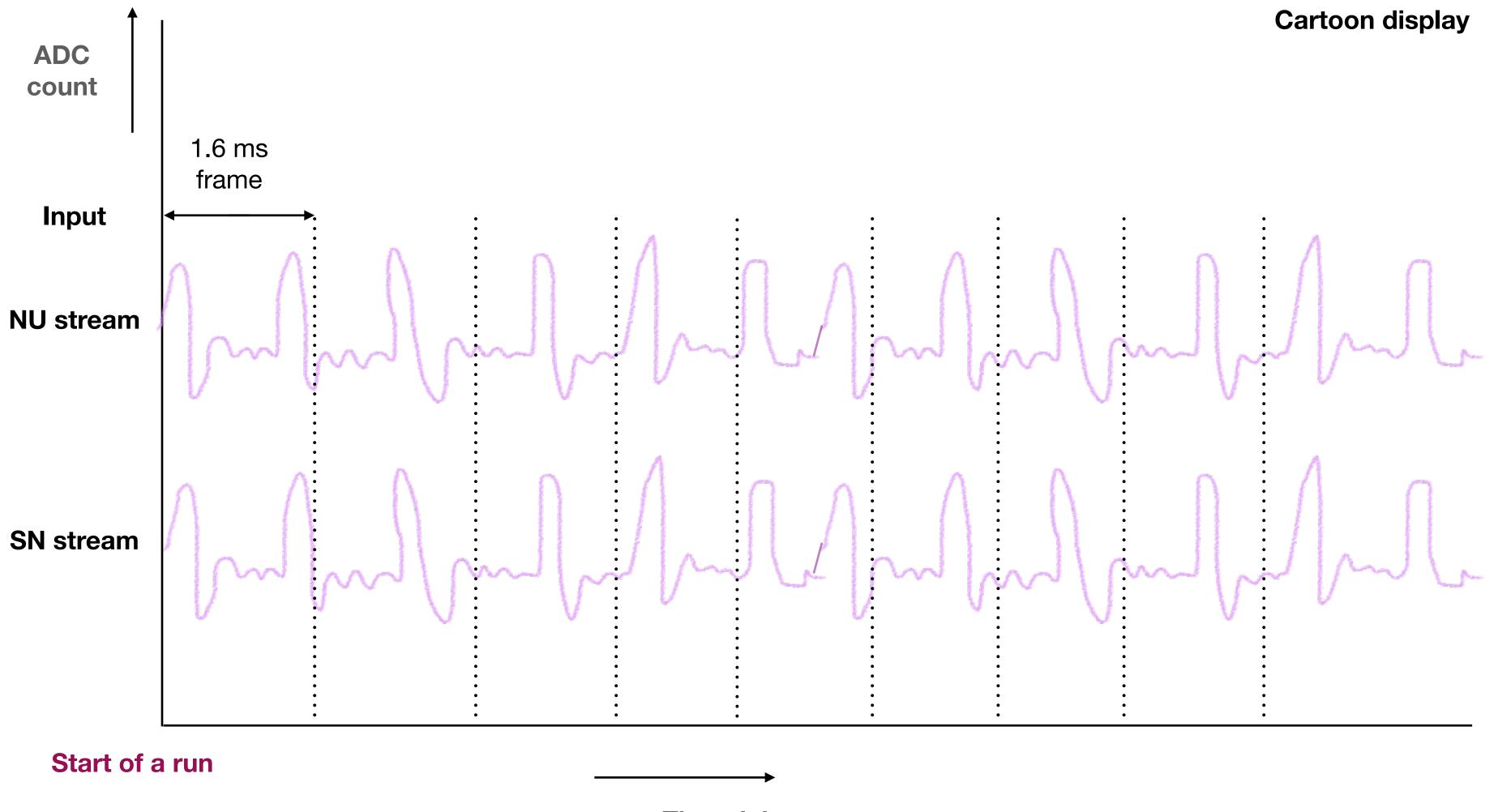
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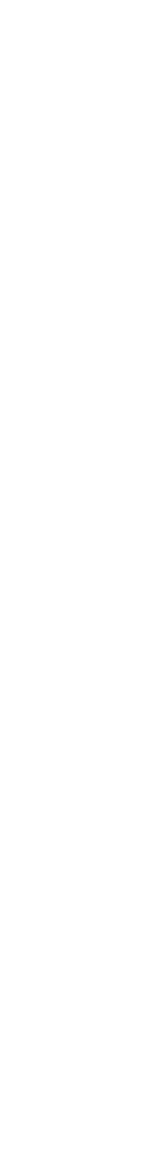
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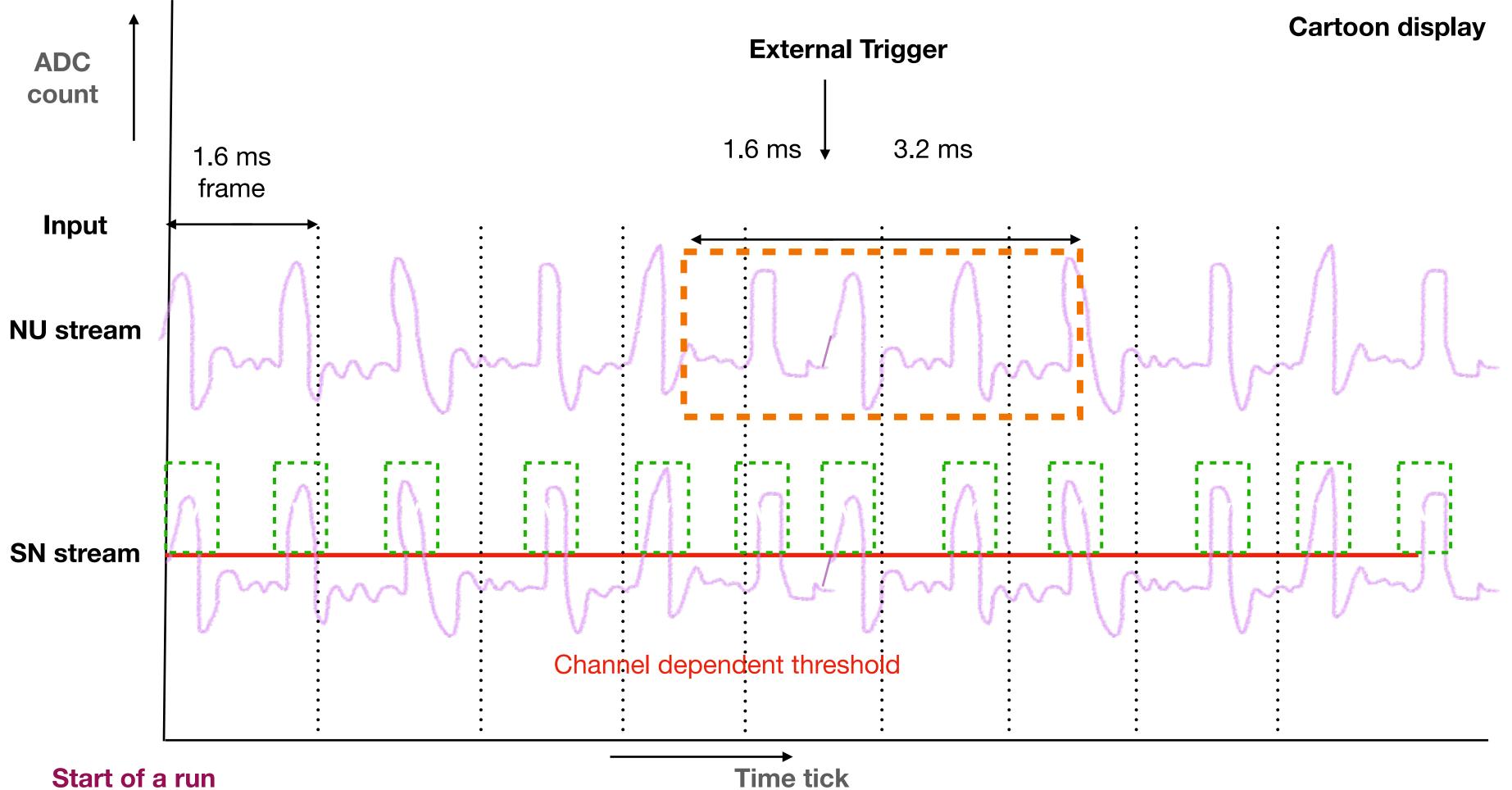












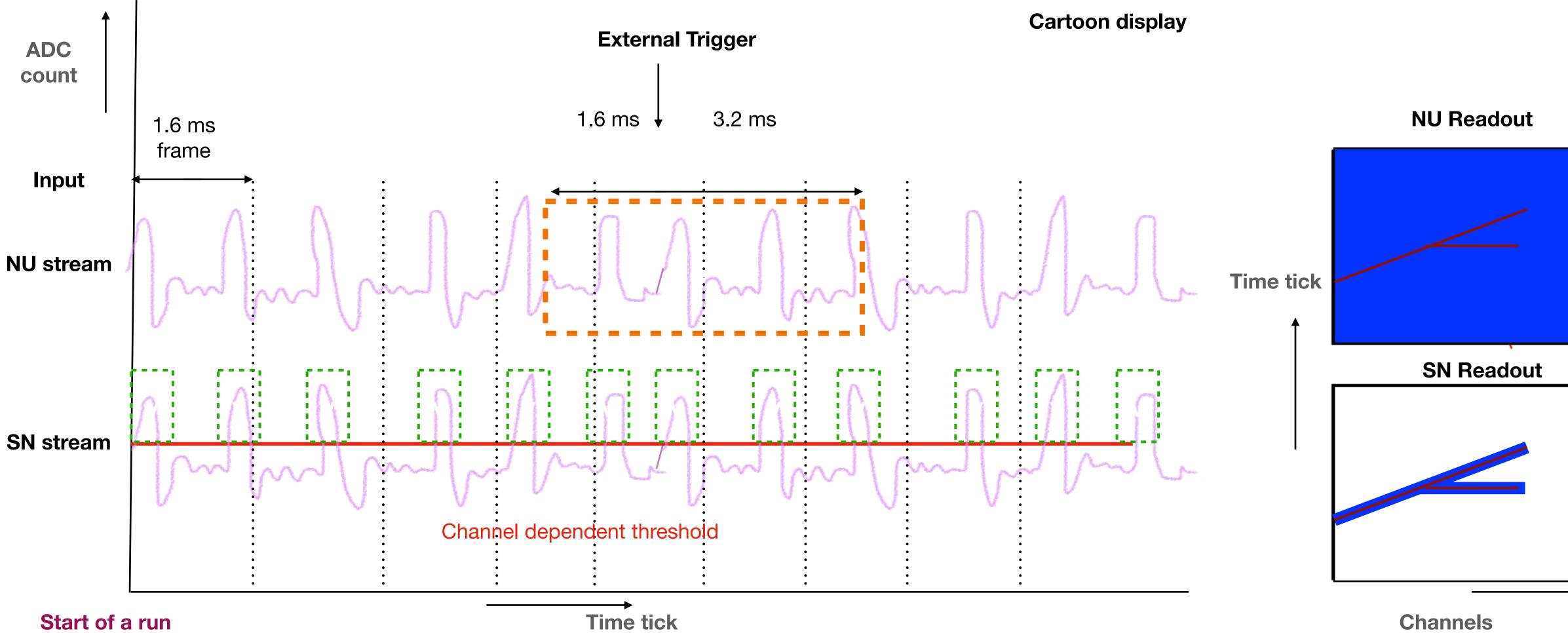
- NU stream: On receiving an external trigger, 4.8 ms of data is readout.

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• SN stream: Regions of interest (ROI) are extracted, whenever a waveform crosses a certain threshold.

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- NU stream: On receiving an external trigger, 4.8 ms of data is readout.

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**Channels** 

• SN stream: Regions of interest (ROI) are extracted, whenever a waveform crosses a certain threshold.

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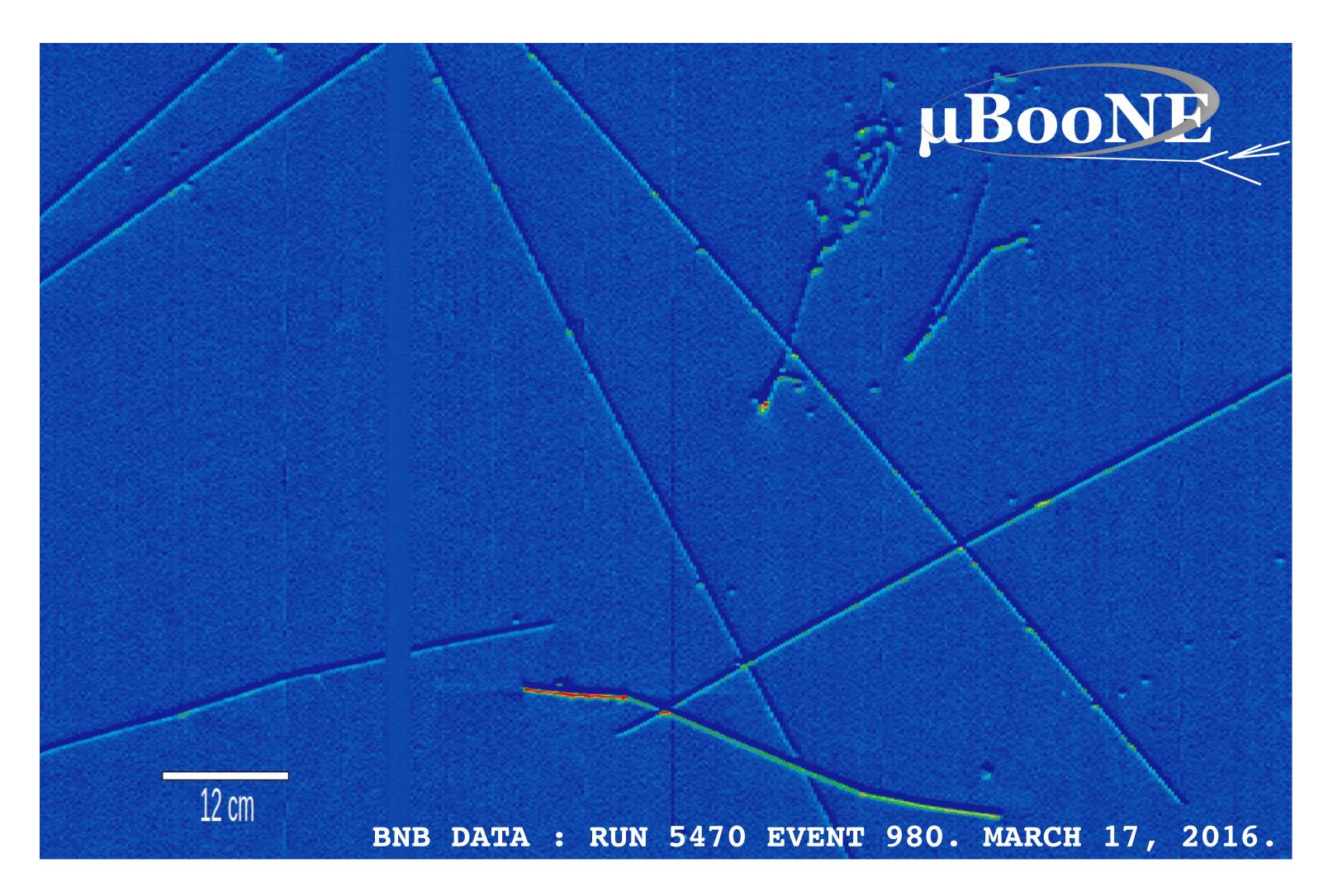




- NU stream is saved every time there is a trigger and then is diverted to Sub Event Buffers (SEBs) and then to Event Builders to build events (Data used for beam physics measurements)
- SN stream is saved to drive and is written to disk on receiving a SuperNova Early Warning System (SNEWS) alert (Data to search for non-beam events).



### Which events to trigger on?



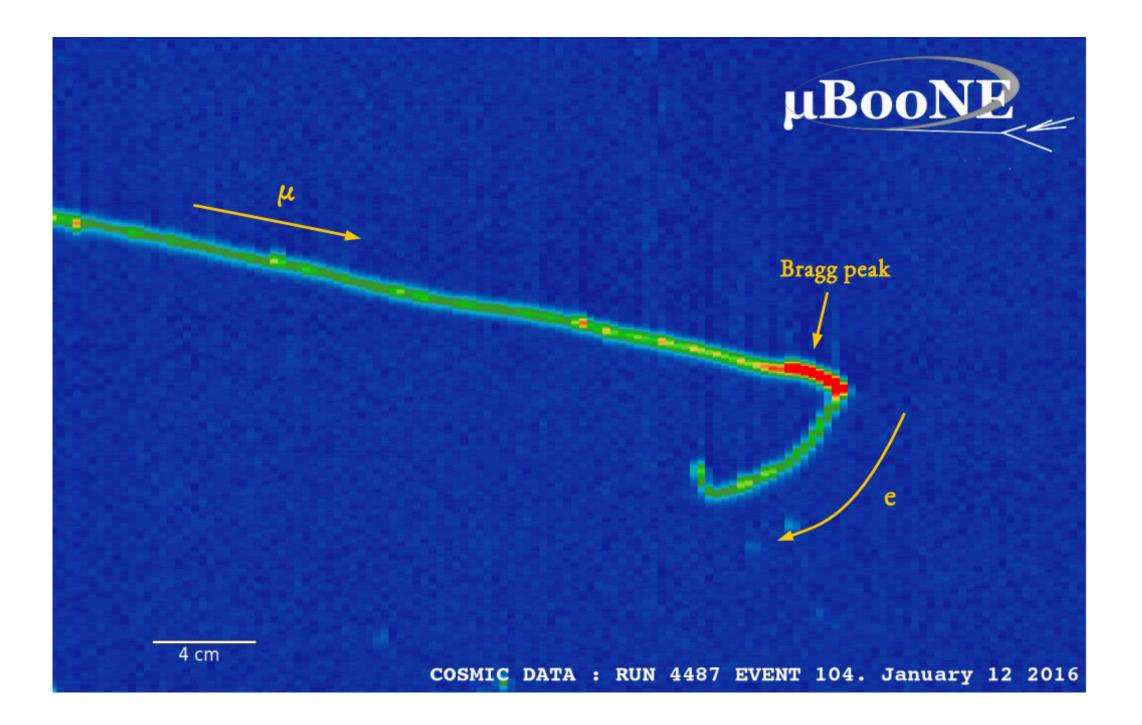
#### MicroBooNE, being situated on-surface, has a lots of cosmogenic activities.

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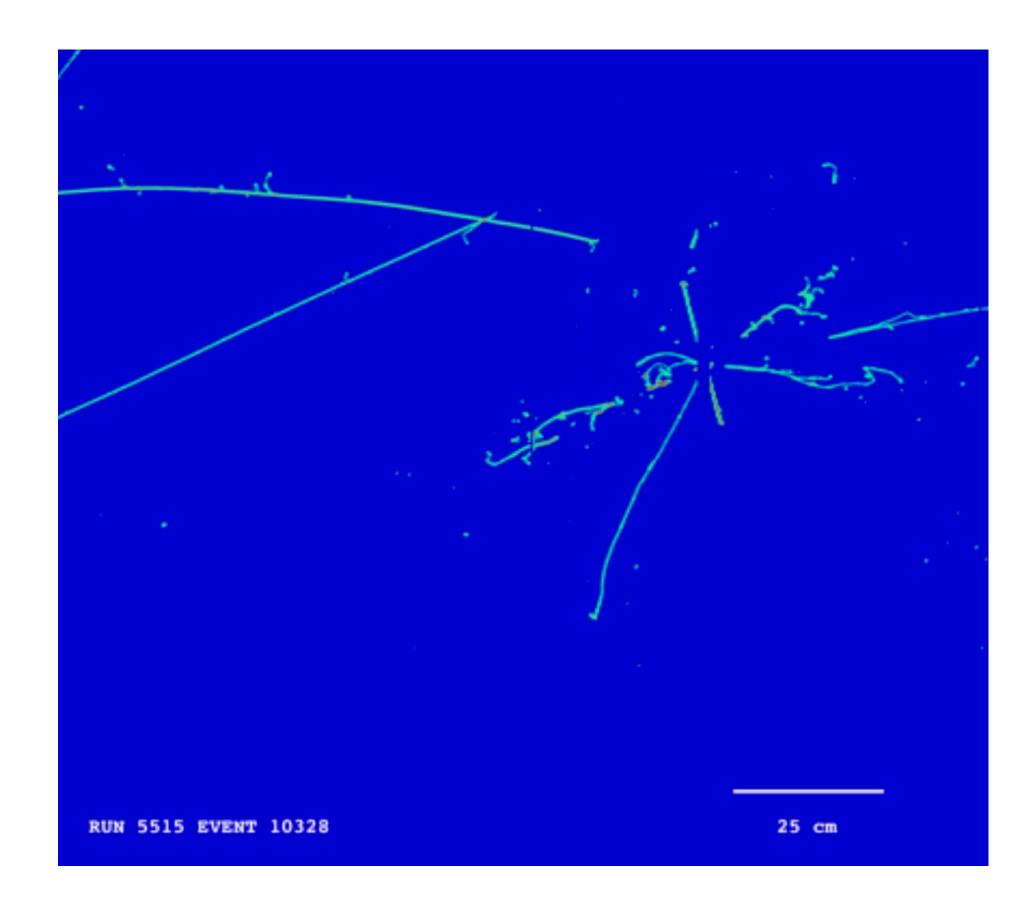


# Interesting events to trigger



 Some of the interesting interactions to trigger on include stopping muons, cosmogenic anti-proton or anti-neutron annihilation.

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# **TPC Trigger Strategy**

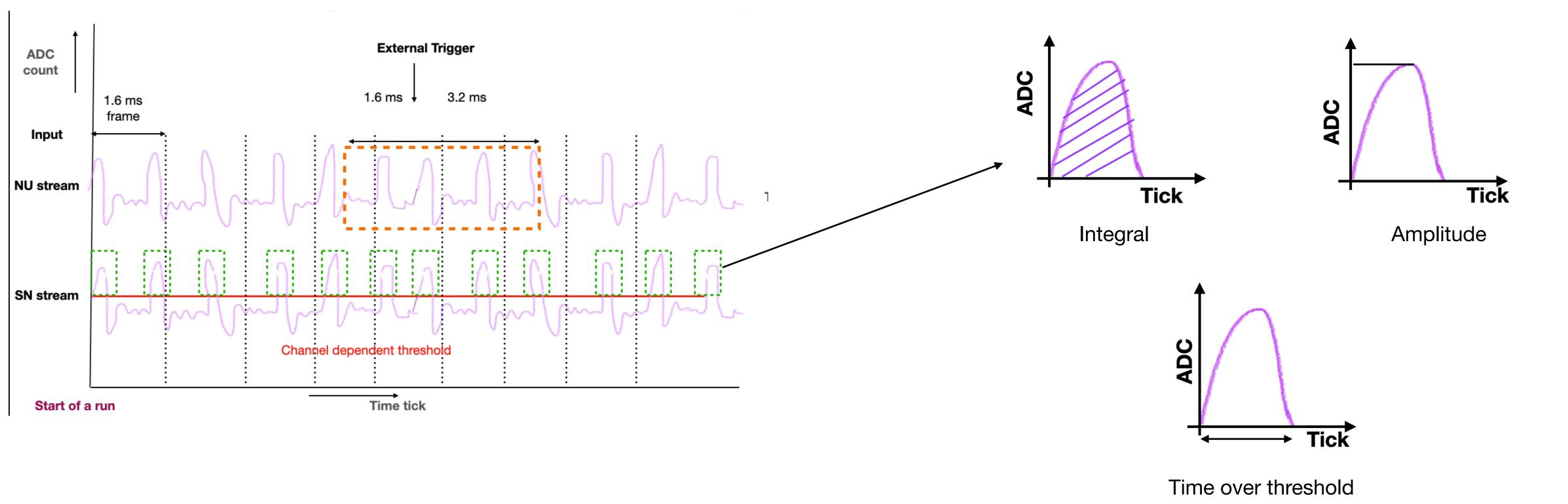
• Following DUNE trigger strategy, trigger primitives (TPs) can be constructed from MicroBooNE's SN stream ROIs.

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# **TPC Trigger Strategy**

• Following DUNE trigger strategy, trigger primitives (TPs) can be constructed from MicroBooNE's SN stream ROIs.



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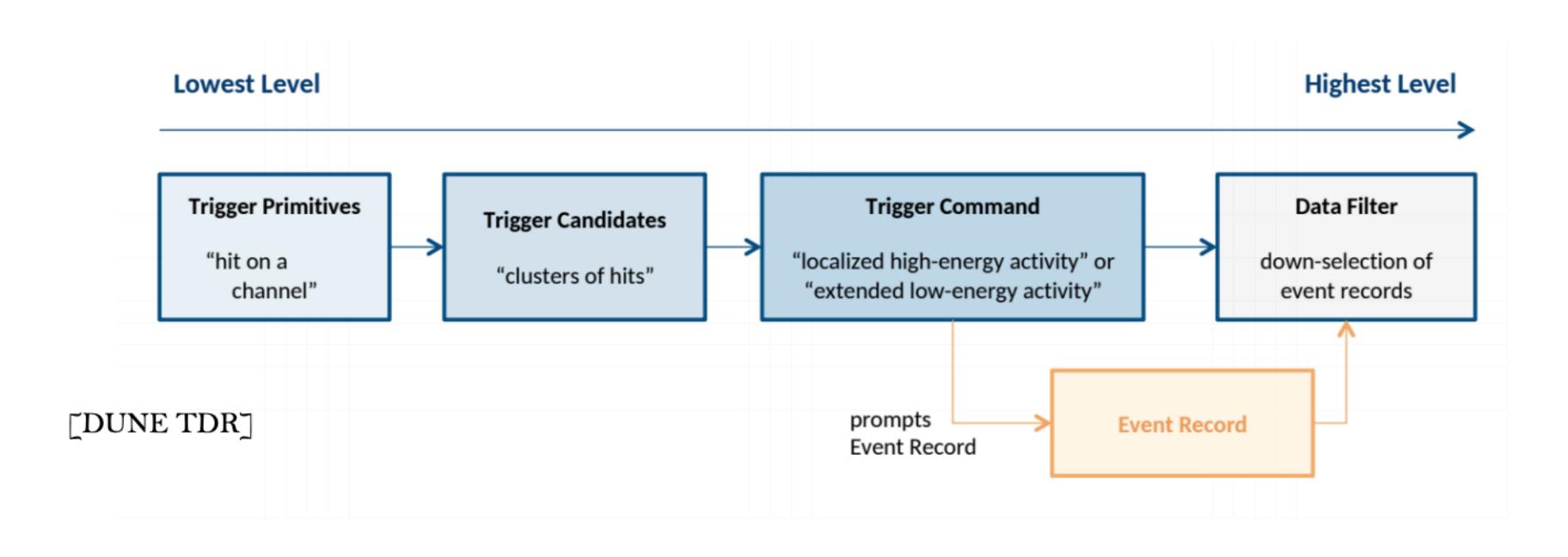
TPs are defined as a "summary" of an ROI:





# **TPC Trigger Strategy**

- Following DUNE trigger strategy, trigger primitives (TPs) can be constructed from MicroBooNE's SN stream ROIs.
- TPs can be used to make an online TPC trigger decision (TD) (in CPUs or GPUs) by constructing higher-level TPC triggered objects.

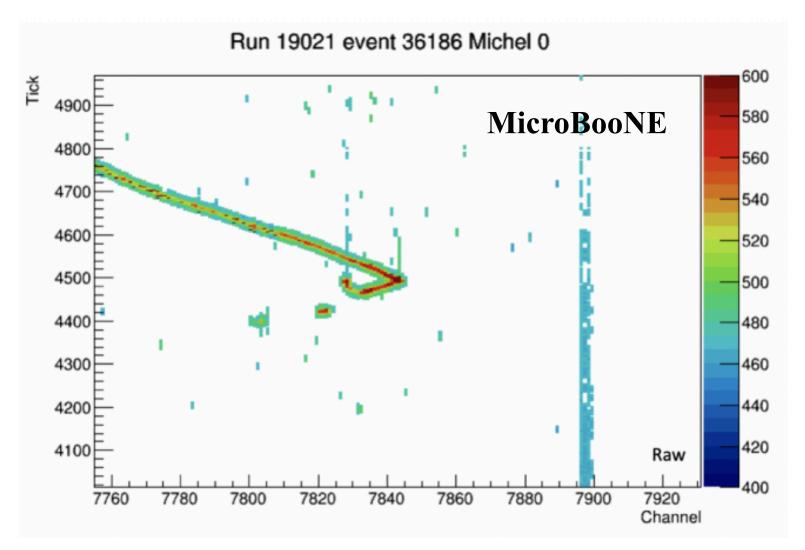


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# **Trigger Approaches**

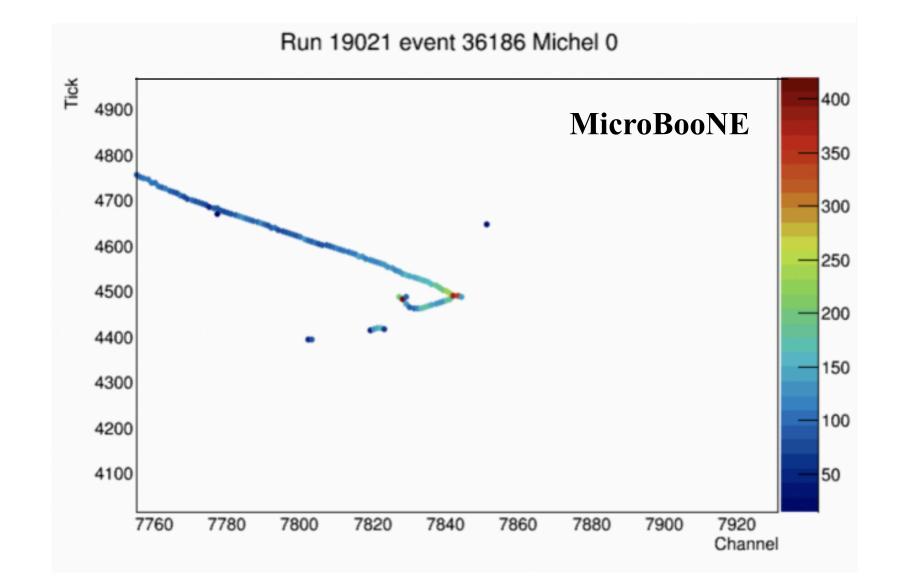
#### Michel electron candidate event display (MicroBooNE's ROI)



#### Hits are found by offline Gaussian Hit Finder Module

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#### An example of applying TP generation to ROIs. (ROI integral)



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### **Current Status for Online Trigger Development**

- MicroBooNE (& SBND in near-future).
- Working towards TP processing software and algorithms for online trigger generation.

0xFFFFFFFF	Begin of crate read-out
0xE0000000	End of crate read-out

FFFFFFF F1E3FFFF F6A8F001 F003F000 F002F000 F000F000 F000F000 107F4792 C000C000 C000C000 C000C000 E0000000 FFFFFFF F1E3FFFF F6A8F001 F003F000 F002F000 F000F000 F000F000 108049A2 C060C05D C030C154 C006C822 E0000000 FFFFFFF F1E3FFFF F6A8F001 F003F000 F002F000 F000F000 F000F000 10814993 C042C20E C008C7B8 C001C201 E0000000 FFFFFFF F1E3FFFF F6A8F001 F003F000 F002F000 F000F000 F000F000 108249A1 C063C848 C031C141 C006C822 E0000000 FFFFFFF F1E3FFFF F6A8F001 F003F000 F002F000 F000F000 F000F000 10834992 C045C7F0 C008C7AB C001C200 E0000000 FFFFFFF F1E3FFFF F6A8F001 F003F000 F002F000 F000F000 F000F000 108449A1 C06CC060 C036C13C C006C821 E0000000 FFFFFFF F1E3FFFF F6A8F001 F003F000 F002F000 F000F000 F000F000 10854991

TP generation has been implemented in FPGA for real-time implementation and testing in

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		FFFFF	FF F1	le3FFFF	F6A8F001	F003F000	F002F000	F000F000	F000F000	107F47
		C000C0	00 Ce	0000000	C000C000	E0000000	FFFFFFF	F1E3FFFF	F6A8F001	F003F0
		F002F0	00 F0	000F000	F000F000	108049A2	C060C05D	C030C154	C006C822	E00000
		FFFFF	FF F1	le3FFFF	F6A8F001	F003F000	F002F000	F000F000	F000F000	108149
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	FEM ID in the next 3 bits						)2F000	F000F000	F000F000	108549
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	event			FEM for this	event					
0xF	contains lower 12 bits of event number		0xF	contains upp	per 12 bits of even	t number				
0xF	contains lower 12 bits of frame number		0xF	contains upp	per 12 bits of frame	e number				
0xF000			0xF000							
0xF000			0xF000							
	Left 16bit 0xF 0xF 0xF 0xF	16bit0xFcontains FEM physical (slot) address in crate in the lowest FEM ID in the next 3 bits0xFcontains lower 12 bits of number of ADC words from FEM f event0xFcontains lower 12 bits of event number0xFcontains lower 12 bits of frame number	Left 16bitDescription0xFcontains FEM physical (slot) address in crate in the lowest 5 bits and FEM ID in the next 3 bits0xFcontains lower 12 bits of number of ADC words from FEM for this event0xFcontains lower 12 bits of frame number0xFcontains lower 12 bits of frame number	Left 16bit   Description   Right 16bit     DxF   contains FEM physical (slot) address in crate in the lowest 5 bits and FEM ID in the next 3 bits   0xFFFF     0xF   contains lower 12 bits of number of ADC words from FEM for this event   0xF     0xF   contains lower 12 bits of event number   0xF     0xF   contains lower 12 bits of frame number   0xF     0xF   contains lower 12 bits of frame number   0xF     0xF   contains lower 12 bits of frame number   0xF     0xF   contains lower 12 bits of frame number   0xF     0xF000   vert   0xF   0xF	Left 16bit   Description   Right 16bit   Description   Right 16bit   Description     0xF   contains FEM physical (slot) address in crate in the lowest 5 bits and FEM ID in the next 3 bits   0xFFF   0xFFF     0xF   contains lower 12 bits of number of ADC words from FEM for this event   0xF   0xF   contains lower 12 bits of frame number   0xF   0xF   contains lower 12 bits of frame number   0xF   0xF   contains lower 12 bits of frame number   0xF   0xF   contains lower 12 bits of frame number   0xF   0xF   contains lower 12 bits of frame number   0xF   0xF   contains lower 12 bits of frame number   0xF   contains upp FEM for this     0xF   contains lower 12 bits of frame number   0xF   0xF   contains upp FEM for this     0xF   contains lower 12 bits of frame number   0xF   0xF   contains upp     0xF   contains lower 12 bits of frame number   0xF   0xF   contains upp     0xF   contains lower 12 bits of frame number   0xF   0xF   contains upp     0xF.000    0xF   0xF   0xF   contains upp     0xF.00	Left 16bit   Description   Cotains FEM physical (slot) address in crate in the lowest 5 bits and FEM ID in the next 3 bits   Right FEM ID in the next 3 bits   Description   Right FEM ID in the next 3 bits   Description     0xF   contains lower 12 bits of number of ADC words from FEM for this event   OxF   Contains lower 12 bits of frame number   DxF   Contains upper 12 bits of number for this event     0xF   contains lower 12 bits of frame number   OxF   OxF   Contains upper 12 bits of frame number     0xF   contains lower 12 bits of frame number   OxF   Contains upper 12 bits of frame number     0xF   contains lower 12 bits of frame number   OxF   Contains upper 12 bits of frame number     0xF   contains lower 12 bits of frame number   OxF   Contains upper 12 bits of frame     0xF   contains lower 12 bits of frame number   OxF   Contains upper 12 bits of frame     0xF   contains lower 12 bits of frame number   OxF   Contains upper 12 bits of frame	Left 16bit   Description   S2-bit FEM header words   Right 16bit   Description   F000F000   F000F000	Left 16bit   Description   S2-bit FEM header words   Right 16bit   Right 16bit   Description   Right 16bit   Description   Right 16bit   Description   Right 16bit   Description   S2-bit SEM physical (slot) address in crate in the lowest 5 bits and FEM ID in the next 3 bits   N=FFF   FFFF   S2-bit Sef Number of ADC words from FEM for this event   N=FFF   S2-bit Sef Number of ADC words from FEM for this event   N=FFF   S2-bit Sef Number of ADC words from FEM for this event   N=FFF   S2-bit Sef Number of ADC words from FEM for this event   N=FFF   S2-bit Sef Number of ADC words from FEM for this event   N=FFF   S2-bit Sef Number of ADC words from FEM for this event   N=FFF   S2-bit Sef Number of ADC words from FEM for this event   N=FFF   S2-bit Sef Number of ADC words from FEM for this event   N=FFF   S2-bit Sef Number of ADC words from FEM for this event   S2-bit Sef Number of ADC words from FEM for this event   S2-bit Sef Sevent number   S2-bit Sef Sevent num	Left   0   2-bit FEM header words   Right   0 <t< td=""><td>F002F000   F000F000   F000F000   108249A1   C063C848   C031C141   C006C822     Left   Description   Right   Description   Pescription   F000F000   F000F00   F000F00   F000F00   F000F00   F000F00   F000F00   F000F00   F000F00<!--</td--></td></t<>	F002F000   F000F000   F000F000   108249A1   C063C848   C031C141   C006C822     Left   Description   Right   Description   Pescription   F000F000   F000F00   F000F00   F000F00   F000F00   F000F00   F000F00   F000F00   F000F00 </td

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TP generation has been implemented in FPGA for real-time implementation and testing in





### **Current Status for Online Trigger Development**

- MicroBooNE (& SBND in near-future).
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FFFFFFFF F1E3F Channel header(0x1..), C000C000 C000C F002F000 F000F TP header (0x[4..7]), unique Id) FFFFFFFF F1E3F C042C20E C008C **TP** Payload F002F000 F000F FFFFFFFF F1E3F C045C7F0 C008C )001 15 14 13 12 11 10 Word # / Bit # 3 LE3F 1 Time over threshold (number of values) MSB 0 1 1 2 Integral 0 0 1 1 0 4 Integral over 12 samples 0 0 1 1 MSB 0 0 1 1 5 6 Amplitude 0 1 1 MSB 0

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-		-				
FFFF	F6A8F001	F003F000	F002F000	F000F000	F000F000	107F47
C000	C000C000	E0000000	FFFFFFF	F1E3FFFF	F6A8F001	F003F0
F000	F000F000	108049A2	C060C05D	C030C154	C006C822	E00000
FFFF	F6A8F001	F003F000	F002F000	F000F000	F000F000	108149
C7B8	C001C201	E0000000	FFFFFFF	F1E3FFFF	F6A8F001	F003F0
F000	F000F000	108249A1	C063C848	C031C141	C006C822	E00000
FFFF	F6A8F001	F003F000	F002F000	F000F000	F000F000	108349
C7AB	C001C200	E0000000	FFFFFFF	F1E3FFFF	F6A8F001	F003F0
F000	F000F000	108449A1	C06CC060	C036C13C	C006C821	E00000
FFFF	F6A8F001	F003F000	F002F000	F000F000	F000F000	108549

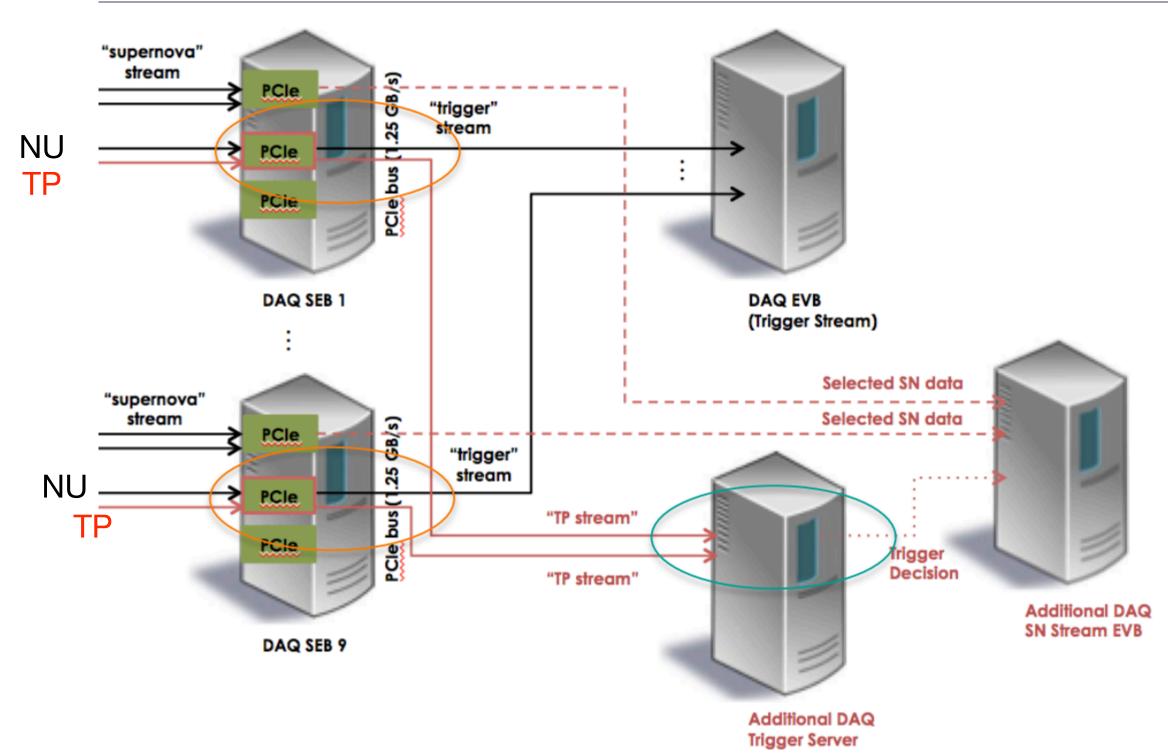


# **Trigger Approaches**

- MicroBooNE (& SBND in near-future).
- Working towards TP processing software and algorithms for online trigger generation.

- TPs stream to DAQ servers for online processing with a goal of generating TD.
- TD can be used to select the buffered SN readout data for subsequent event building.

#### TP generation has been implemented in FPGA for real-time implementation and testing in

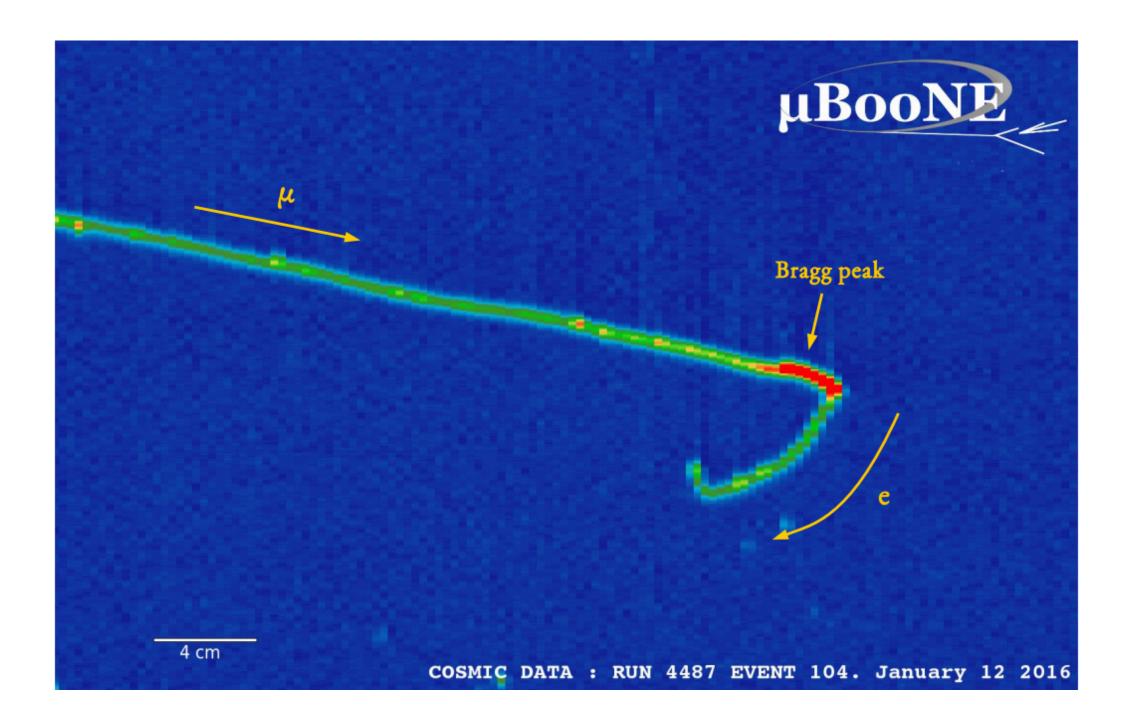


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# **Trigger Approaches**

- One can look for stopping muons, \*by looking at straight tracks making use of topological (existence of kink) and calorimetric (change in dE/dx at bragg peak) information to trigger on.
- There is also a possibility of exploring **image classification**, rather than having to cluster TPs to make a track to construct high lever trigger objects.



\*Michel Electron Reconstruction Using Cosmic Ray Data from MicroBooNE LArTPC (MicroBooNE Collaboration), JINST 12 (2017) 09, P09014

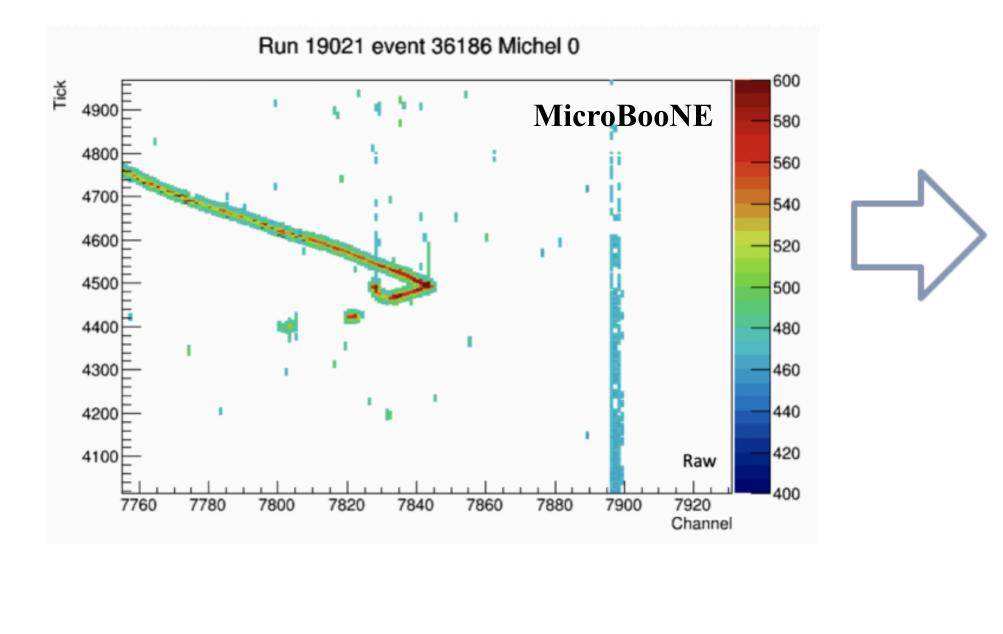
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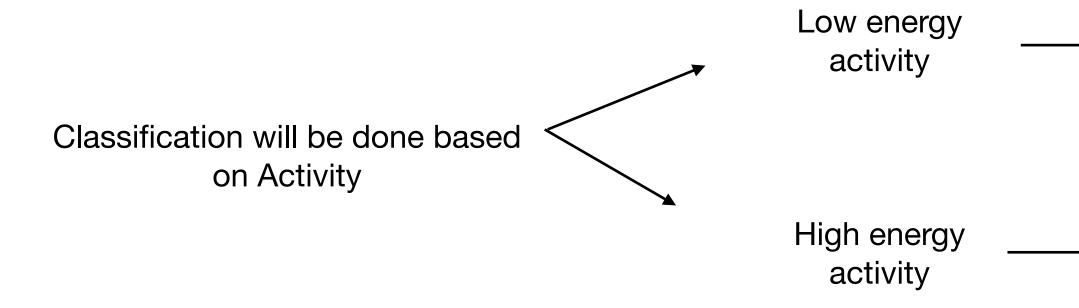


# Machine Learning (ML) based Trigger Approach

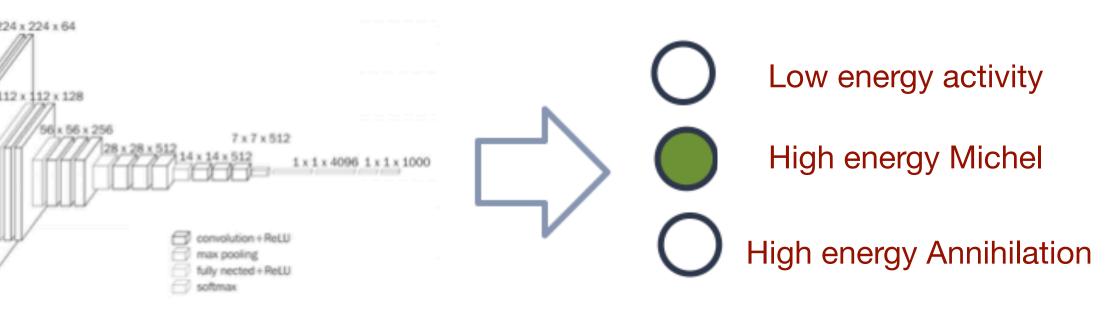
#### Image classification



224 x 224 x 3 224 x 224 x 64



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CNN classification



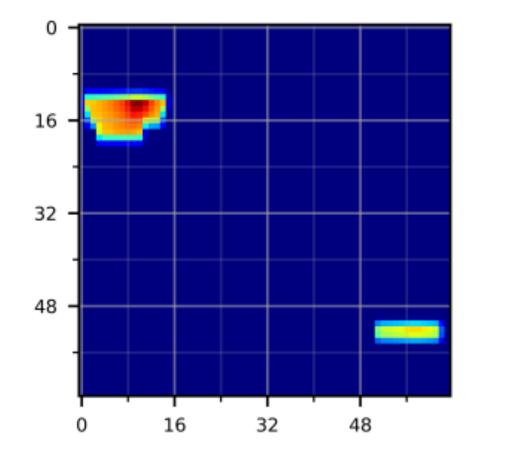
Michel electron, anti-neutron or anti-proton annihilation

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## **Future Possibility**

- like Field Programmable Gate Array (FPGA).
- Our group is also working on deploying CNN on FPGA (hardware stage of data selection, using HLS4ML tools\*) as it is much more power efficient.
- Preliminary results on ROI downsized images.



**Downsized 2D image of physics interaction** (Collection-plane only)

\*Please refer to the other talks in **TDAQ Session** of CPAD-2021 for more details

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• For future experiment such as DUNE, there is a possibility to use ML tools on specialized hardware



CNN classification

selection (e.g., lowest background class score)

**TDAQ Session, CPAD-2021** 



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	Train	Test	Ac	curacy (	Inference	
Sample	Size	Size	$\epsilon_{NB}$	$\epsilon_{LE}$	$\epsilon_{HE}$	Time (ms)
NB	12,023	4,027	99.53	0.47	0.12	
LE	12,050	3,970	4.01	94.48	1.51	1.6±0.1
HE	10,137	3,417	3.63	6.15	90.22	

NB: Noise & Background LE: Low Energy HE: High Energy

> \*Accelerating Deep Neural Networks for Real-time Data Selection for High Resolution Imaging Particle Detectors https://ieeexplore.ieee.org/stamp/stamp.jsp?tp=&arnumber=8909784&tag=1

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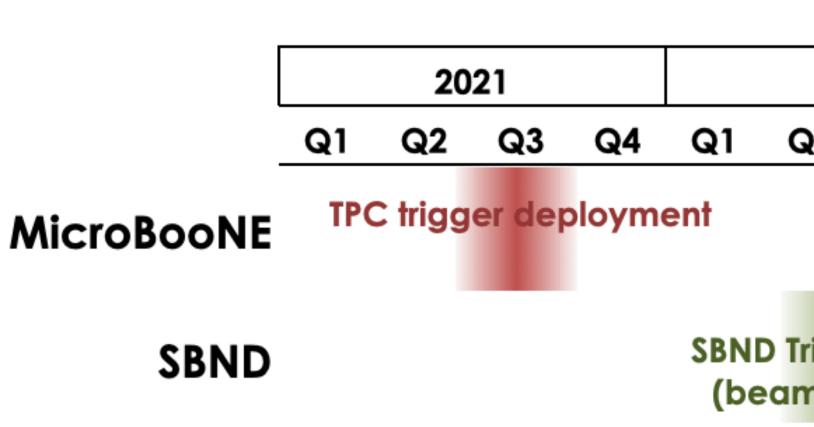


## Summary

With the currently & soon to be operating MicroBooNE & SBND LArTPCs, we have an exciting opportunity to: Carry out dedicated demonstrations for DUNE TPC trigger design.

- Develop novel (ML based) LArTPC trigger techniques for online or real-time data processing.
- Enhance future SBND and DUNE physics program.

**Timeline:** 



20	22		2023					
22	Q3	Q4	Q1	Q2	Q3	Q4		

SBND Trigger Commissioning (beam, photon detectors)







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Thank you

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