

CPAD Instrumentation Frontier Workshop 2021

Thursday, 18 March 2021

Readout and ASIC: Readout and ASIC - 1 (12:00 - 13:40)

-Conveners: Mitch Newcomer

time	[id] title	presenter
12:00	[53] The VMM ASIC - From R&D to production	IAKOVIDIS, George
12:20	[85] Characterization of ITkPix-V1 pixel readout chip	YANG, Hongtao
12:40	[175] Design and Integration of the Readout Electronics for ATLAS Liquid Argon Calorimeter for HL-LHC	XU, Hao
13:00	[180] R&Ds of ASICs and Optical Modules for Detector Data Communication	YE, Jingbo

Readout and ASIC: Readout and ASIC - 2 (14:00 - 16:00)

-Conveners: Gabriella Carini

time	[id] title	presenter
14:00	[65] The QPix pixelated readout concept for future Liquid Argon Time Projection Chambers: status and prospects	Dr LIU, Gang
14:20	[183] Low noise, low jitter cryogenic amplifier for superconducting nanowire detectors.	BRAGA, Davide
14:40	[154] A scalable low-noise skipper-CCD readout ASIC in 65 nm LP CMOS	ENGLAND, Troy
15:00	[153] Towards scalable fast-neutron and reactor-antineutrino detectors based on 6Li-doped PSD plastic scintillators and SiPM arrays	LI, Viacheslav

Friday, 19 March 2021

Readout and ASIC: Readout and ASIC - 3 (12:00 - 13:40)

-Conveners: Mitch Newcomer

time	[id] title	presenter
12:00	[144] First Observations with the BMX 21cm Intensity Mapping Pathfinder	Dr O'CONNOR, Paul
12:20	[173] Testing Virtual ASICs with Real Data: an Example from CMS timing upgrade	Dr WHITE, Sebasatian
12:40	[139] Multi-use advanced digital readouts using 3D stacking	YOUNGER, Richard
13:00	[186] Towards Edge Computing: Co-Design for Machine Learning based ASICs for Scientific Applications	MIRYALA, Sandeep
13:20	[104] A reconfigurable neural network ASIC for detector front-end data compression at the HL-LHC	HIRSCHAUER, James

Readout and ASIC: Readout and ASIC - Roundtable (14:00 - 15:10)

-Conveners: Gabriella Carini

time	[id] title	presenter
14:00	[208] Roundtable	