

# PACMAN v1rev3 update

Most significant change relative to previous versions is that this iteration needs to be able to drive eight 10x10 tiles

Validated:

- power draw consistent with v2b (0.3 W analog; 1.1 W digital for 10x10 tile)
- toggle VDDA, VDDD → DVM measurement roughly consistent with value from ADC
- data I/O with LArPix-v2 ASIC on all 8 “tiles”
- noise consistent with v2b
- data I/O full readout chain: tile → 2 m cable → PCB feedthrough → v1rev3 PACMAN

\*1 of 32 UARTs ineffective → 300-pin connector (observed with v1rev2b as well)

Next:

- 8-tile enclosure test
- ship to Bern

Thanks to Armin and Peter for quick iteration and debugging on firmware and circuit components

