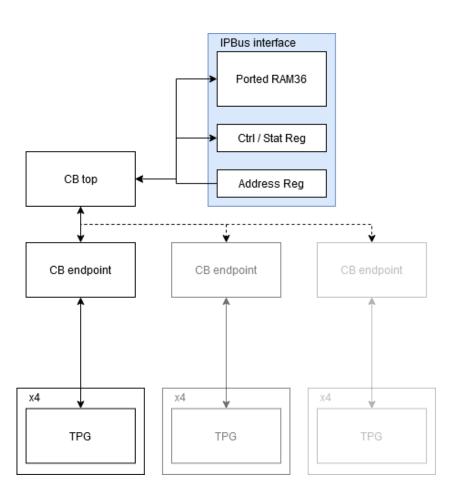
CB resource usage

William Wulff
Upstream DAQ
28-01-2021



Config Block integration

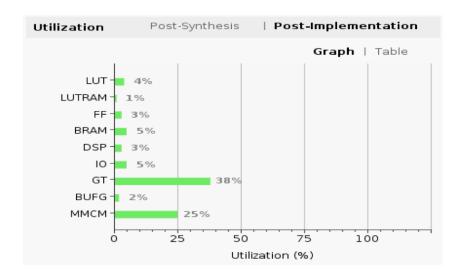


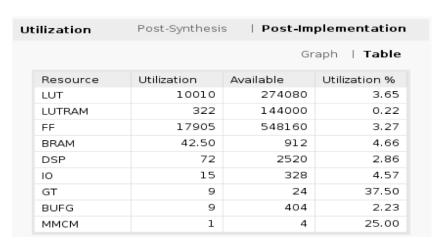
- Integration done for the tpgen-1fibre project
- ZCU based
- One processing chain
- Ipbus interface + CB top + CB endpoint



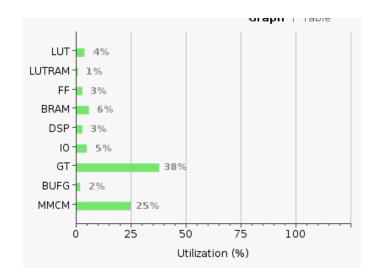
28-01-2021

Before





After



Resource	Utilization	Available	Utilization %	
LUT	10410	274080	3.80	
LUTRAM	322	144000	0.22	
FF	18089	548160	3.30	
BRAM	58.50	912	6.41	
DSP	72	2520	2.86	
10	15	328	4.57	
GT	9	24	37.50	
BUFG	9	404	2.23	
ммсм	1	4	25.00	



Specifics

Instance	Module		 Logic LUTs 		+ RAMB36
cb_toplevel cb_toplevel (cb_toplevel)	conf_block_top conf_block_top conf_block_top	282(0.10%) 282(0.10%) 38(0.01%)	282(0.10%) 282(0.10%)	160(0.03%) 160(0.03%)	16(1.75%) 16(1.75%) 0(0.00%)
CB_BACKEND	cb_backend	101(0.04%)	101(0.04%)	,	0 (0.00%)
fabric	ipbus_fabric_selparameterized01	33 (0.01%)			0 (0.00%)
mem1	ipbus_ported_dpram36 ipbus syncreg v parameterized1	80(0.03%) 30(0.01%)	80 (0.03%) 30 (0.01%)	17(0.01%) 52(0.01%)	16(1.75%) 0(0.00%)
reg slave1	ipbus_reg_vparameterized1	1 (0.01%)	1 (0.01%)	32 (0.01%)	0 (0.00%)

Instance	+ Module +	Total LUTs	Logic LUTs	FFs	RAMB36
endpoint	cb_endpoint cb_endpoint	40(0.01%)	40 (0.01%)	•	0(0.00%)



Integration summary

- An increase of
 - 400 LUTs
 - 184 FFs
 - 16 BRAMs
- Backend and Endpoint close to expected utilisation
- IPBus component accounts for roughly 50% of 1-link build



28-01-2021

Can we do better?

- Some options for optimisation of logic remain
 - Will most likely only result in minor gains
- Memory usage is high
 - Currently uses more memory than needed (14 bits)
 - CB is designed with the assumption that all parameters need to be stored internally. Is this needed?



Virtual memory

