

FPGA Development at Fermilab

Monday, 22 February 2021 14:00 (1 hour)

The purpose of this session is to provide a forum to raise awareness, introduce, and communicate about Field-Programmable Gate Array (FPGA) and, in general, real-time control and processing activities in which Fermilab engineers are participating. Example activities to be discussed might include small- and large-scale trigger, timing distribution and synchronization, event building, event processing, artificial intelligence, machine learning, feedback control loops, visualization, internet-of-things, and detector readout. Please join us as we share ideas as to how to get started with FPGAs, improve our workflows, and target future opportunities.

Summary

Presenter: RIVERA, Ryan (FNAL)

Session Classification: FPGA Development at Fermilab