



Workshop on concurrency in the many-cores era

November 21 and 22, 2011
FNAL / EVO

FNAL/ADSS and CERN/SFT

Meeting Location(s)

- Indico: <<https://indico.fnal.gov/conferenceDisplay.py?confid=4986>>
- FNAL: Wilson 7th floor, cross-over
- Meeting URL
 - <http://evo.caltech.edu/evoNext/koala.jnlp?meeting=MVM2Ml2e2uDeDl929eDe9v>
- Phone Bridge
 - ID: 432 8690

Breaks

- ☐ Coffee breaks outside meeting room
 - ☐ 10:30 and 15:30
- ☐ Lunch (Cafetería - not provided)
 - ☐ 12:30 to 14:00

Schedule

- Monday 09:00-10:30
 - Requirements and needs from existing and upcoming experiments.
- Monday 11:00-12:30
 - Hardware technology and trends and how it is planned to be done in other domains
- Monday 14:00-15:00
 - Requirements and needs from existing and upcoming experiments (Part 2)
- Monday 15:00-17:00 and Tuesday 9:00-12:30
 - Evaluation of technologies, designs ideas and prototypes
- Tuesday 14:00-15:00:
 - Discussion the future of hardware.
- Tuesday 15:00-17:00:
 - Discussion on future development.

Workshop Goals

- Understand the requirements and constraints of current and future experiments to efficiently exploit future many-core processor architectures. Assess the interest for a joint development of a generic software framework or a collection of services.
- Determine the degree of overlap between the needs and the medium to long term plans of the experiments Fermilab and CERN are supporting.

Identify a list of potential technologies that would allow the effective use of upcoming hardware landscape and ensure that all the relevant solutions are being explored.

Identify commonalities and synergies that lend themselves to collaboration. Decide on communication model.