

PPS: Summary and Planning

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HEP-CCE All Hands Meeting
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Plans for FY22 / FY23

- Want to allocate ~6 months to write reports
 - end major code development by April 1 2023
- Project timelines / development plans
 - WCT
 - Finish Kokkos, OpenMP and SYCL ports
 - develop testing framework, test interoperability with TBB
 - FCS
 - port to OpenMP and Alpaka
 - see what happens with `std::par` and `nvc++`
 - Patatrack
 - fully test AMD and Intel backends
 - port to SYCL and OpenMP
 - P2R
 - finish existing ports
 - develop better/more reproducible testing
 - ACTS
 - full chain ready by end of FY22 (hit clusterization, track finding, track fitting: ~15 kernels)
 - SYCL port (code structure is such that majority of functions are shared between CUDA/SYCL)
 - `std::par` if ready

Preparation for Final Report

- Performance matrix for all testbeds
 - hardware architecture vs portability layer
- Common format to report/store results
 - including metadata such as hardware, compiler/library/driver versions, code versions, etc
- Continue benchmarking existing codes as backends evolve
 - eg ROCm, SCYL and std::par
- Access to testing machines for more consistent reproducibility / comparisons
 - Aurora
 - LBL exalearn machines for A100 and MI100
 - What about Frontier?
- Write report!
 - decide where to publish

	Implementations								
	TBB	CUDA	HIP	Kokkos	Alpaka	Std::par (PSTL)	SYCL	DPL	OpenACC
NVIDIA									
AMD									
Intel									
CPU									

Completed
 In progress
 Under investigation
 Not started

Outreach

- Fall 22: present preliminary reports to experiments
 - during sessions at general meetings of experiments
- Spring 23: technical workshops
 - more technology focussed
 - addressed to wider audience (HEP community)
 - potentially involve HSF and IRIS
 - help develop ideas for our final reports
- Conferences to target
 - PASC 22 (June 26)
 - ACAT 22 (October 24)
 - CHEP 23 (May 8)
 - Supercomputing 23 (mid Nov)

 - coordinate with IOS
 - have both technical presentations from PPS and overarching from all of CCE