QA/QC plan for MOD-1

- **Old electronics will be swapped by the new one in Fermilab. Old one will be used in future tests in Bern**
- **Main concern is whether we can start QA/QC of electronics before we pass the review?**

▸ **QA/QC can be performed at some testing bench setup if possible**

1. **ADC check up, software setting up. Power ON ADC, waveform observation in software.**
2. **VGA check up, ch by ch testing by means of signal from pulse generator which is observed on ADC.**
3. **VGA control test, software setting up. Outputs voltage check up for a consistency with setup values. Joint test with VGA, few VGA channels is enough.**
4. **SiPM PS + SiPM PS control test. Software setting up, then SiPM PS check up without connected HV - observing ~5V on channels.**
5. **White Rabbit synchronization check up. Connect ADC with WR-switch, check WR timestamp in data.**

▸ **Warm test of Module-1**

1. **Insert all electronics in crate. Connect all the cables between boards in the crate and connect FT to the adapter cards. Power ON crate and LVPS. Check the current load on LV PS.**
2. **LED test. Power ON the HV PS and provide HV to SIPMs. Power ON LEDs one by one and check signal presence in ADC software.**