



Contribution ID: 110

Type: 20-minute talk

## BxB BPM and EPICS interface using the RFSoc FPGA

*Tuesday, 25 April 2023 14:20 (20 minutes)*

I will present the RFSoc based 500 MHz BxB Beam Position Monitor development status and epics control system interface.

The RFSoc-based advanced BxB provides many capabilities for measuring BxB beam transients, multi-bunch coupling, bunch charging, and beam trip transients.

Also, Xilinx RFSoc FPGA supports a hard ARM CPU core for interface with EPICS control systems.

I will introduce BxB hardware architecture, FPGA data processing, extensive data capturing, and epics interface for a control system.

**Please select if talk will be in person or on zoom**

In person

**Primary author:** HA, Kiman (BNL(NSLS-II))

**Presenter:** HA, Kiman (BNL(NSLS-II))

**Session Classification:** Plenary Session

**Track Classification:** SoC/SoM for Instrumentation