



Contribution ID: 115

Type: **20-minute talk**

## **AI/ML hosted on FPGAs at Fermilab**

*Wednesday, 26 April 2023 09:40 (20 minutes)*

This talk will include

- Discussion on FPGA-based system architectures, used in current AI/ML projects for accelerator applications.
- Lessons learned about interfaces along data signal path will focus on READS project.

### **Please select if talk will be in person or on zoom**

In person

**Primary authors:** BERLIOZ RIVERA, Jose Rene; IBRAHIM, M. A. (FNAL -AD/INST)

**Presenters:** BERLIOZ RIVERA, Jose Rene; IBRAHIM, M. A. (FNAL -AD/INST)

**Session Classification:** Plenary Session

**Track Classification:** AI/ML