

COPROCESSORS AND ACCELERATORS

PHILIPPE CANAL

ANNUAL CONCURRENCY MEETING - FNAL -
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CHALLENGES AND

- HOW TO MITIGATE LATENCIES (DUE TO DATA TRANSFER)
 - USE OF PIPE-LINING, CUDA-STREAMS, ETC.
- CRITERIA TO DECIDE WHICH COMPUTATION SHOULD BE DONE ON THE GPU OR ON THE CPU
- DATA STRUCTURES DESIGN
- OPTIMIZATION STRATEGIES
- PERFORMANCE GAINS AND EXPECTATIONS, TOTAL COST OF OWNERSHIP
- RISK OF VENDOR LOCK-IN

GEANT4 ON GPU

- GAIN 12 TO 34 FOR RUNGA-KUTTA,
- FLOAT FASTER BUT IN THE CASE OF G4 DEGRADE THE PRECISION TOO MUCH.
- IMPLEMENTED (PART OF) 3 EM PHYSICS - NO SECONDARY PRODUCTION YET WHICH IS THE NEXT CHALLENGE.
- IMPORTANCE OF THE MEMORY LAYOUT AND ACCESS.
- CUDA STREAM WORKED AS EXPECTED - NICELY TO REDUCE MEMORY UPLOAD/DOWNLOAD LATENCY.
- CHANGE FOR CUDA LEADS TO PERFORMANCE IMPROVEMENT IN CPU CODE ITSELF.

CMS ON GPU

- OPENCL -> WRITE ONCE, RUN EVERYWHERE.
- MORE CPU INCLUDE ON BOARD GPU THAT ARE IDLE IF NOT EXPLICITLY USED.
- PORTED MULTIPLE SCATTERING ALGORITHM.
- WITH OPENCL NEEDED 10,000 TRACKS TO AMORTIZE DATA-TRANSFER AND SCHEDULING.
- OPENCL ON CPU AS PERFORMANT AS REGULAR CODE.
- OPENCL BEING BEHIND FEATURE-WISE IS COMPENSATED BY THE PORTABILITY.
- OPENMP SCALING FOR “ FOR LOOP “ PARALLELIZATION.

NA62 ON GPU

- USE CUDA STREAM BUT IN REAL-TIME ENVIRONMENT NEED TO MITIGATE THE PROBLEM OF UNKNOWN LATENCY.
- PCIE 2 TO 3 DOES DOUBLE THE BANDWIDTH.
- IN 99.9% OF THE CASE LATENCY STABILITY IS GOOD.
- READY FOR PRODUCTION.
- WANTS TO INVESTIGATE DIFFERENT KIND OF SYNCHRONIZATION MECHANISM.

MINUIT ON GPU

- PARALLELIZING THE FUNCTION EVALUATION WITHIN MINUIT
- USES THE THRUST LIBRARY (SO FAR THE ONLY BACKEND).
- HIGH SPEED UP BUT REQUIRE CODING IN CUDA FOR PDF.
- LACK OF FINE-GRAINED PROFILING MAKES IT HARD TO TRACK DOWN BOTTLENECKS IN EXECUTION.

REVIEW

- **CUDA: ALLOW MAXIMAL PERFORMANCE.**
 - **STREAM IMPORTANT TO HIDE LATENCY, ARE THEY AVAILABLE IN OPENCL?**
- **OPENCL: ALLOW CROSS PLATFORM DEVELOPMENT.**
- **GPU PROVEN TO BE QUITE COST COMPETITIVE.**
 - **AS BANDWIDTH INCREASE CAPACITY INCREASE ALSO, SO LATENCY STAY AN ISSUE FOR THE NEXT GENERATION.**
- **CHANGE FOR CUDA LEADS TO PERFORMANCE IMPROVEMENT IN CPU CODE ITSELF.**