

# MAPS

## For future $e^+e^-$ detectors

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Caterina Vernieri, Alexandre Habib, Christos Bakalis, James E. Brau, Martin Breidenbach, Lorenzo Rota, Caterina Vernieri and Angelo Dragone

SLAC  
University of Oregon

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# Existing & Future efforts

- Our interest in MAPS is for both tracking and calorimetry applications
  - Simulation work in place to derive requirements for ASIC
- SLAC has engaged in FY22-23 with international efforts led by CERN and ALICE in the 65 nm development with a submission of a pixel sensor prototype in the ER1 run (within the CERN WP1.2 collaboration) and contributing effort to the ALICE ITS3 testing team (2212.08621, doi: 10.1109/TNS.2023.3299333)
- Chips received in September and currently under test
  - We are working on the next design iteration
- **We plan to develop common blocks on TJ65nm technology with CERN**

