Daphne v2A at CIEMAT

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Goals

- <u>First goal</u> FPGA Design:
 - Design and Implement a Self-Trigger algorithm.
 - Design and Implement a Waveform`s Primitive Calculation algorithm.
 - Minimize, as much as possible, HW resources used in the FPGA.
- <u>Second goal</u> Adapt Daphne for PMTs data acquisition:



Self-trigger & Primitive Calculation ALGORITHM (1)

Motivation



- <u>Self-trigger:</u>
 - Only packages with useful information are sent → Reducing data flow compared with streaming mode.
- Basic Waveform's Primitive Calculation:
 - Basic information of the signal is sent in the headers of the package.
 - Global PDS trigger can be done based on that specific information → At an early stage it is not necessary to analyze every single waveform at the DAQ.



Self-trigger & Primitive Calculation ALGORITHM (2)

Peak Detection





Self-trigger & Primitive Calculation ALGORITHM (3)

Trigger Condition & Primitive Calculation



- TRIGGER CONDITION: When a Peak is detected in NO DETECTION State.
- NO DETECTION State:
 - Peak detection variables calculation (Baseline Calculation).
- DETECTION State :
 - Peak detection variables calculation (Baseline remains constant)
 - Waveform`s Primitive Calculation.
 - Peak detection does not generate a self-trigger signal.
- SEND DATA State: Waveform's Primitve Data available.



Self-trigger & Primitive Calculation ALGORITHM (4)

Waveform's Primitives

- Real-time calculation of different parameters (trigger primitives) from the SiPM's waveform:
 - Peak Time
 - Amplitude (at peak-time)
 - Width (signal width above threshold)
 - Charge (area above baseline)
 - Number of Peaks detected (before baseline recovery)
- In order to reduce HW resources (Raw units)
 - Time (Peak Time, Width): Relative to trigger timestamp, number of bins / samples.
 - Amplitude: ADC counts
 - Charge: ADC*tics





Self-trigger & Primitive Calculation ALGORITHM (5)

Achievements and future plans

Achievements

- Design a Self-Trigger & Primitive Calculation Algorithm.
- Test the algorithm with Python Scripts using real data from CIEMAT's PDE measurement setups.
- Implement and Simulate (Post-Synthesis timing simulation) a 1 channel "Self-Trigger & Primitive Calculation" block in the FPGA
 - HW Resources: 246 LUT and 247 FF
- Test the 1 channel "Self-Trigger & Primitive Calculation" block in Daphne v2A firmware.
 - The block was placed in Daphne TOP Module just to test functionality. Several Spybuffers were created in order to follow different algorithm variables (Baseline, Amplitude, peak detections...)

Future plans

- Test the "Self-Trigger & Primitive Calculation" using the real output FIFOs and collecting data from Daphne Streaming frame format → It is required to add extra header to fit all waveform's primitives
- Implement a block to check trigger coincidences between channels.

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3	0000	TBD Channel																								
4	0000	TBD																								
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7	0000	0 ADC([11:0],6)						ADC([13:0],5)							ADC([13:8],4)											
8	0000	0 ADC([1:0],9) ADC([13:0],8)						ADC([13:0],7)								ADC([13:1]],6)									
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448	0000	ADC([11:0],1014)					ADC([13:0],1013)						AD	ADC([13:8],1012)												
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Self-trigger & Primitive Calculation ALGORITHM (6)

Post-synthesis timing simulation → SiPM





Self-trigger & Primitive Calculation ALGORITHM (7)

Post-synthesis timing simulation \rightarrow X-Arapuca (SuperCell)





Adapt Daphne for PMTs data acquisition (1) Motivation

- Proposal for installing 24 PMTs from ProtoDUNE-DP in ProtoDUNE-VD.
- An interface module between the PMT signals and Daphne will be required for signal conversion.
 - Impedance and signal conversion is required from single-ended to differential.
 - If required, we could also implement a trigger based on the coincidence of several PMTs.





Adapt Daphne for PMTs data acquisition (2)

Interface Daphne - PMT





- **Protoboard** → Transformer
- Transformer: Mini-Circuits TC2-1T+ → Reduces crosstalk between channels
 - Single-ended to Differential
 - Impedance conversion



Adapt Daphne for PMTs data acquisition (3)

Achievements and future plans

Achievements

- Design a circuit that meets the required specifications.
- Prototype and test the designed circuit.

Future plans

- Tune the circuit to obtain better response.
- PCB layout design.
- Implement a block to check trigger coincidences between channels.



Adapt Daphne for PMTs data acquisition (4)

Signal acquisition with our Self-Trigger algorithm





Thanks for your attention!



Peak Detection - BACKUP

Baseline calculation is based in calculating cumulative average.

$\overline{\mathbf{v}}$	$-\sum x_i$	$\rightarrow \overline{\mathbf{x}} - \overline{\mathbf{x}} \perp$	$x_{i+1} - \overline{x_i}$
λ	\overline{n}	$\rightarrow x_{i+1} - x_i + $	<i>n</i> + 1
	$\overline{x_{i+1}}$	$\overline{x}_i + \frac{x_{i+1} - x_i}{2^N}$	<u>i</u>

FILTERED DATA	BASELINE	AMPLITUDE	SOLPE	PEAK DETECTION
Initial condition: $F_0 = x_0$ Algorithm:	Initial condition: $B_0 = x_0$ Algorithm:	Initial condition: $A_0 = 0$ Algorithm:	Initial condition: $S_0 = 0$ Algorithm:	Initial condition: $P_0 = false$ Algorithm:
$F_{i+1} = \frac{x_i + x_{i+1}}{2}$	If Detection $B_{i+1} = B_i$ Else B_{i+1} $= B_i + \frac{F_{i+1} - B_i}{8}$	$A_{i+1} = F_{i+1} - B_{i+1}$	$S_{i+1} = A_{i+1} - A_i$	If $S_{i+1} < -10$ $P_0 = false$ Else $P_0 = true$



Waveform's Primitive Calculation- BACKUP

While **DETECTION** State

PULSE WITH	TIME TO PEAK	MAX AMPLITUDE	CHARGE	NUMBER OF PEAKS
Initial condition: $W_0 = 0$ Algorithm:	Initial condition: $TP_0 = 0$ Algorithm:	Initial condition: $MA_0 = 0$ Algorithm:	Initial condition: $C_0 = 0$ Algorithm:	Initial condition: $NP_0 = 0$ Algorithm:
$W_{i+1} = W_i + 1$	If $Amplitude_{i+1} < MA_i$ $T_{i+1} = W_{i+1}$ Else $T_{i+1} = T_i$	If $Amplitude_{i+1} < MA_i$ MA_{i+1} $= Amplitude_{i+1}$ Else $MA_{i+1} = MA_i$	$C_{i+1} = C_i \\ + Amplitude_{i+1}$	If $Peak_Detection$ $NP_{i+1} = NP_i + 1$ Else $NP_{i+1} = NP_i$

