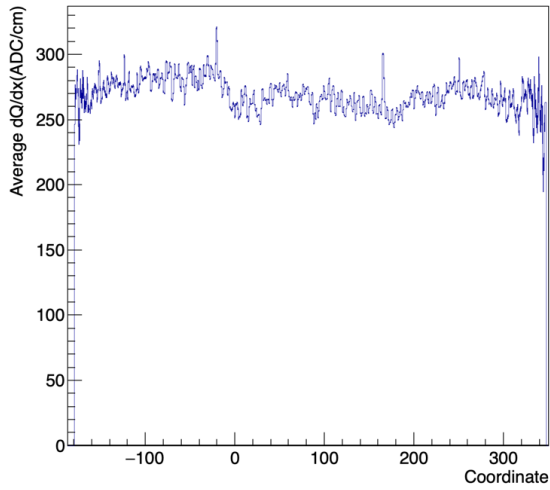
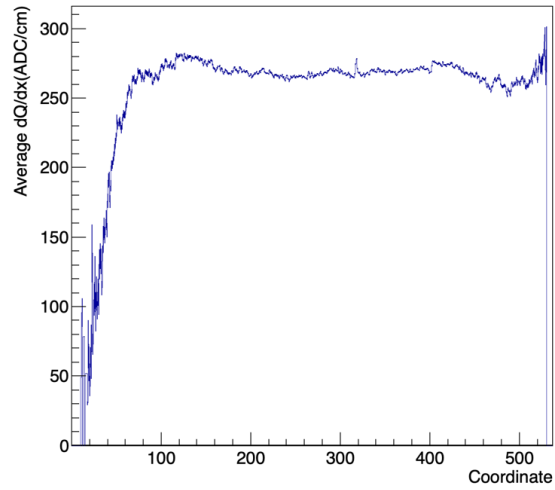


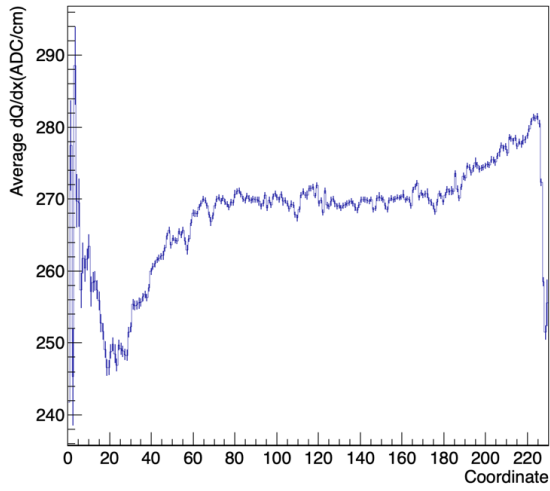
APA: 3 Signal plane: U Binned in U



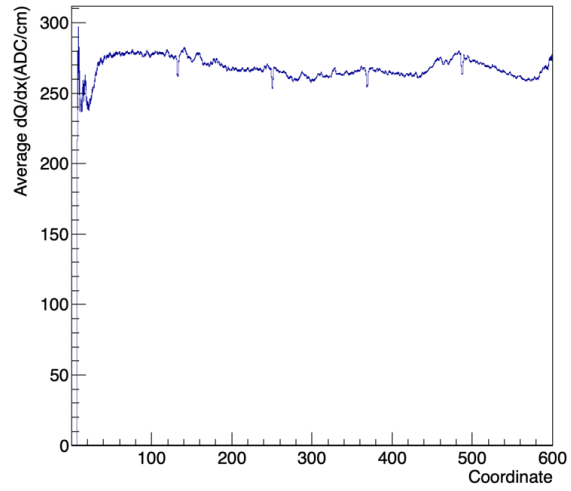
APA: 3 Signal plane: U Binned in V



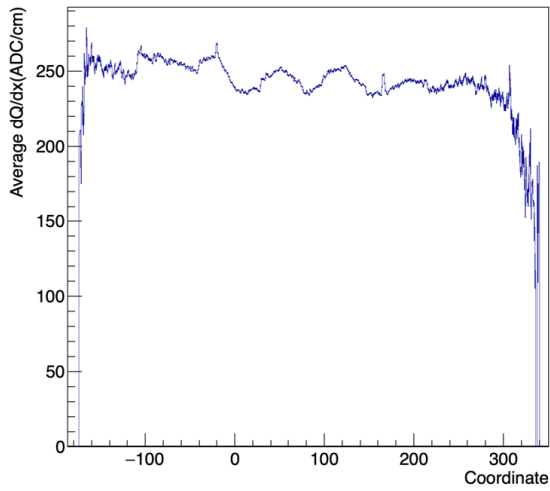
APA: 3 Signal plane: U Binned in Z



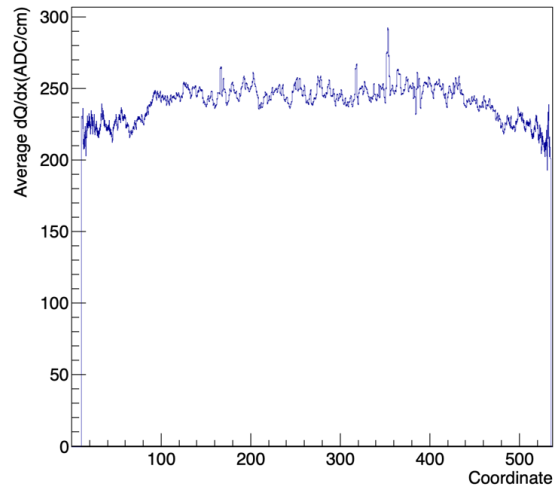
APA: 3 Signal plane: U Binned in Y



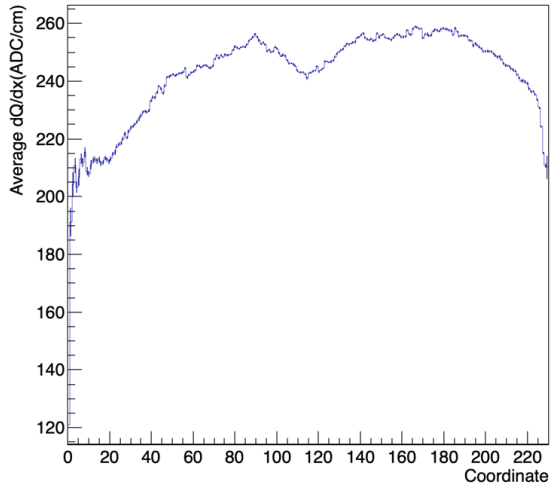
APA: 3 Signal plane: V Binned in U



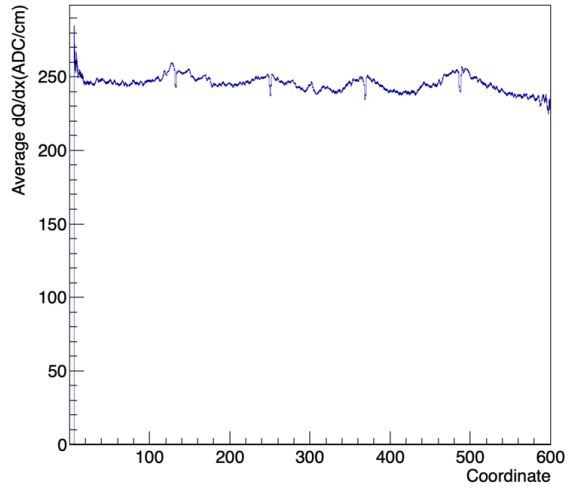
APA: 3 Signal plane: V Binned in V



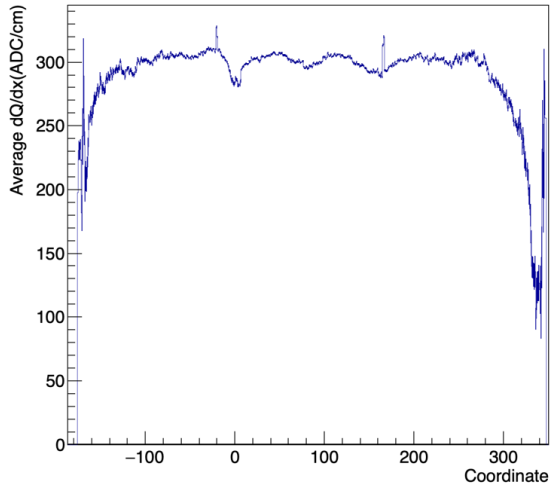
APA: 3 Signal plane: V Binned in Z



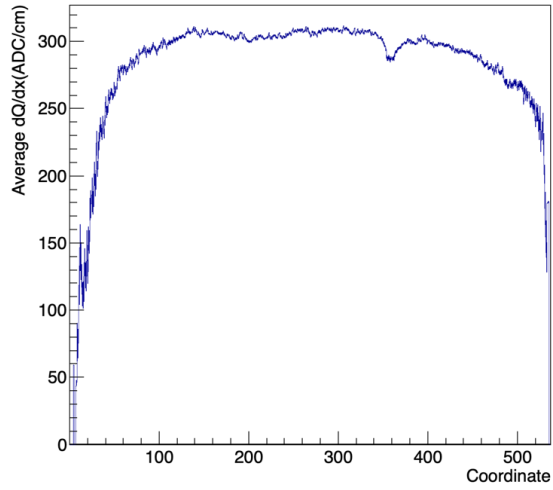
APA: 3 Signal plane: V Binned in Y



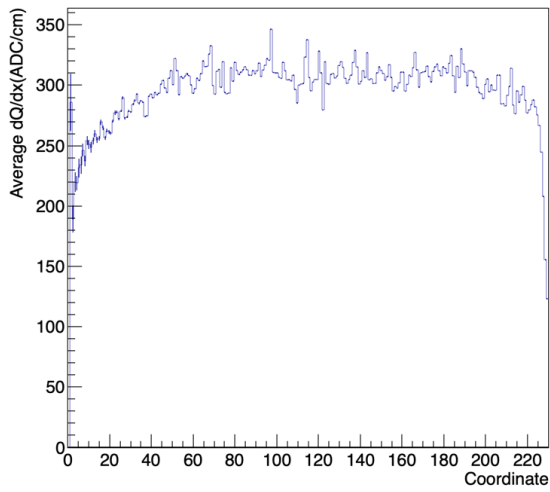
APA: 3 Signal plane: Z Binned in U



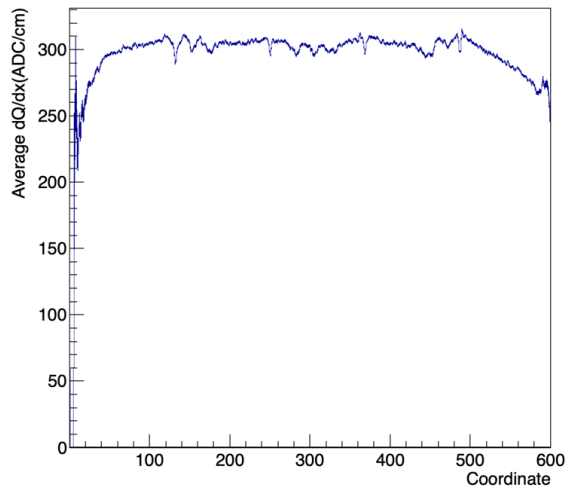
APA: 3 Signal plane: Z Binned in V



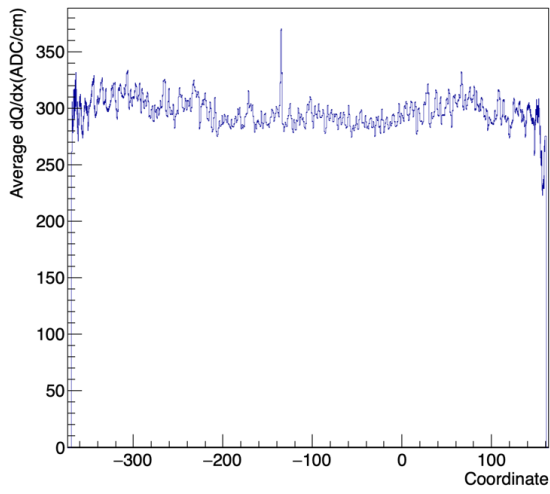
APA: 3 Signal plane: Z Binned in Z



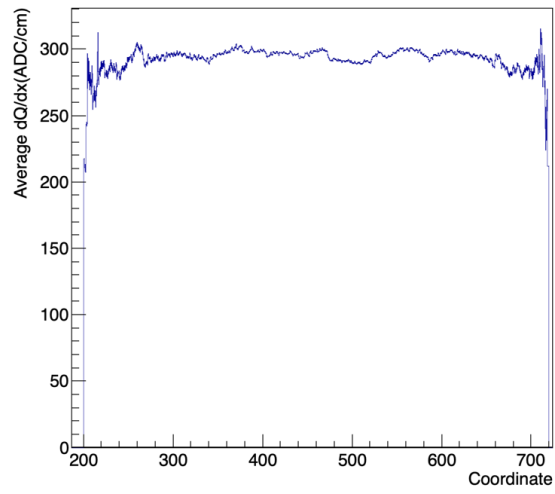
APA: 3 Signal plane: Z Binned in Y



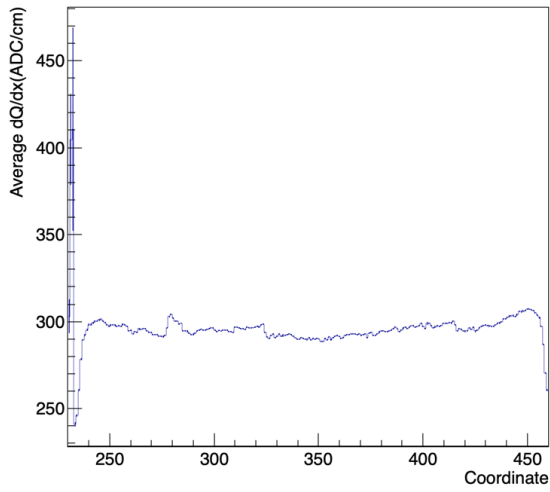
APA: 2 Signal plane: U Binned in U



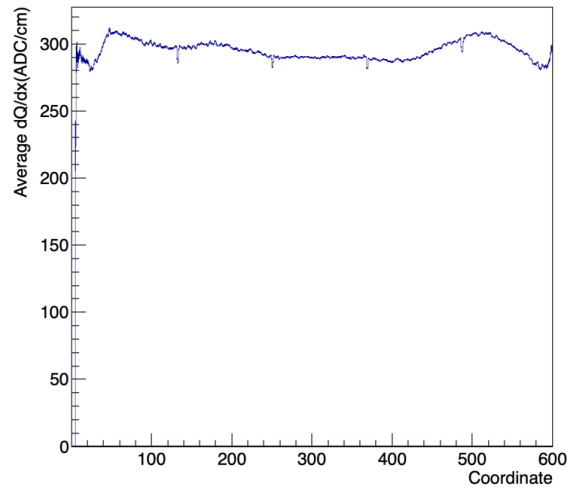
APA: 2 Signal plane: U Binned in V



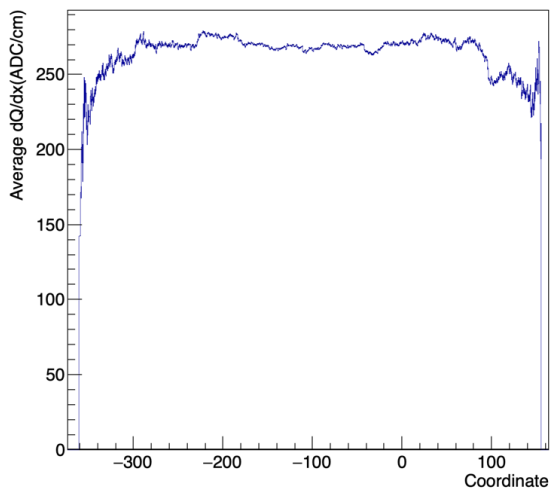
APA: 2 Signal plane: U Binned in Z



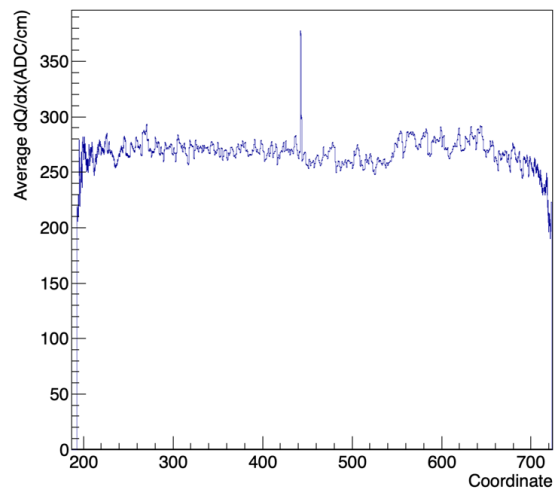
APA: 2 Signal plane: U Binned in Y



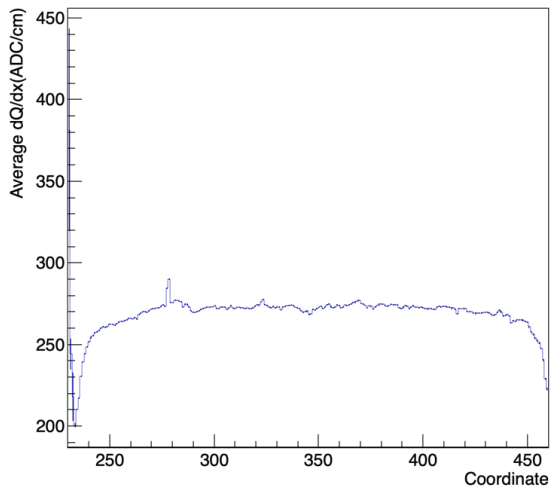
APA: 2 Signal plane: V Binned in U



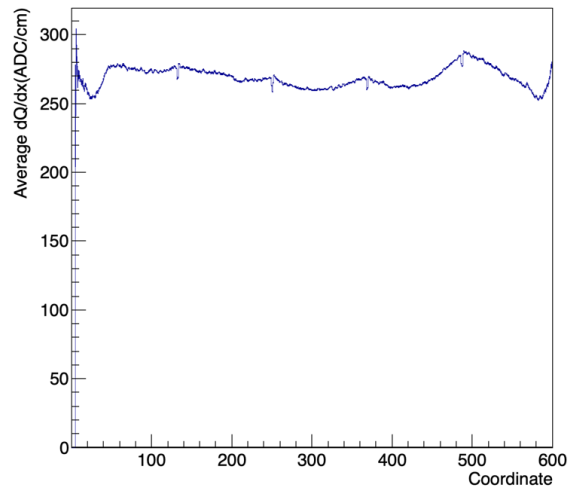
APA: 2 Signal plane: V Binned in V



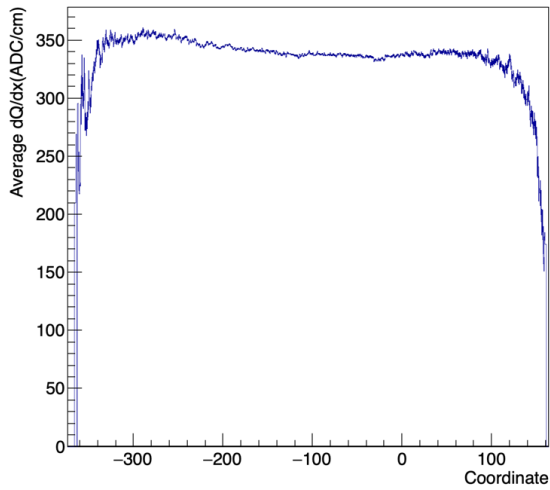
APA: 2 Signal plane: V Binned in Z



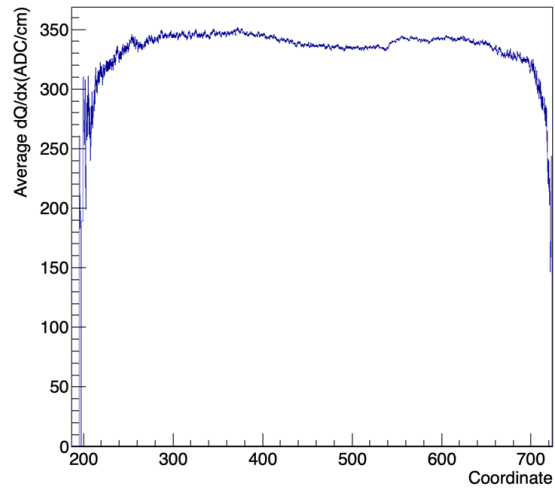
APA: 2 Signal plane: V Binned in Y



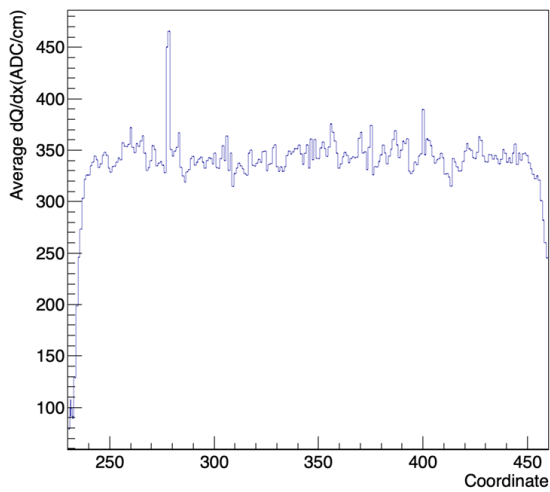
APA: 2 Signal plane: Z Binned in U



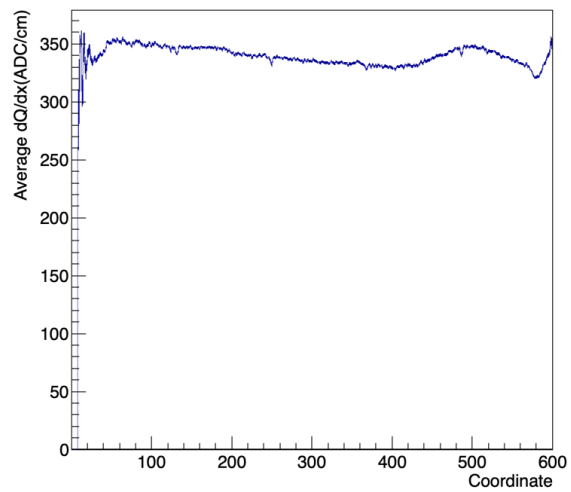
APA: 2 Signal plane: Z Binned in V



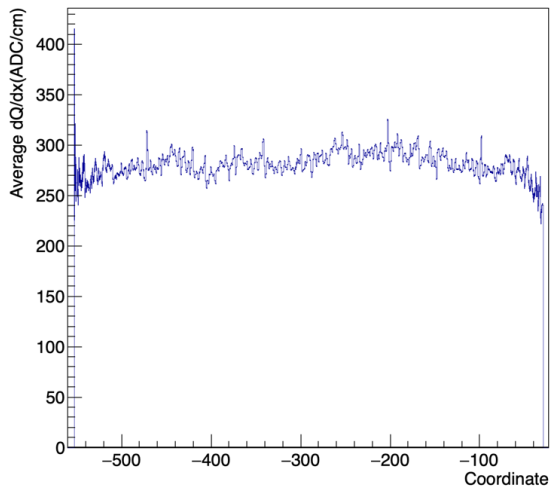
APA: 2 Signal plane: Z Binned in Z



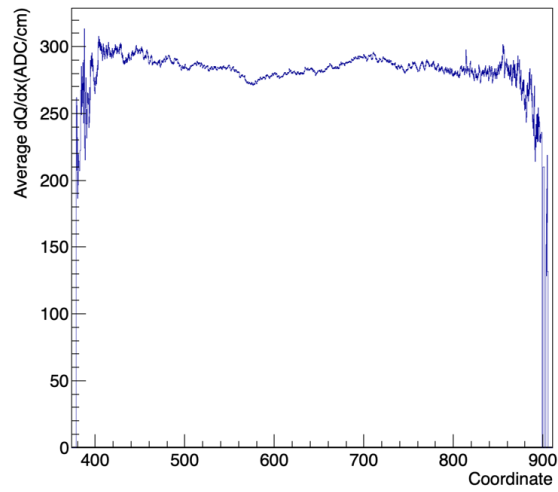
APA: 2 Signal plane: Z Binned in Y



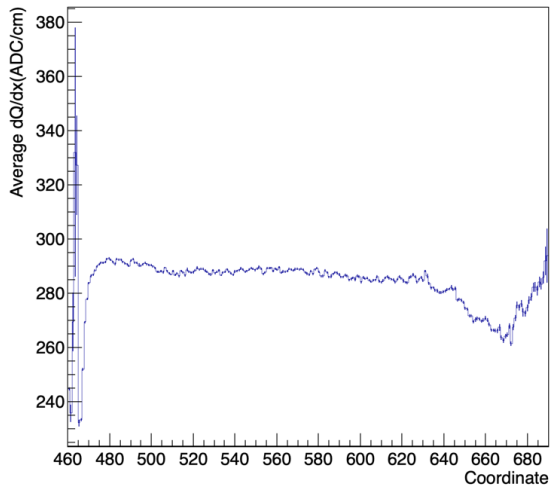
APA: 1 Signal plane: U Binned in U



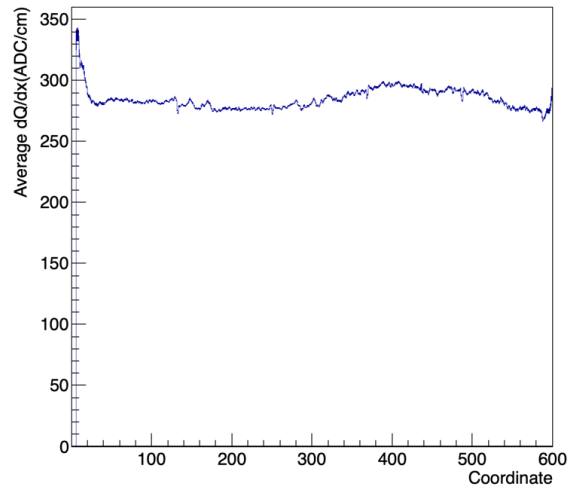
APA: 1 Signal plane: U Binned in V



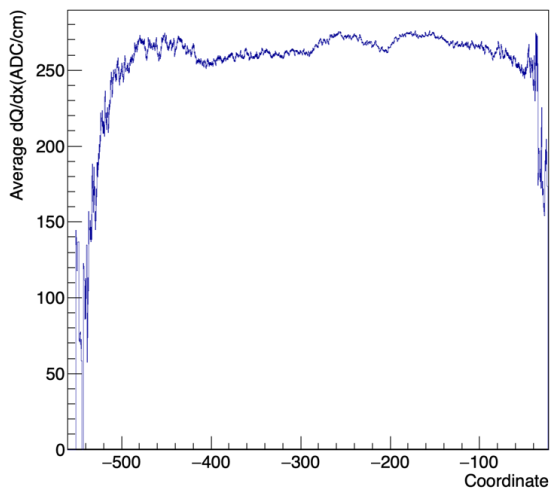
APA: 1 Signal plane: U Binned in Z



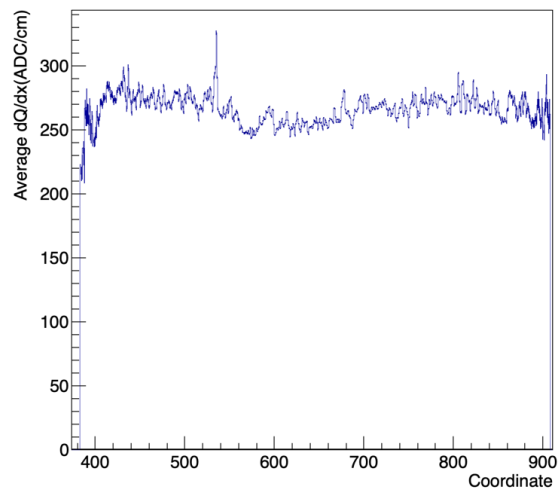
APA: 1 Signal plane: U Binned in Y



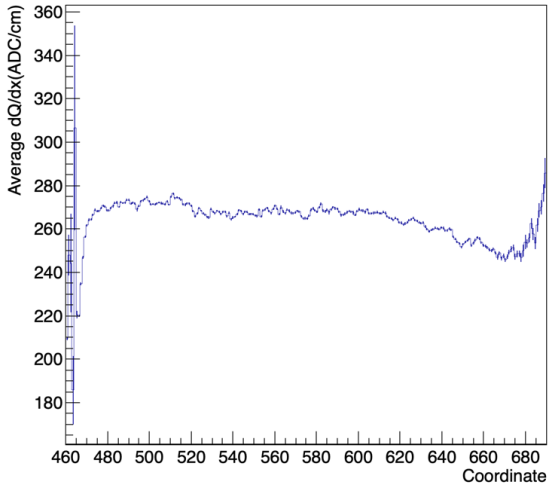
APA: 1 Signal plane: V Binned in U



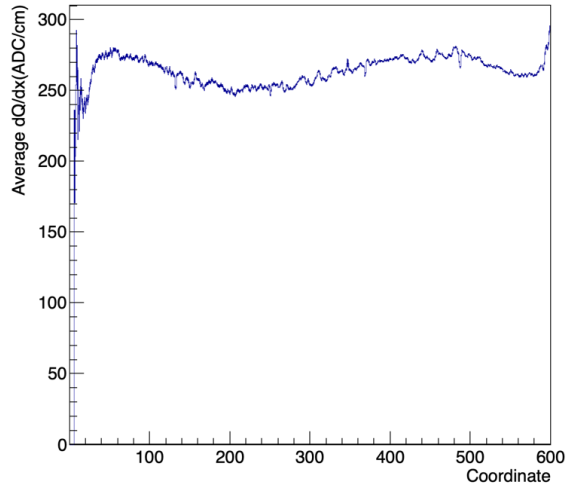
APA: 1 Signal plane: V Binned in V



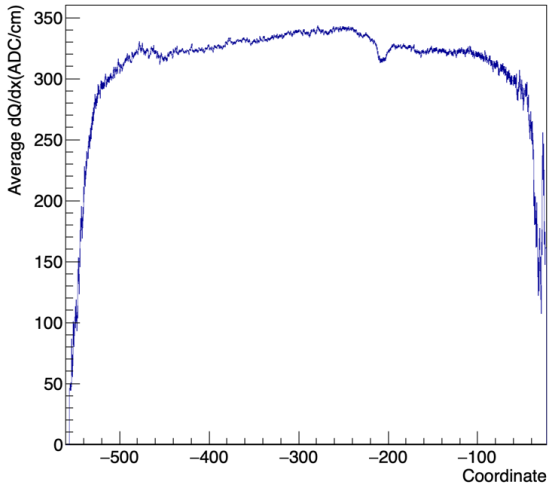
APA: 1 Signal plane: V Binned in Z



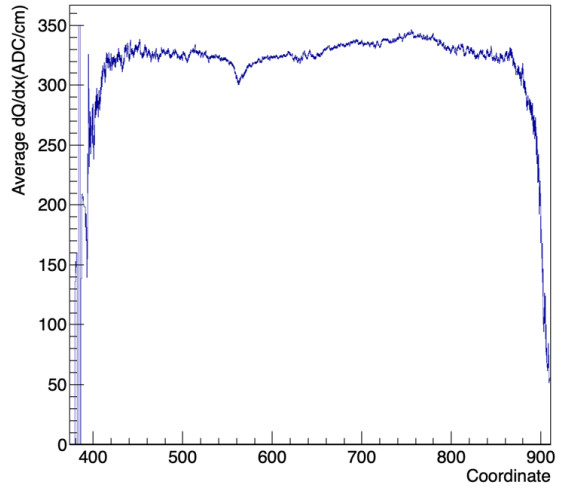
APA: 1 Signal plane: V Binned in Y



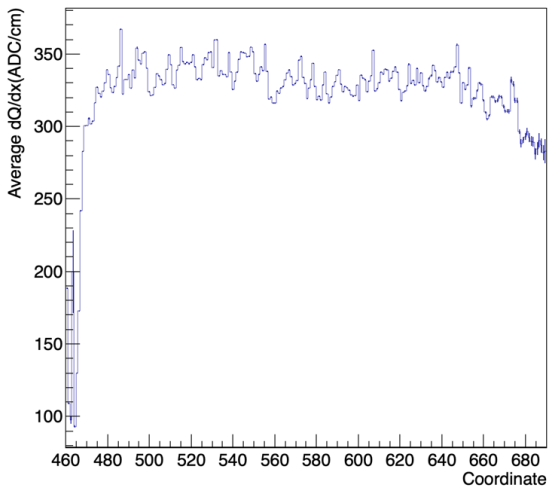
APA: 1 Signal plane: Z Binned in U



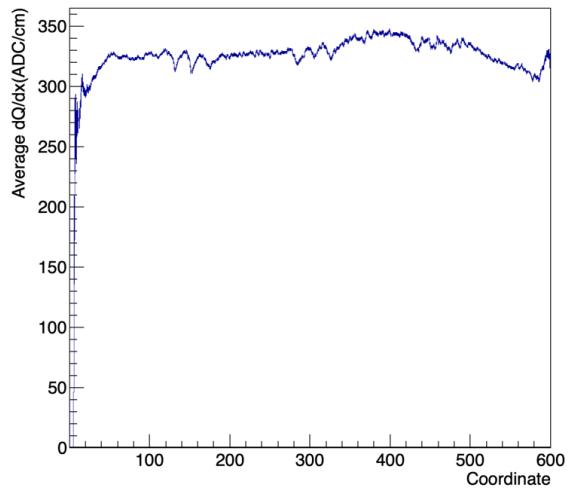
APA: 1 Signal plane: Z Binned in V



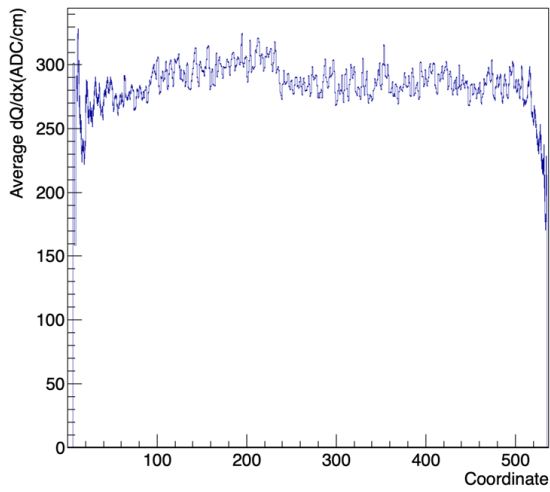
APA: 1 Signal plane: Z Binned in Z



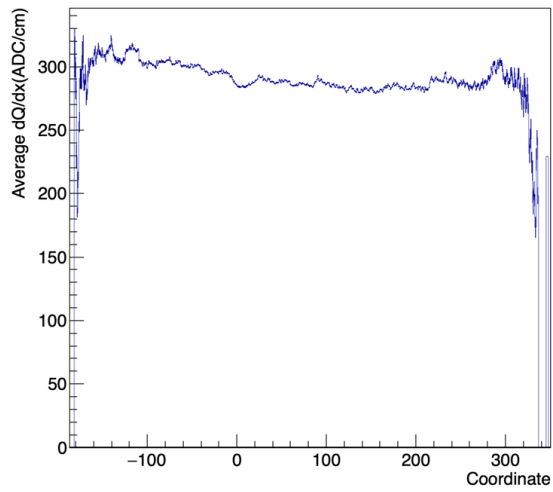
APA: 1 Signal plane: Z Binned in Y



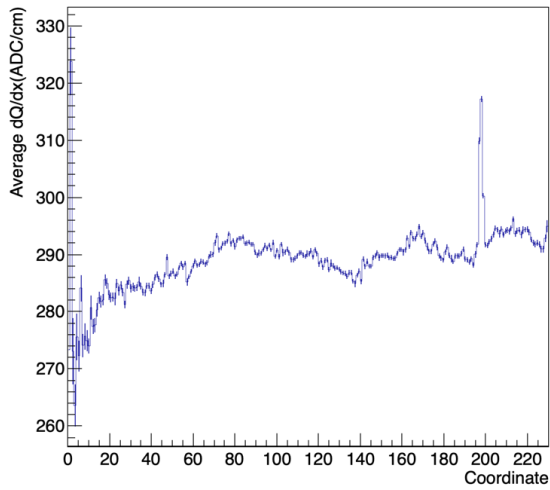
APA: 5 Signal plane: U Binned in U



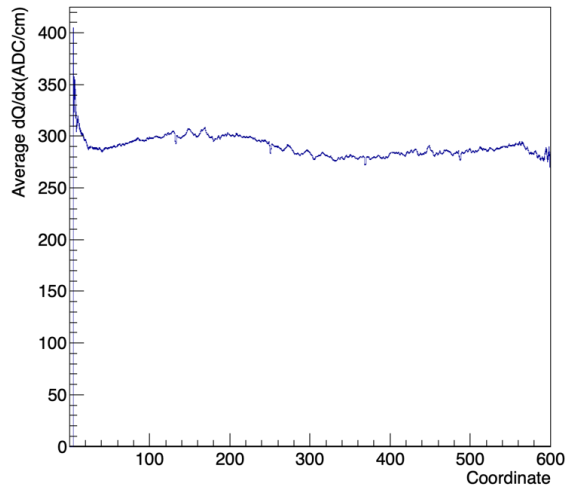
APA: 5 Signal plane: U Binned in V



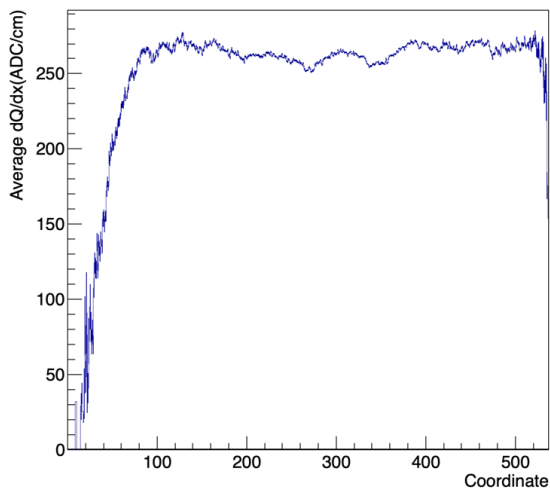
APA: 5 Signal plane: U Binned in Z



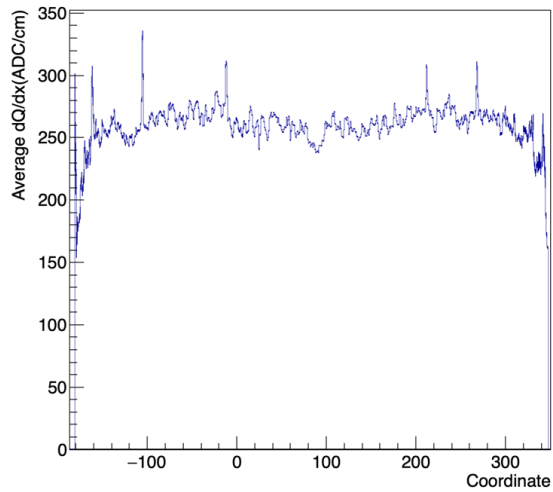
APA: 5 Signal plane: U Binned in Y



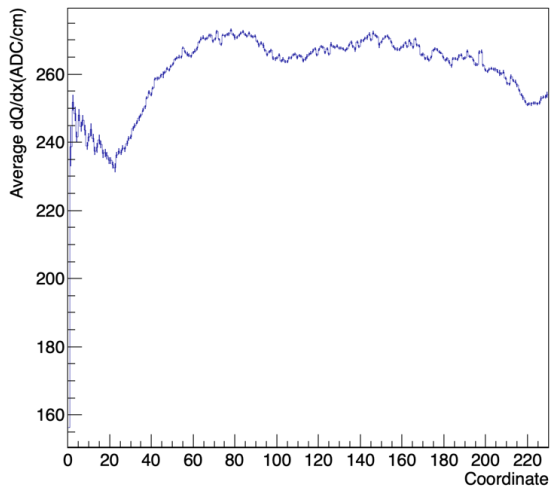
APA: 5 Signal plane: V Binned in U



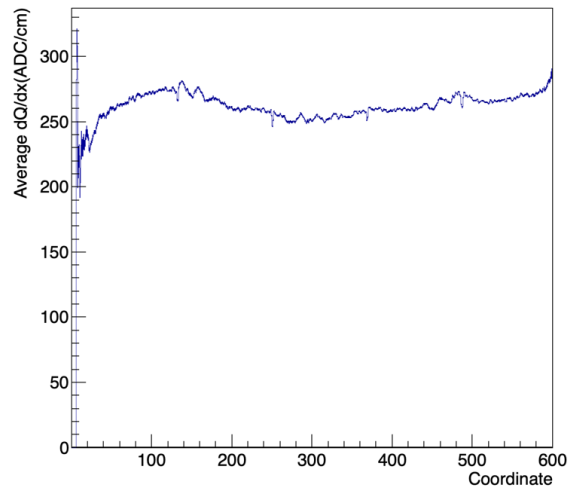
APA: 5 Signal plane: V Binned in V



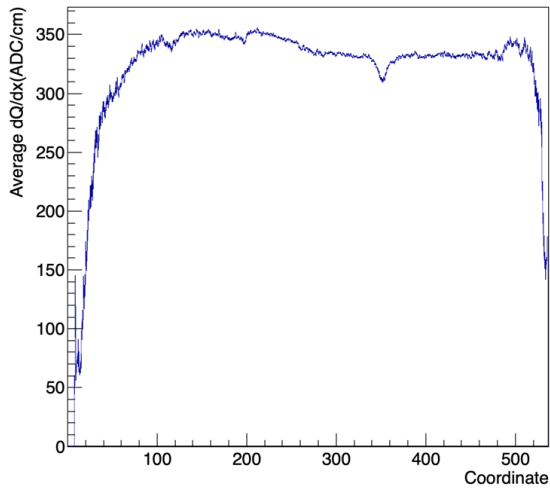
APA: 5 Signal plane: V Binned in Z



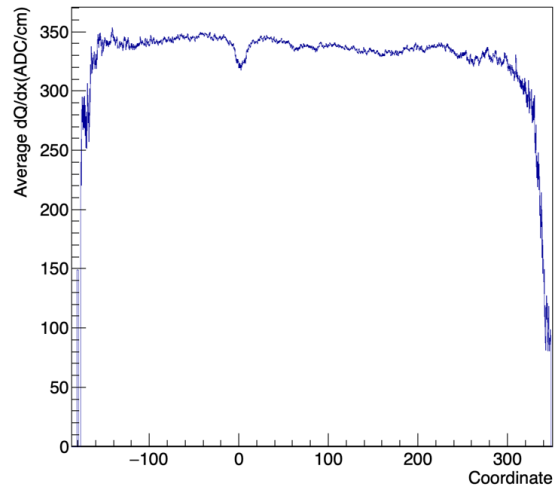
APA: 5 Signal plane: V Binned in Y



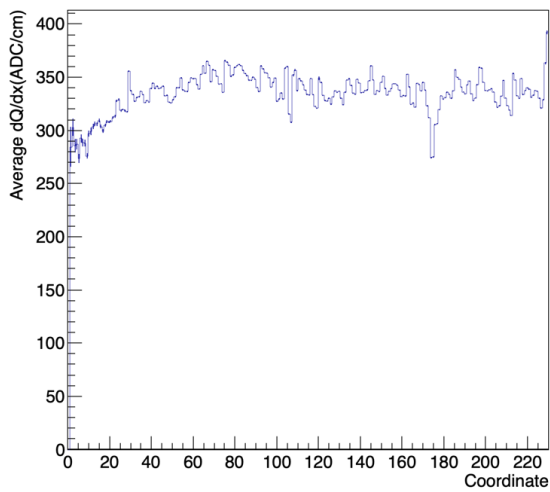
APA: 5 Signal plane: Z Binned in U



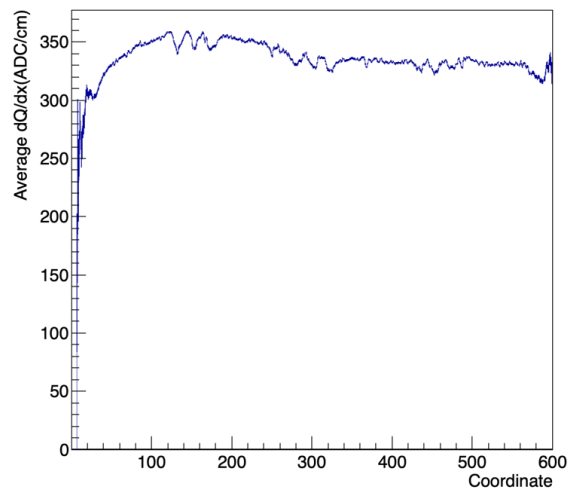
APA: 5 Signal plane: Z Binned in V



APA: 5 Signal plane: Z Binned in Z

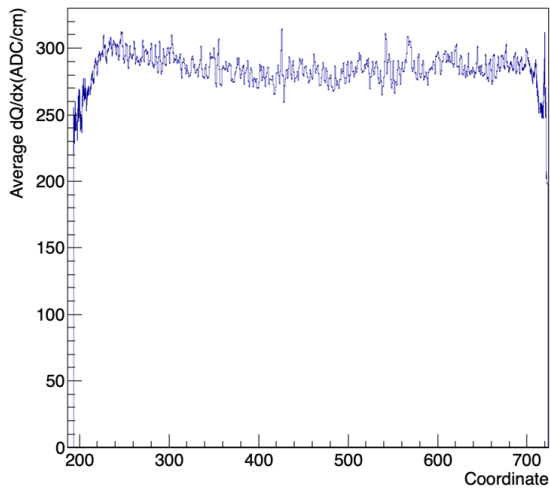


APA: 5 Signal plane: Z Binned in Y

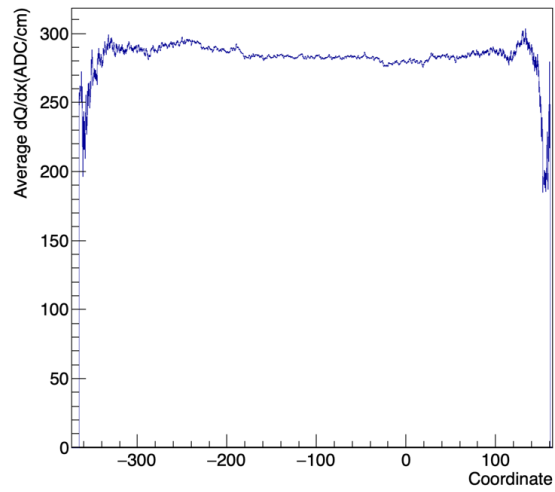




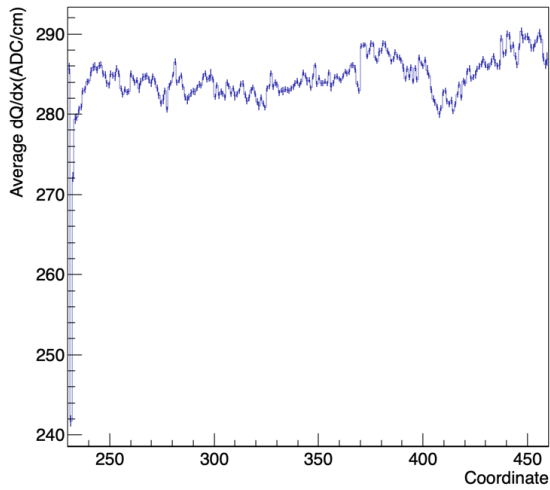
APA: 6 Signal plane: U Binned in U



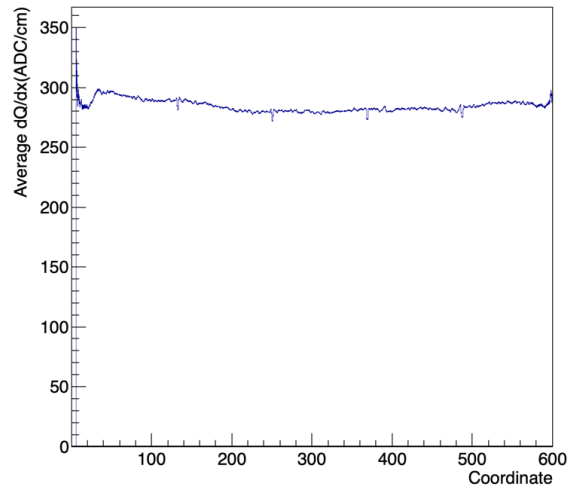
APA: 6 Signal plane: U Binned in V



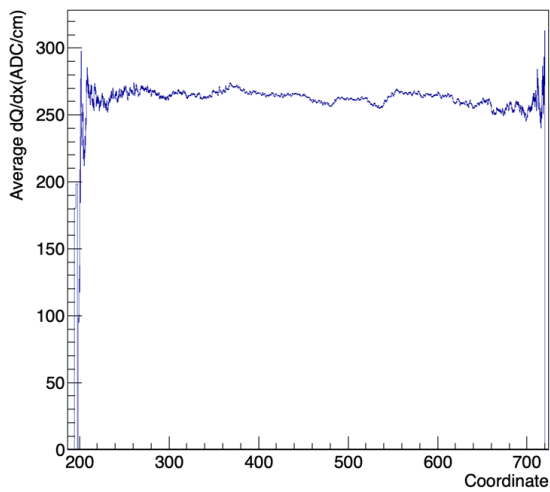
APA: 6 Signal plane: U Binned in Z



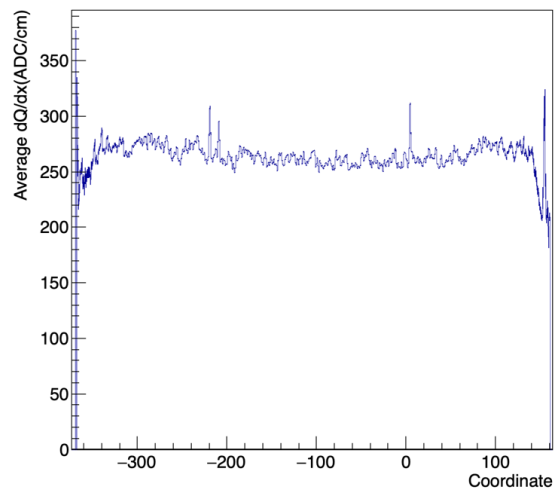
APA: 6 Signal plane: U Binned in Y



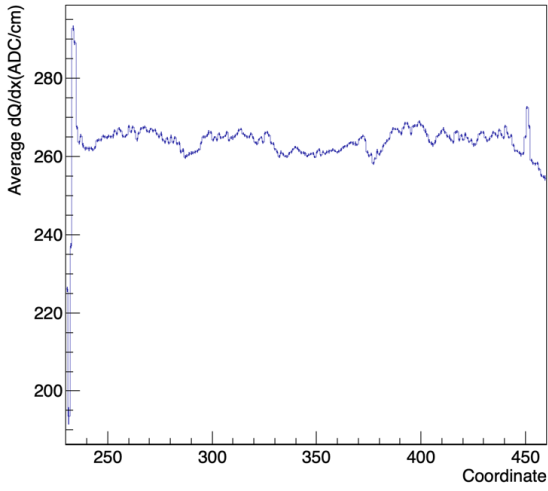
APA: 6 Signal plane: V Binned in U



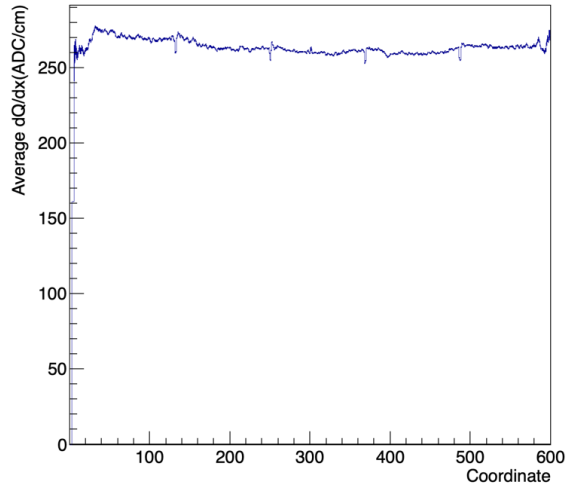
APA: 6 Signal plane: V Binned in V



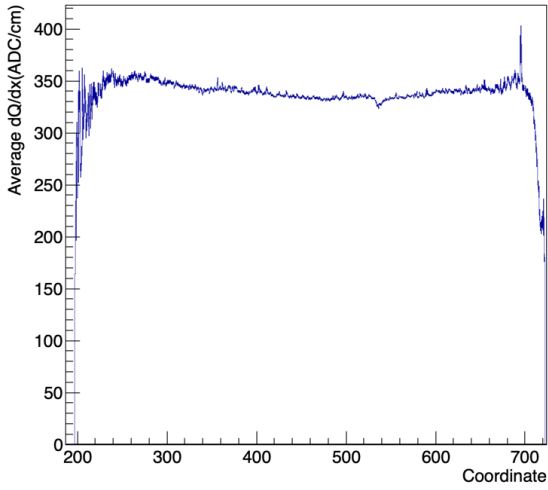
APA: 6 Signal plane: V Binned in Z



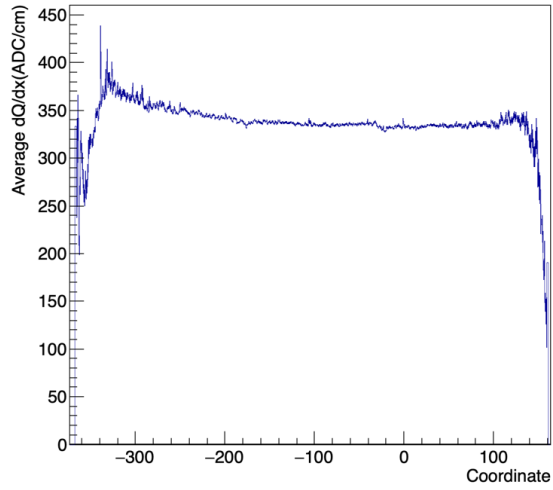
APA: 6 Signal plane: V Binned in Y



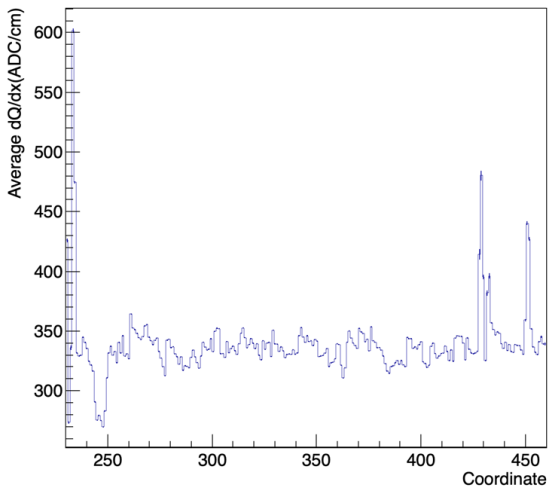
APA: 6 Signal plane: Z Binned in U



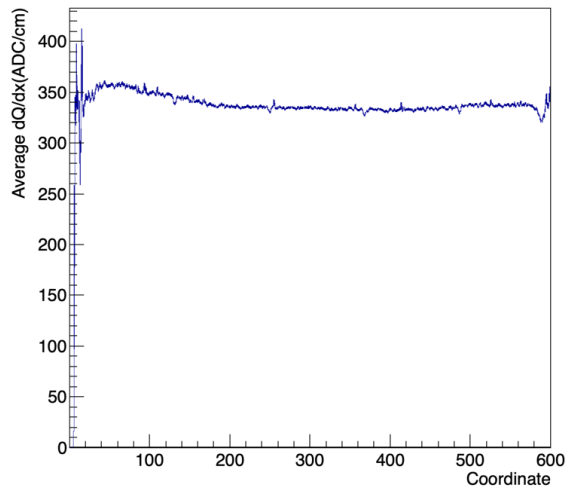
APA: 6 Signal plane: Z Binned in V



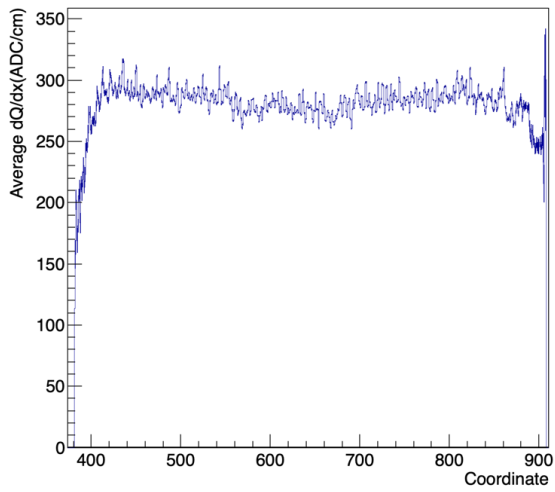
APA: 6 Signal plane: Z Binned in Z



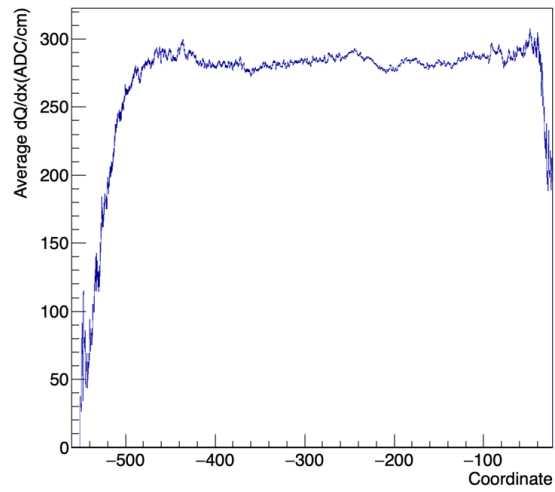
APA: 6 Signal plane: Z Binned in Y



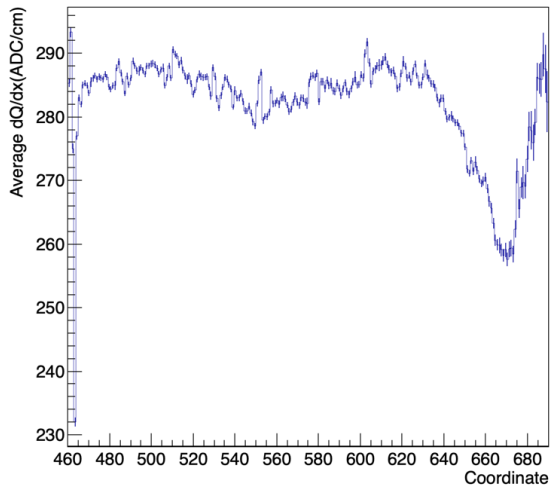
APA: 4 Signal plane: U Binned in U



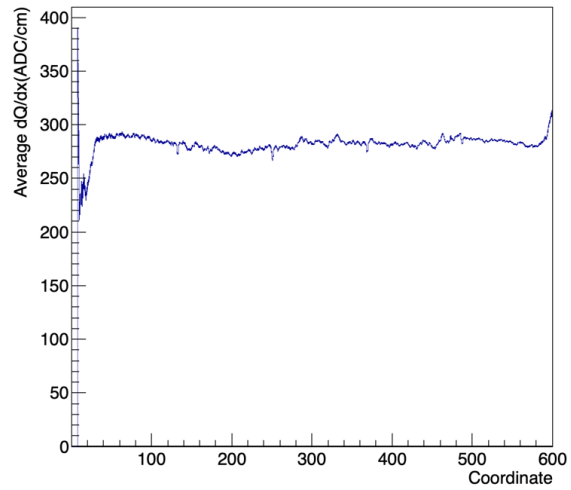
APA: 4 Signal plane: U Binned in V



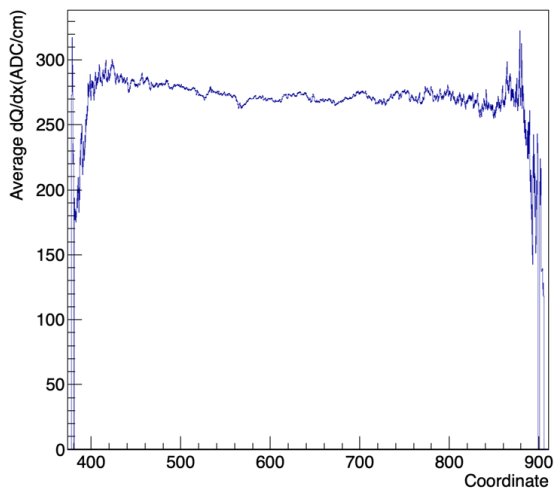
APA: 4 Signal plane: U Binned in Z



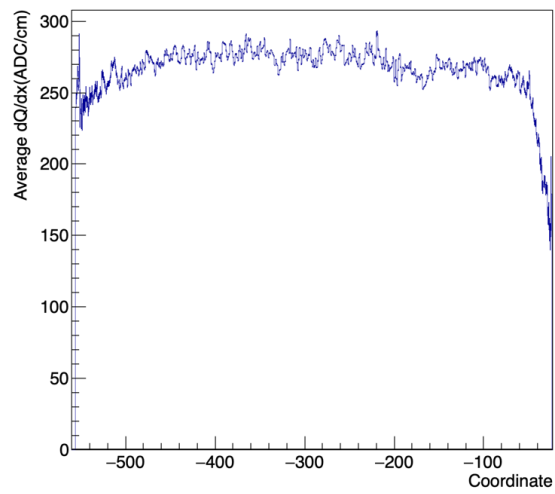
APA: 4 Signal plane: U Binned in Y



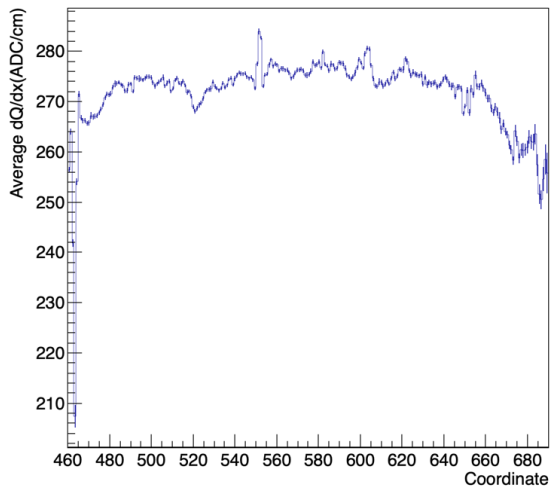
APA: 4 Signal plane: V Binned in U



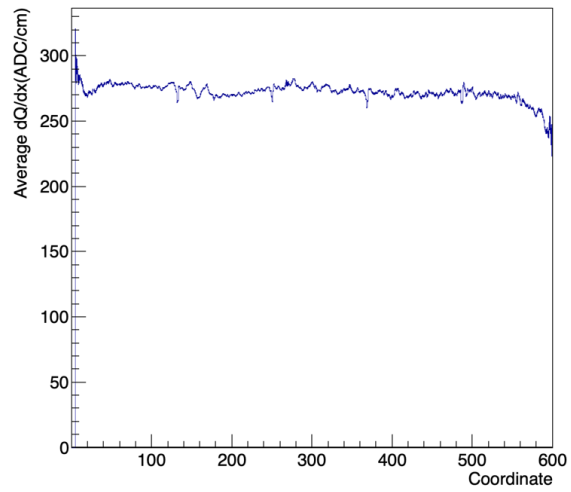
APA: 4 Signal plane: V Binned in V



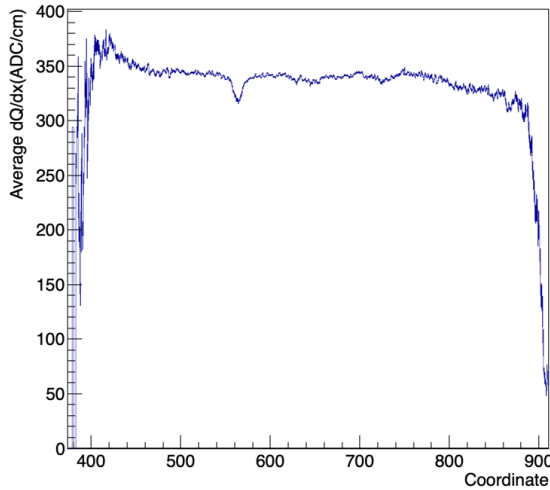
APA: 4 Signal plane: V Binned in Z



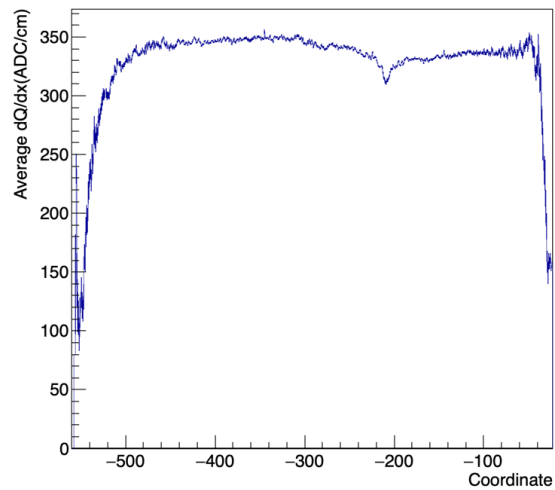
APA: 4 Signal plane: V Binned in Y



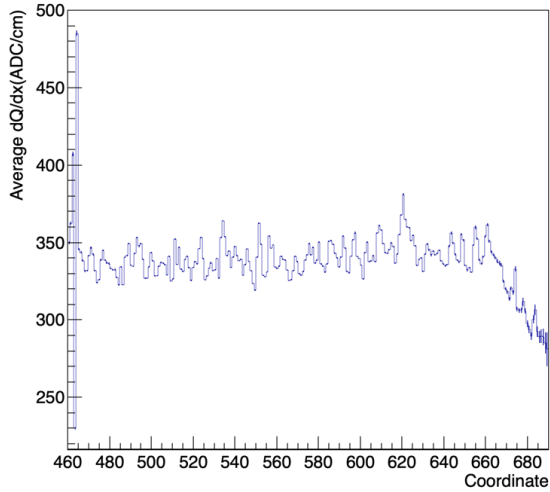
APA: 4 Signal plane: Z Binned in U



APA: 4 Signal plane: Z Binned in V



APA: 4 Signal plane: Z Binned in Z



APA: 4 Signal plane: Z Binned in Y

