

DCEM 1.3

Nov 29nd, 2023

Dante Totani

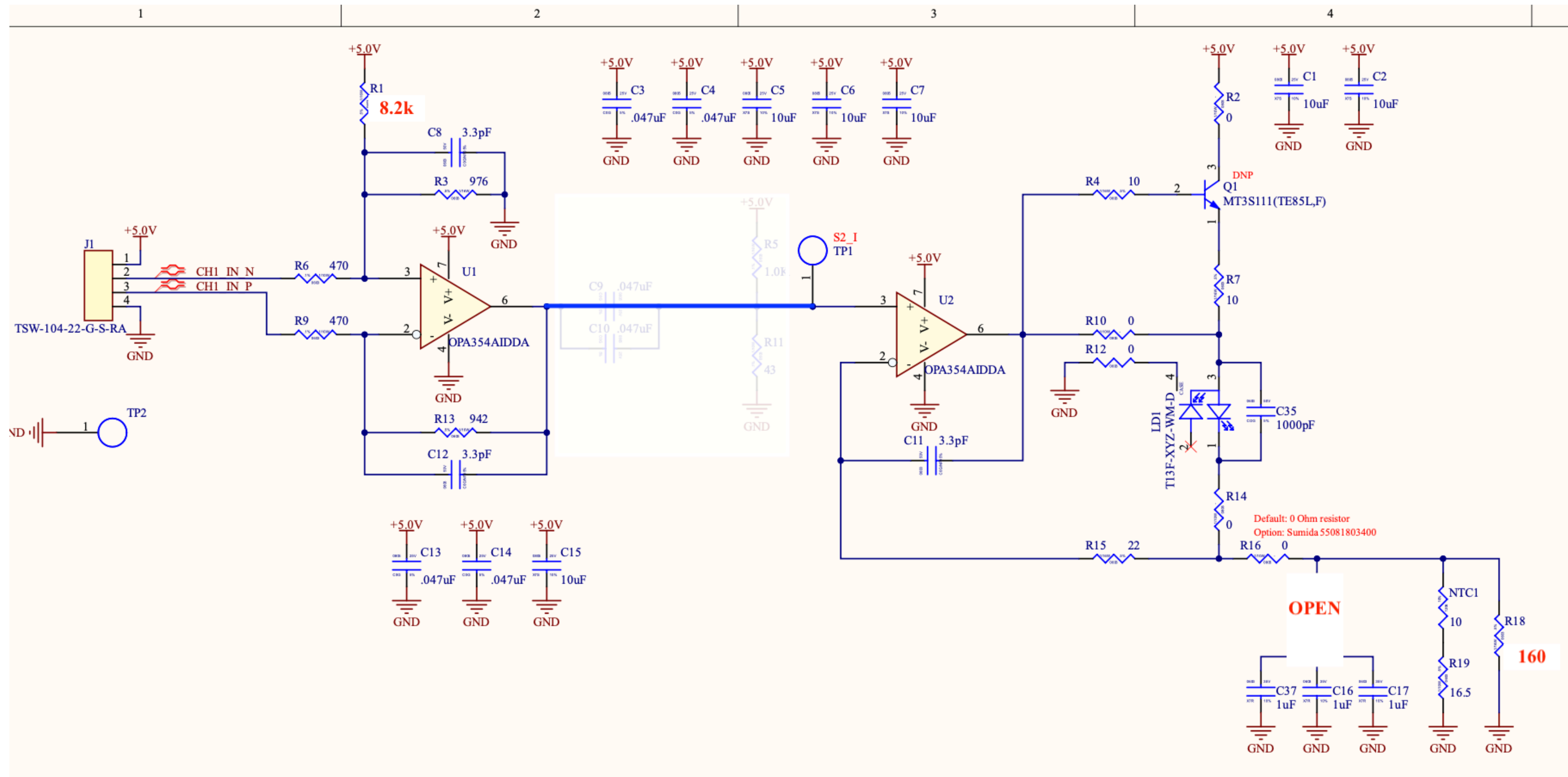
“Final” configuration:

Laser Output Load: R18 = 160 Ohm (only DC)

Offset 2nd OpAmp in: R1 = 8.2k Ohm

$$V_{offset} = 414.2 mV$$

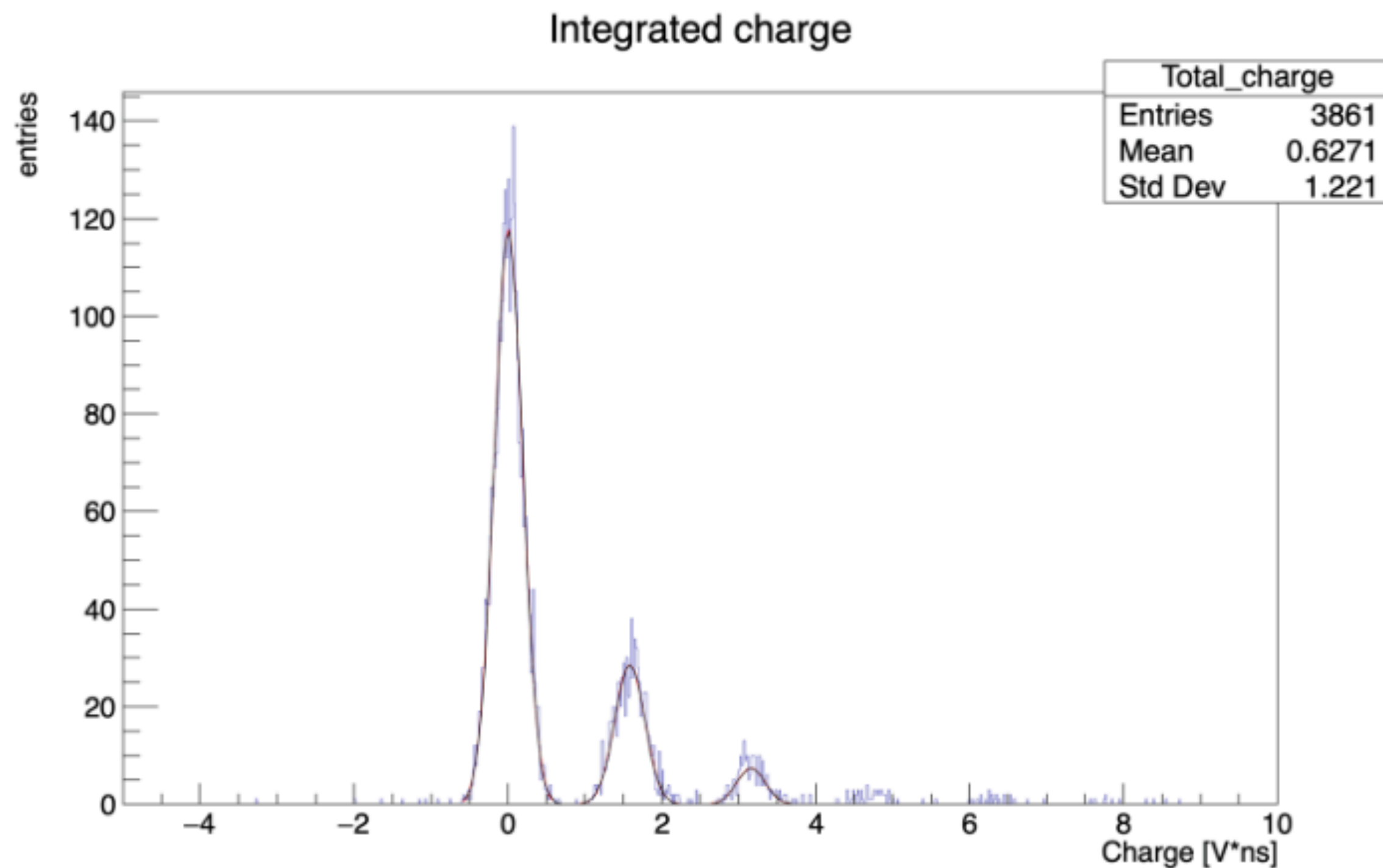
$$I_{laser} = 2.589 mA$$



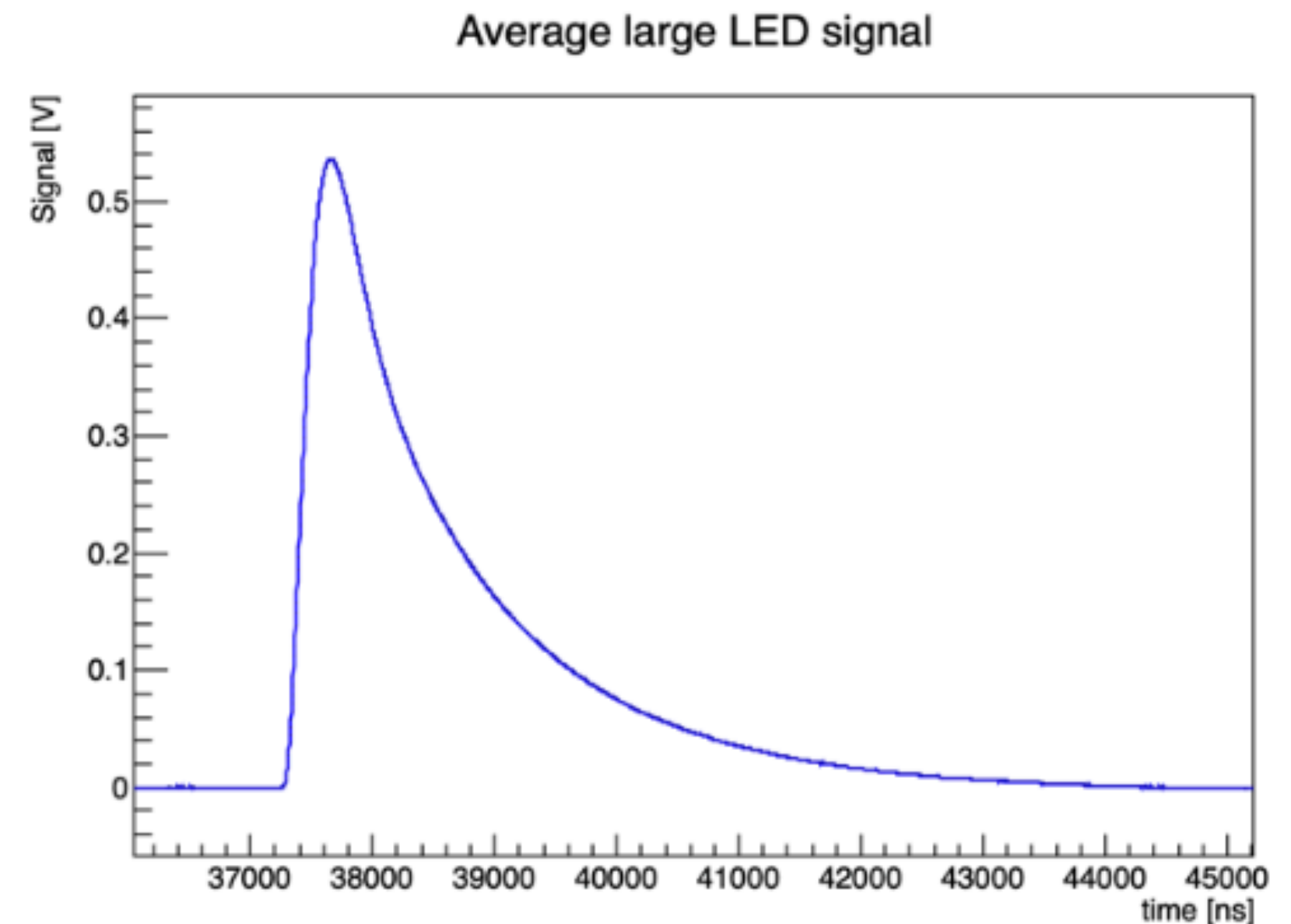
Config 11 board 2 ch 1 (Board2_dcem1p3_LAr_ch1_conf11_47p5v.txt)

SNR = 8.5; Offset = 0.105 V (mean)

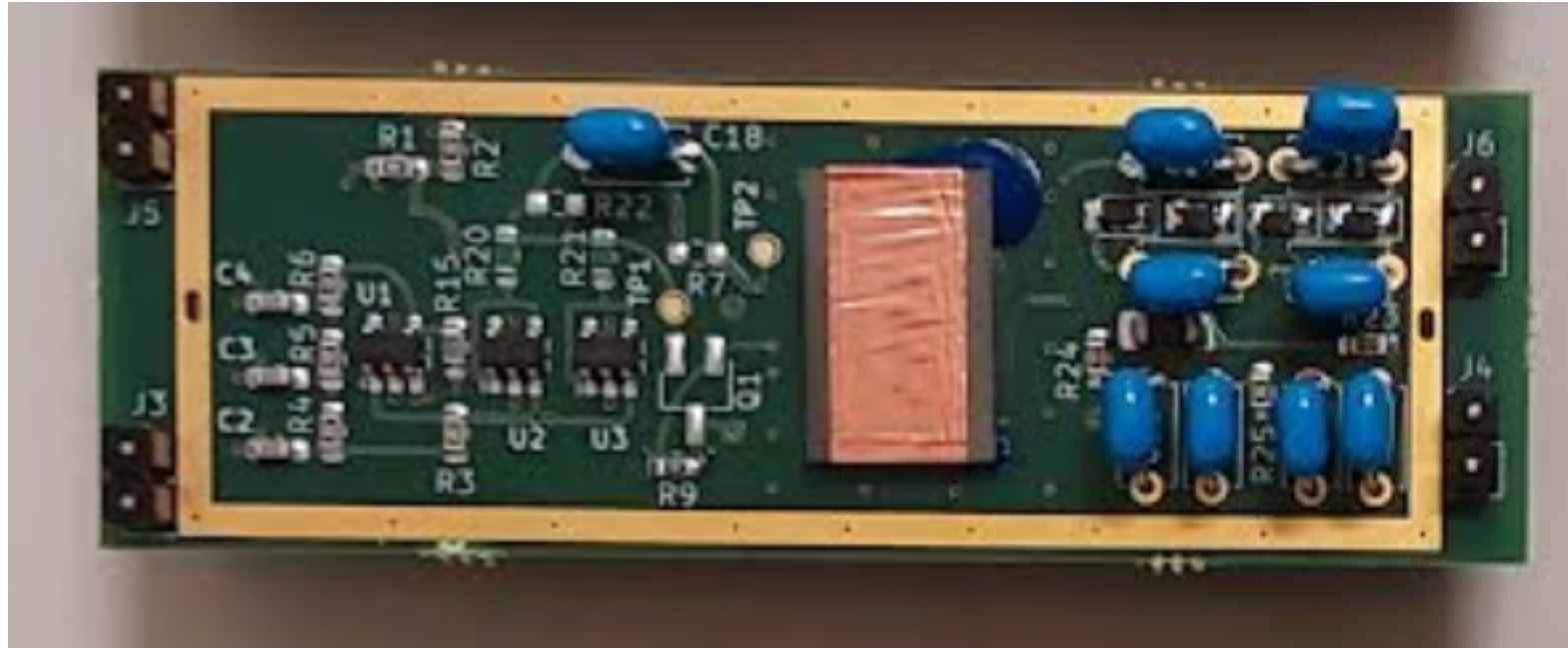
SPE amplitude = 1.10245×10^{-3} V, Gain = 1.58



Amplitude max = 1.60723V
Range = 1458
rise time 220ns decay time = 2636ns
undershoot fraction 0.845148 %



DCDC for HPK



DCDC SN09 4 flex connected

Room T

LAr

$V_{ldo_in} = 5.3\text{ V}$
 $I_{ldo_in} = 126\text{ mA}$

$V_{ldo_in} = 5.3\text{ V}$
 $I_{ldo_in} = 60\text{ mA}$

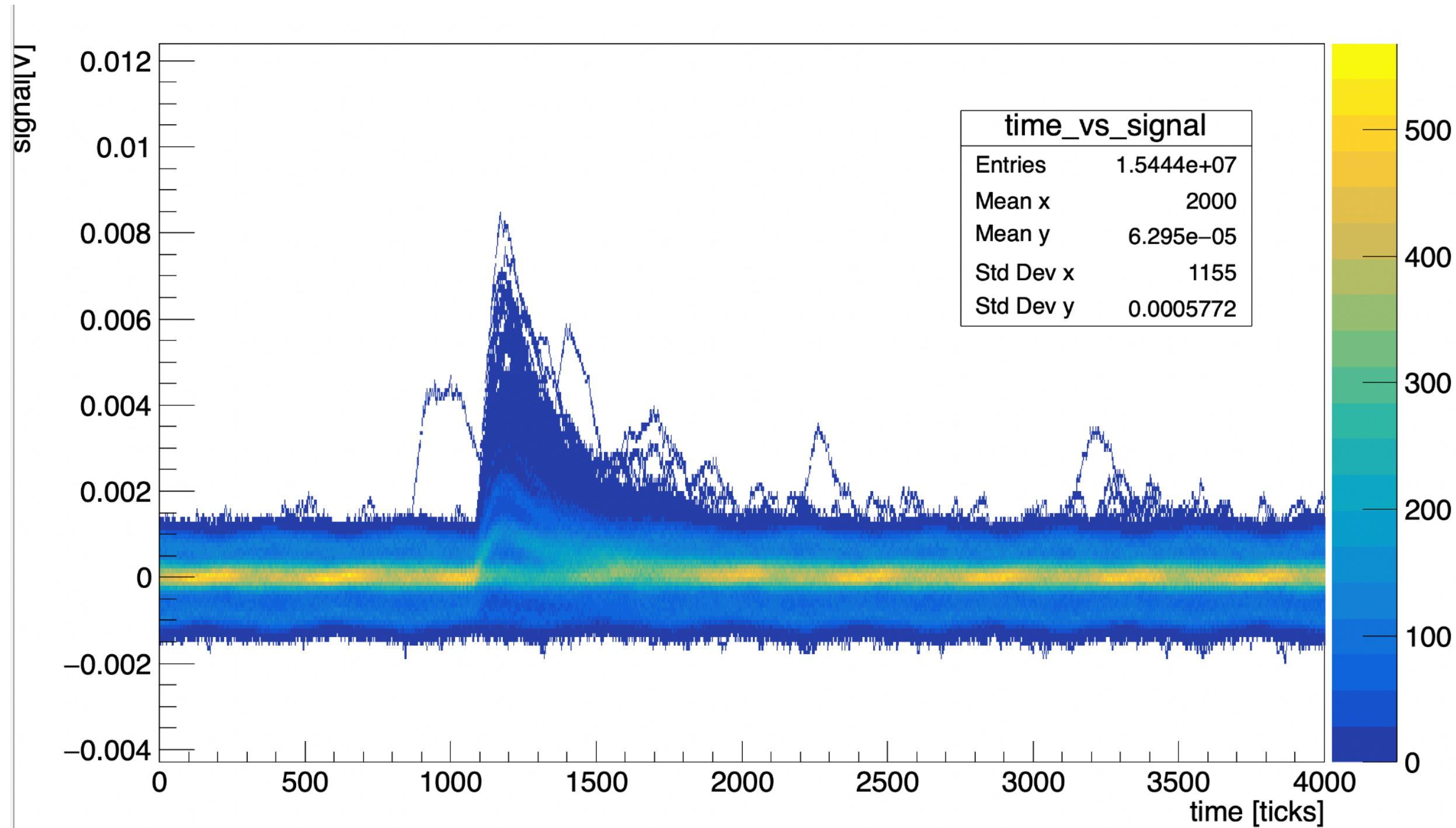
$V_{dcdc_out} = 52.9\text{ V}$

$V_{dcdc_out} = 47.1\text{ V}$

LED pulses well visible

LED pulses well visible

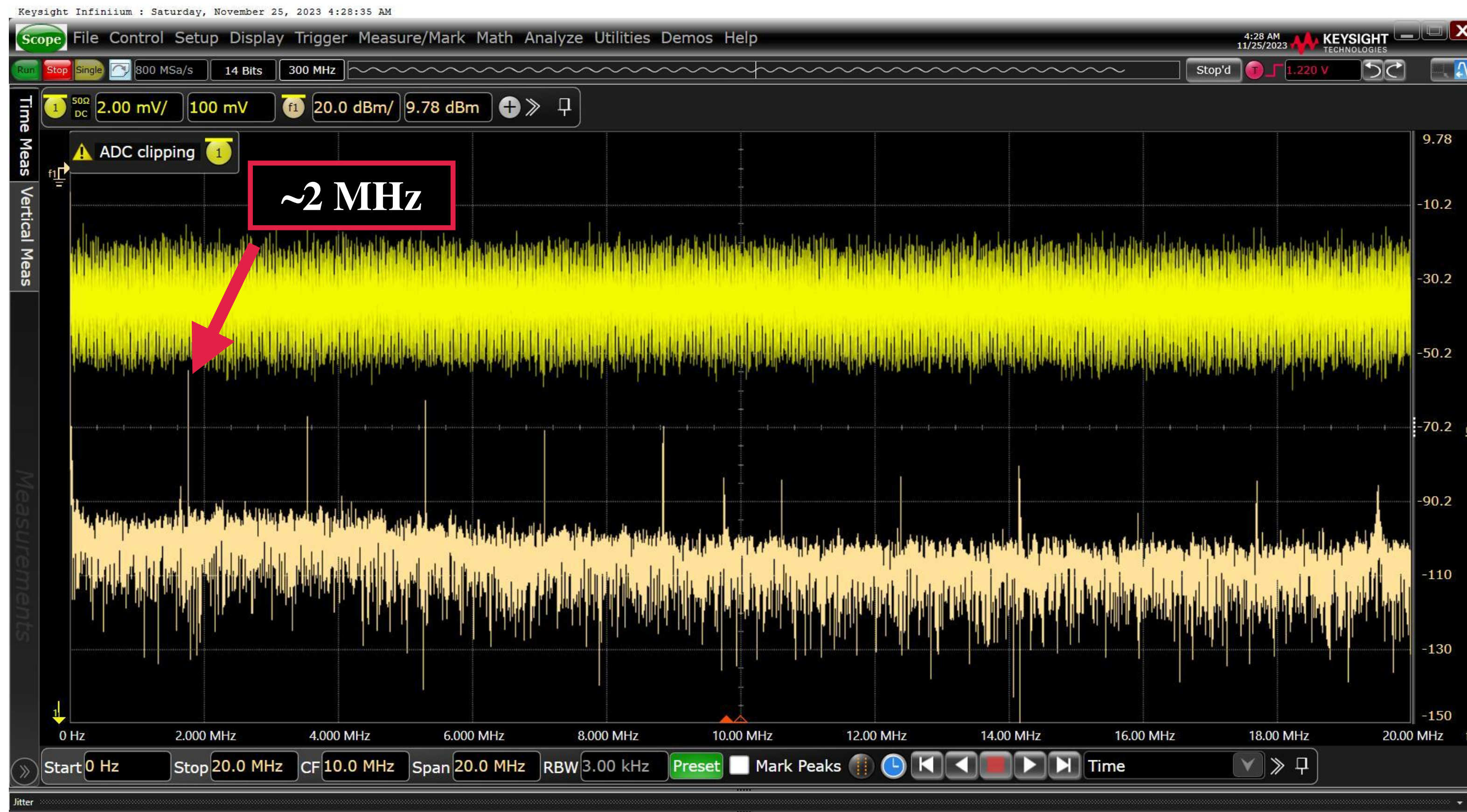
Noise $\sim 2\text{ Mhz}$

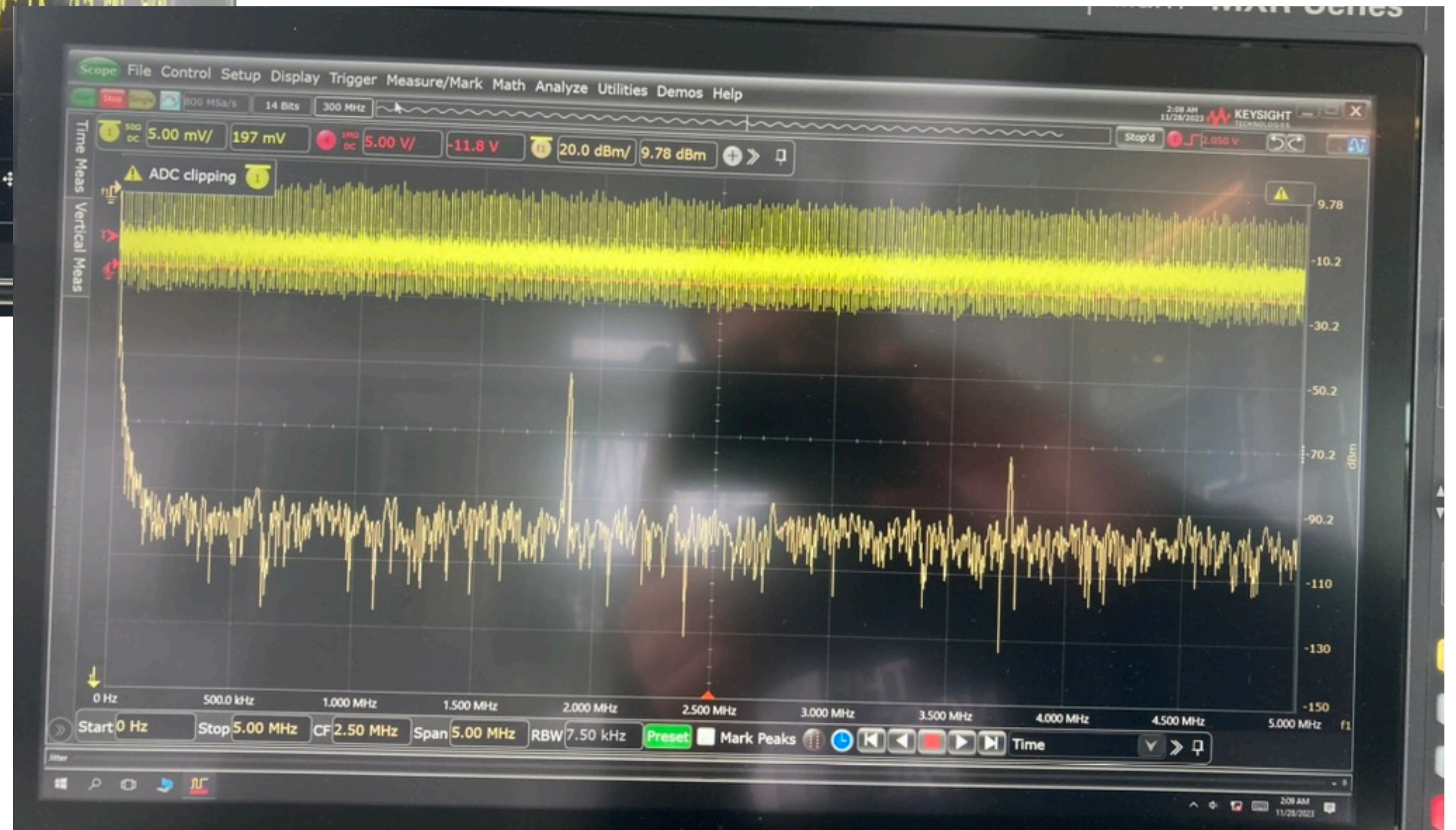
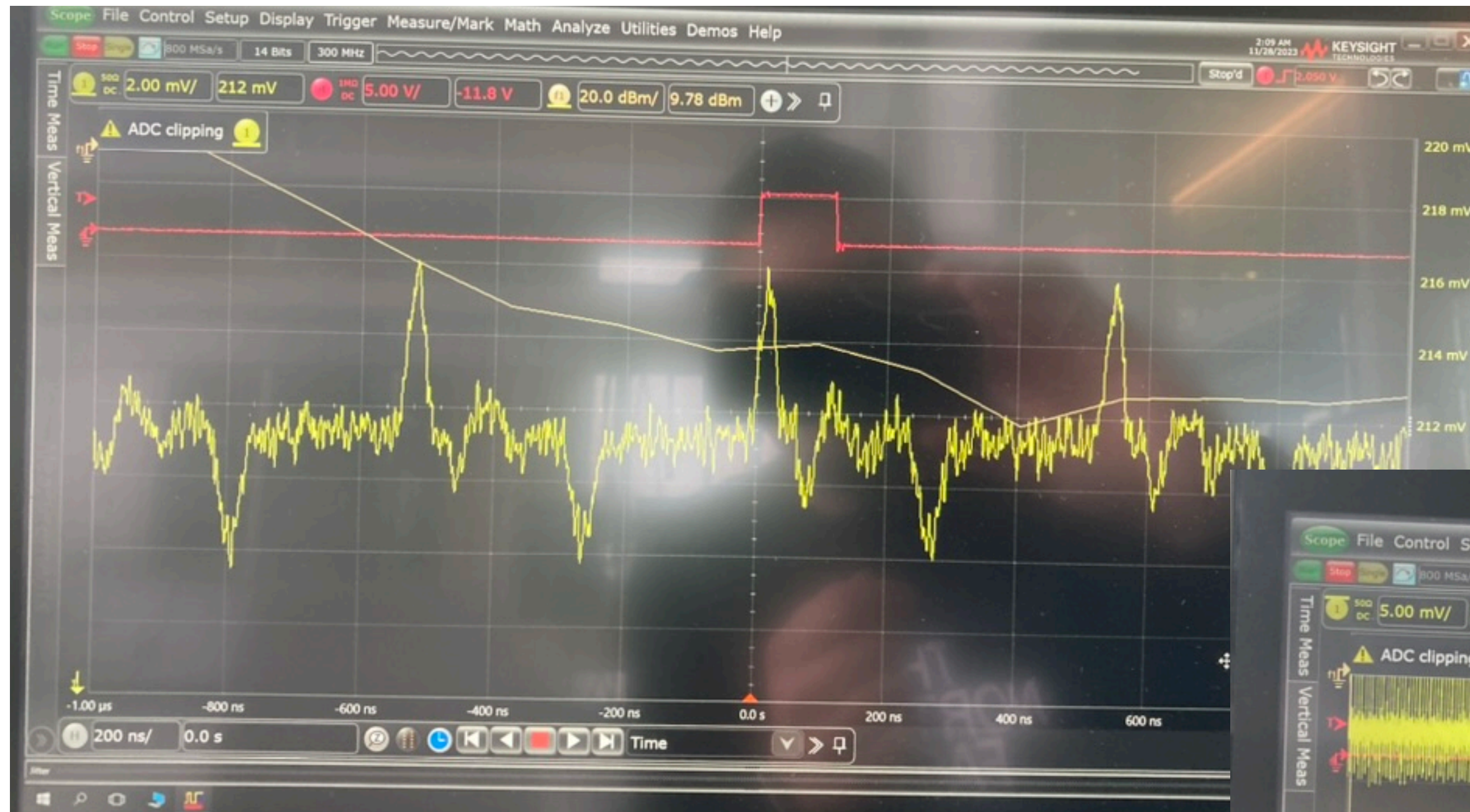


DCDC

A “large” ~2 MHz noise comes from the DCDC
Noise looks to be radiative:

- DCDC connected to DCEM through 1m cables, noise disappear
- Hands made shielding (copper tape) remove almost completely the noise.



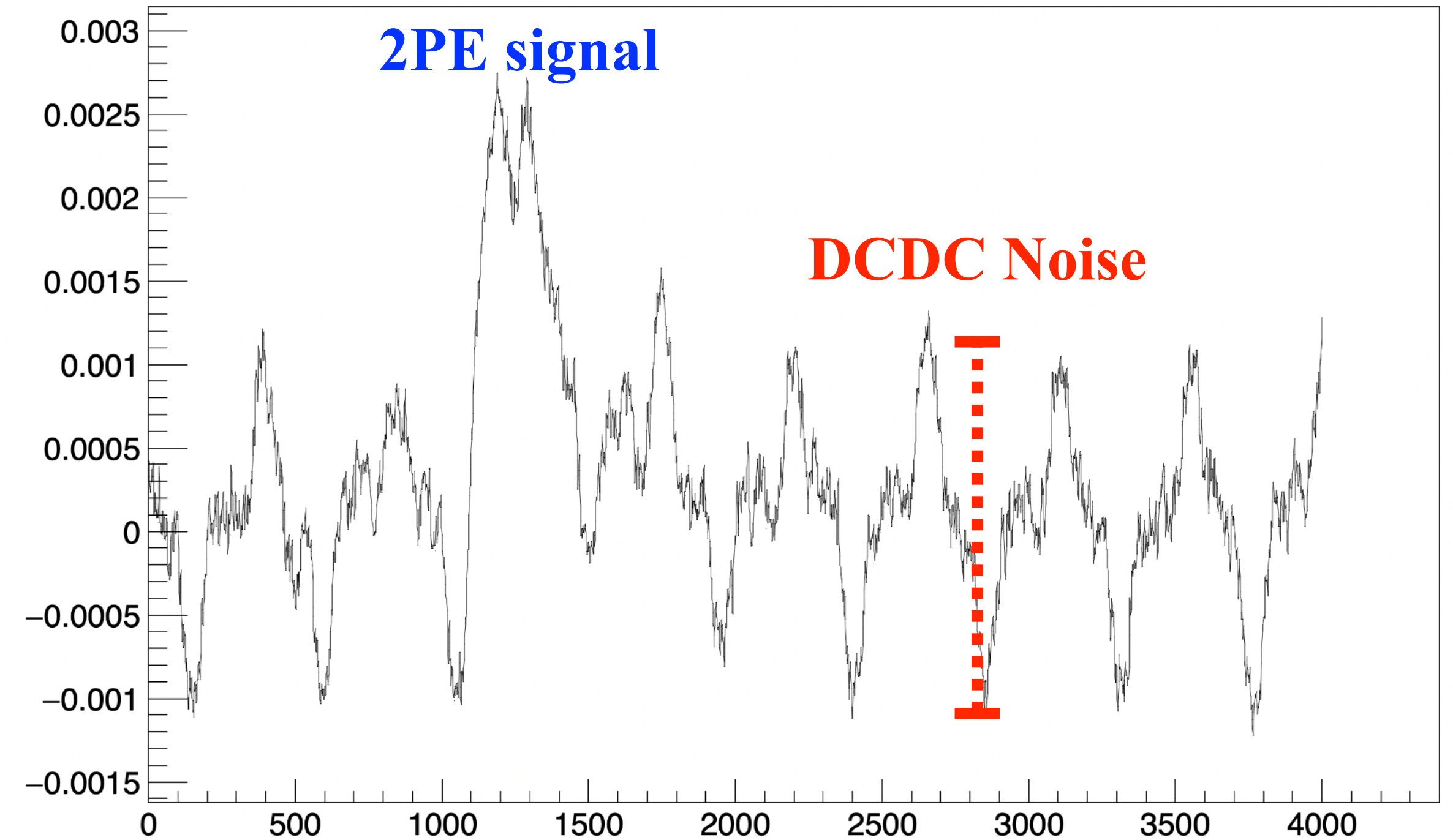


DCDC

- Noise itself is small but it is amplified tighter with the signal.
- Hard to see it looking only at the DCDC.
- Needs to be compared to SPE amplitude.

Noise at the Koheron output:

- Room T ~ 10 mV pkpk
 - LAr ~ 2 mV pkpk
- (Differences are due to different gain cold vs. warm, see NTC)



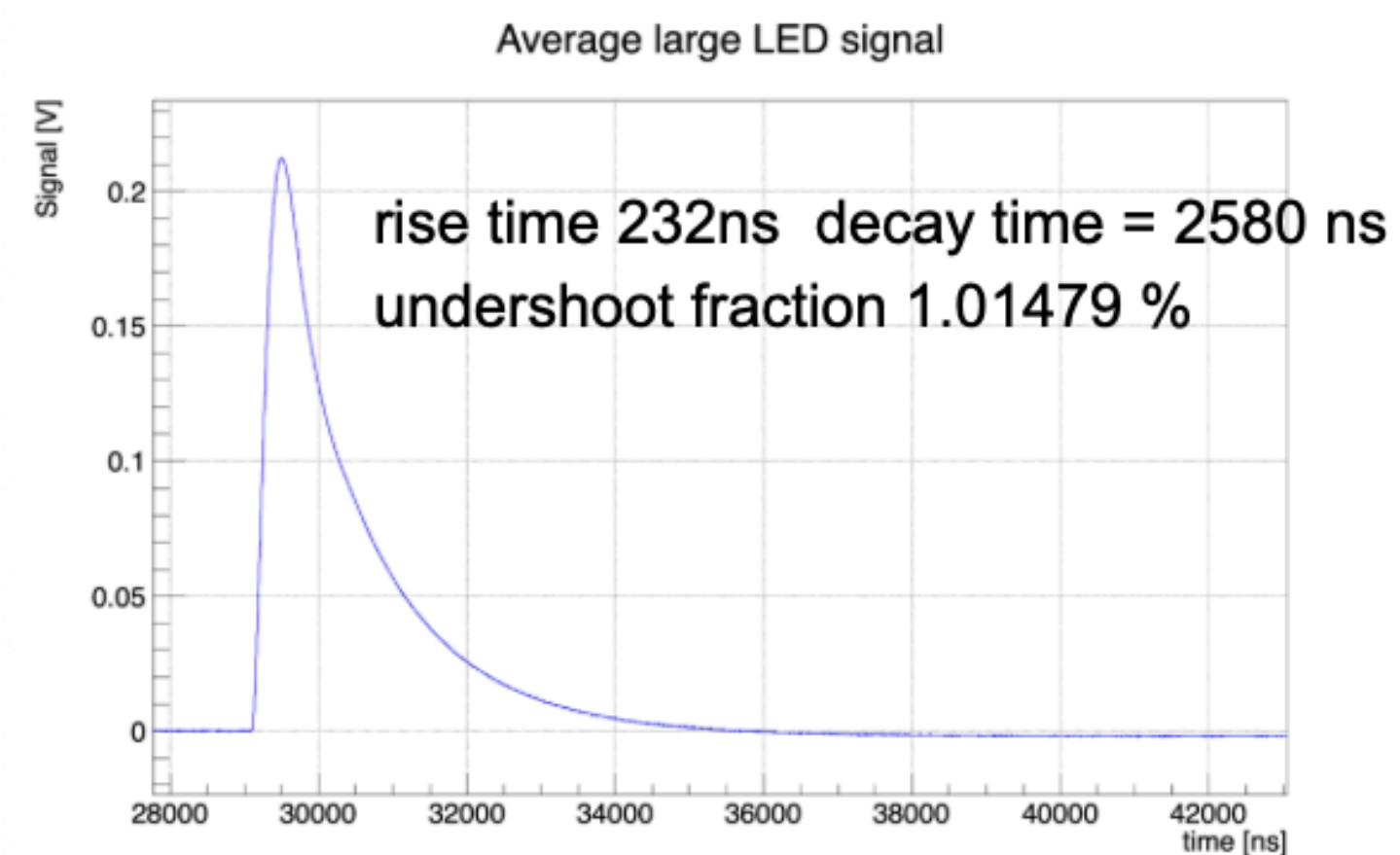
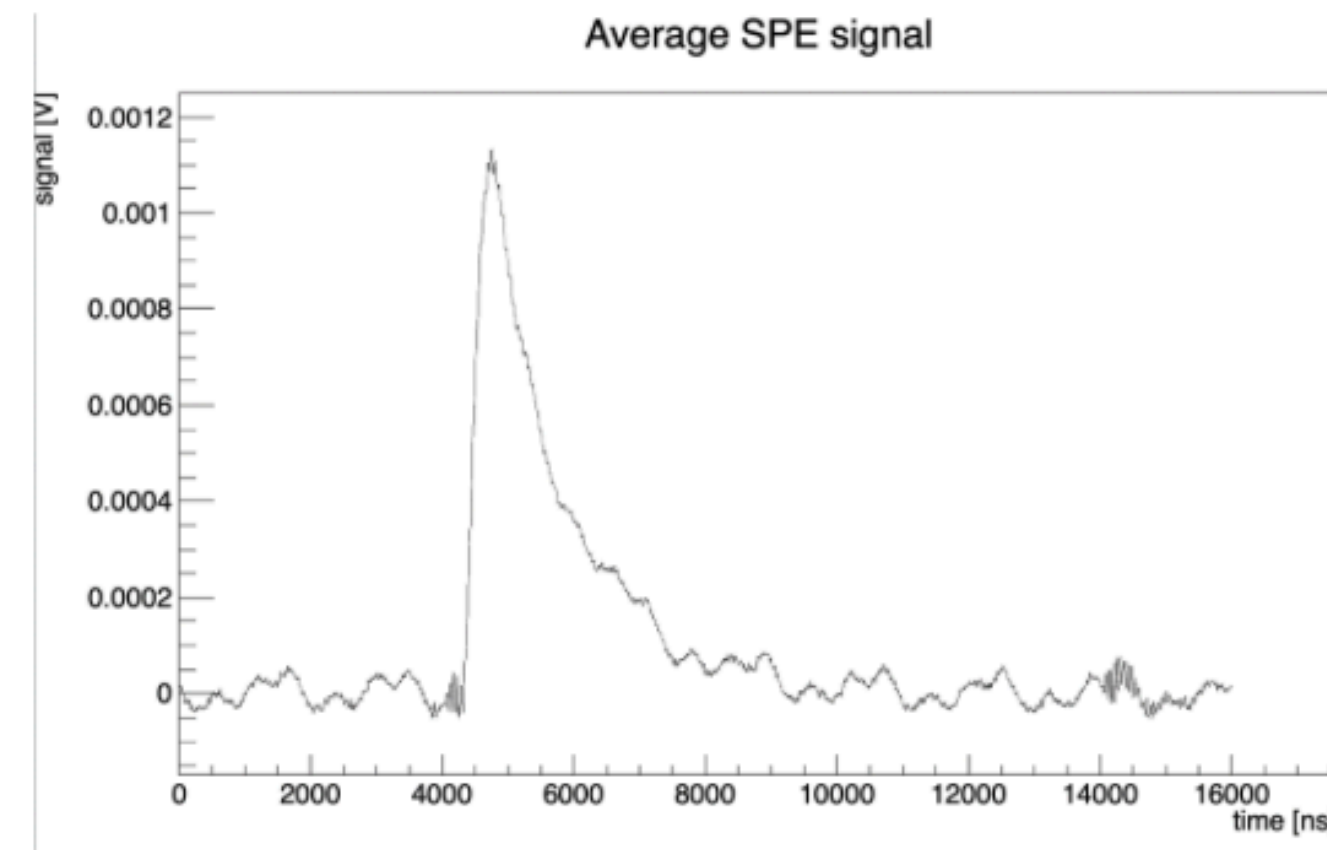
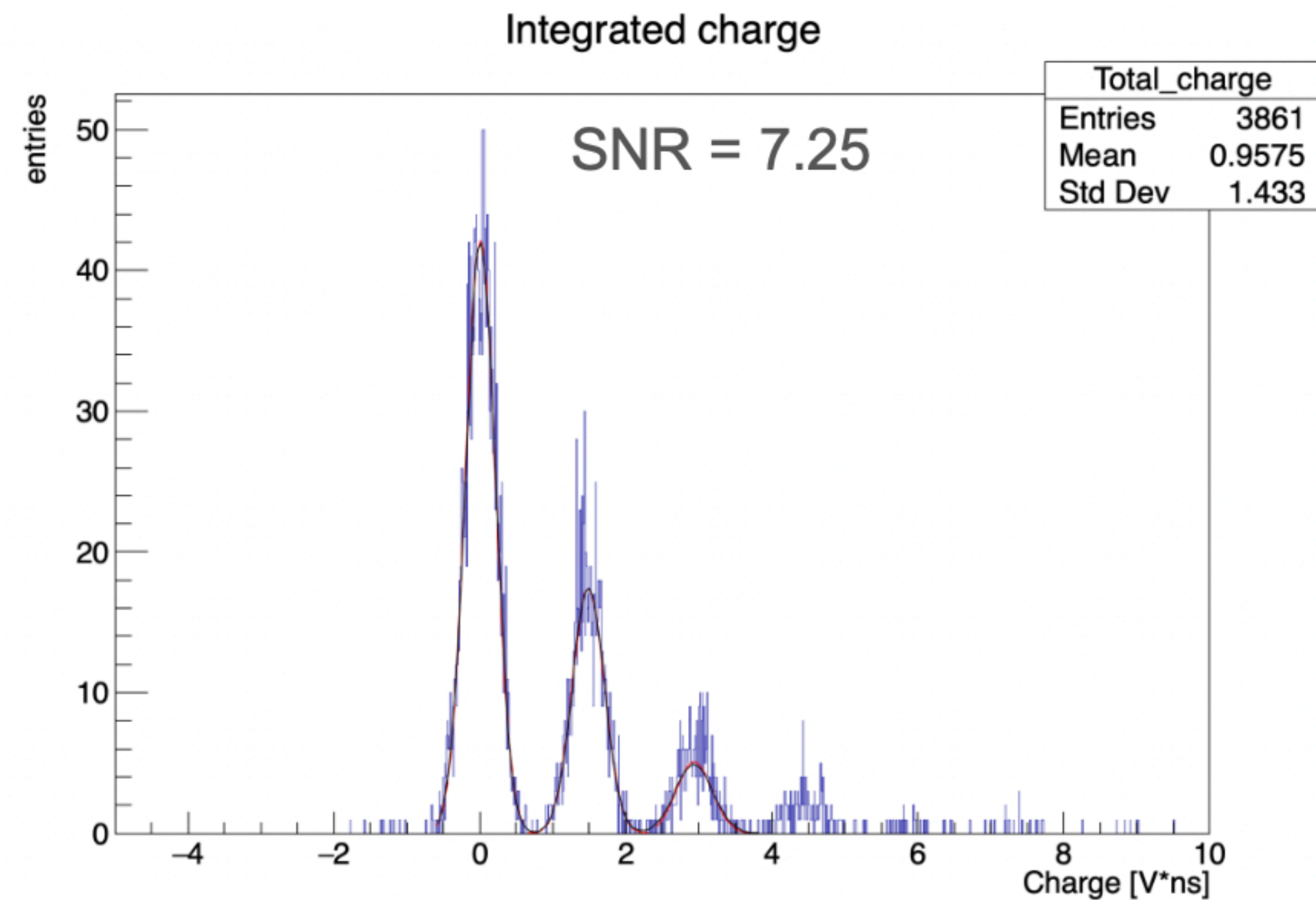
In LAr situation, the noise level is high compared to SPE amplitude:

Noise = 2mV pkpk,
SPE = 1.1mV

However the 2MHz frequency is fast enough to lie inside the signal integration window \rightarrow it is possible to recover the performances obtained with no DCDC even before including a shielding.

“Final” configuration + DCDC

Maximum amplitude = 1.60994 V
SPE amplitude = 1.13 mV
Range = 1424
Gain = 1.49



Note that the DCDC bias is 47.1V

The SNR~8 obtained with an external bias was for 47.5V.

A smaller SNR is expected independently from the noise introduced by the DCDC

DCDC dedicated shielding

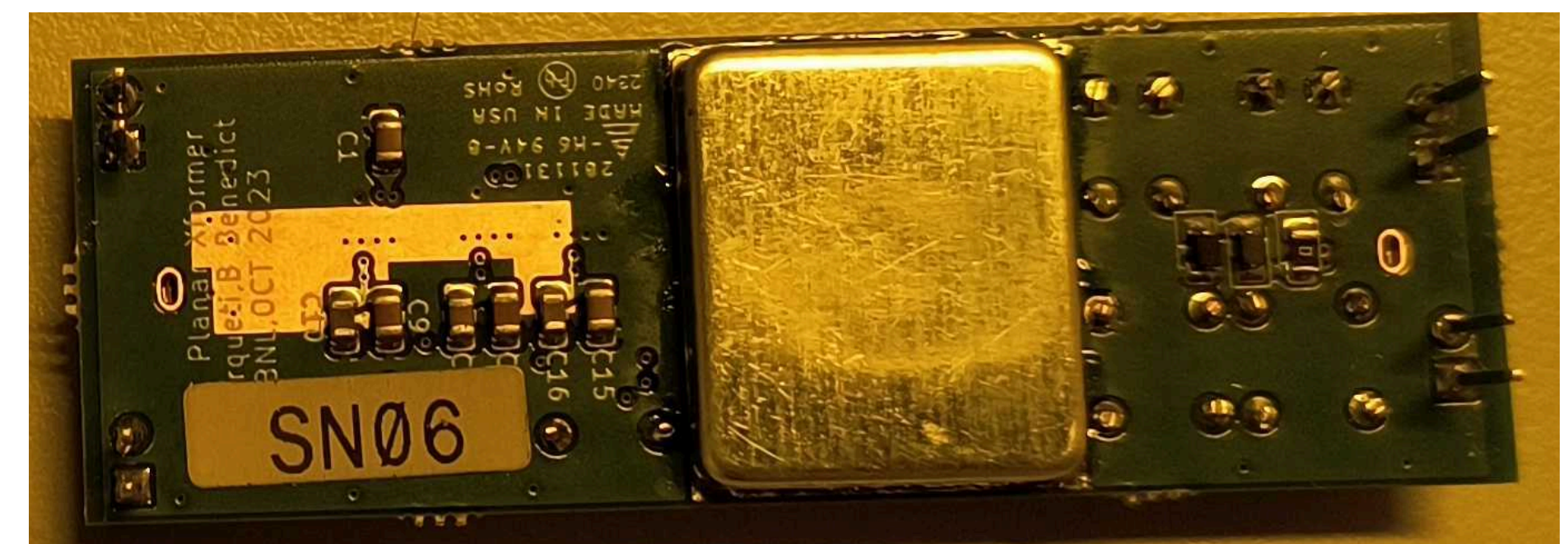
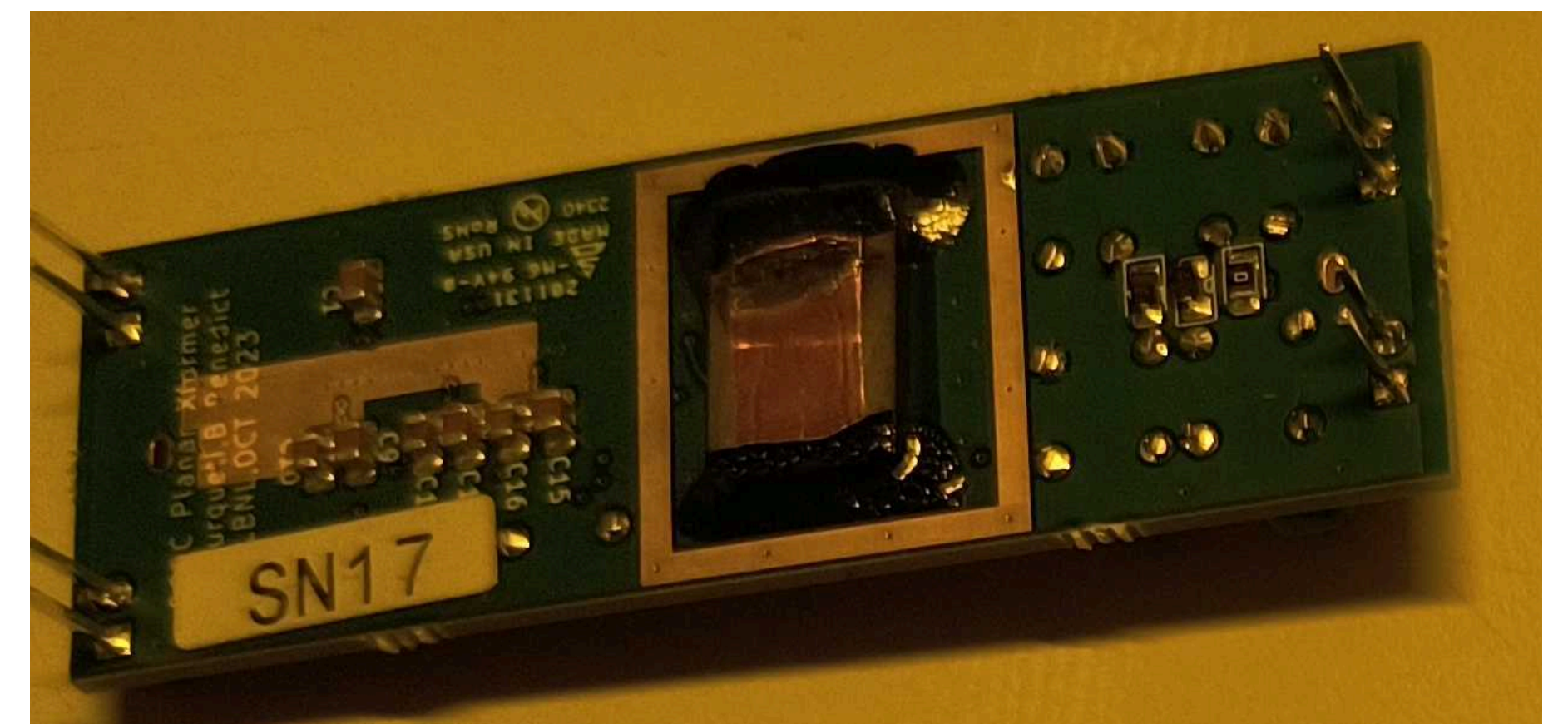
We have a dedicated shielding

Right now, it is installed in one HPK DCDC and one FBK DCDC.

The two DCDCs shielded are both at CERN.

Waiting to receive more DCDC and shielding at Fermilab, needs to check how it works.

In the main time using a handmade shielding, hoping it stay in place when in LAr.

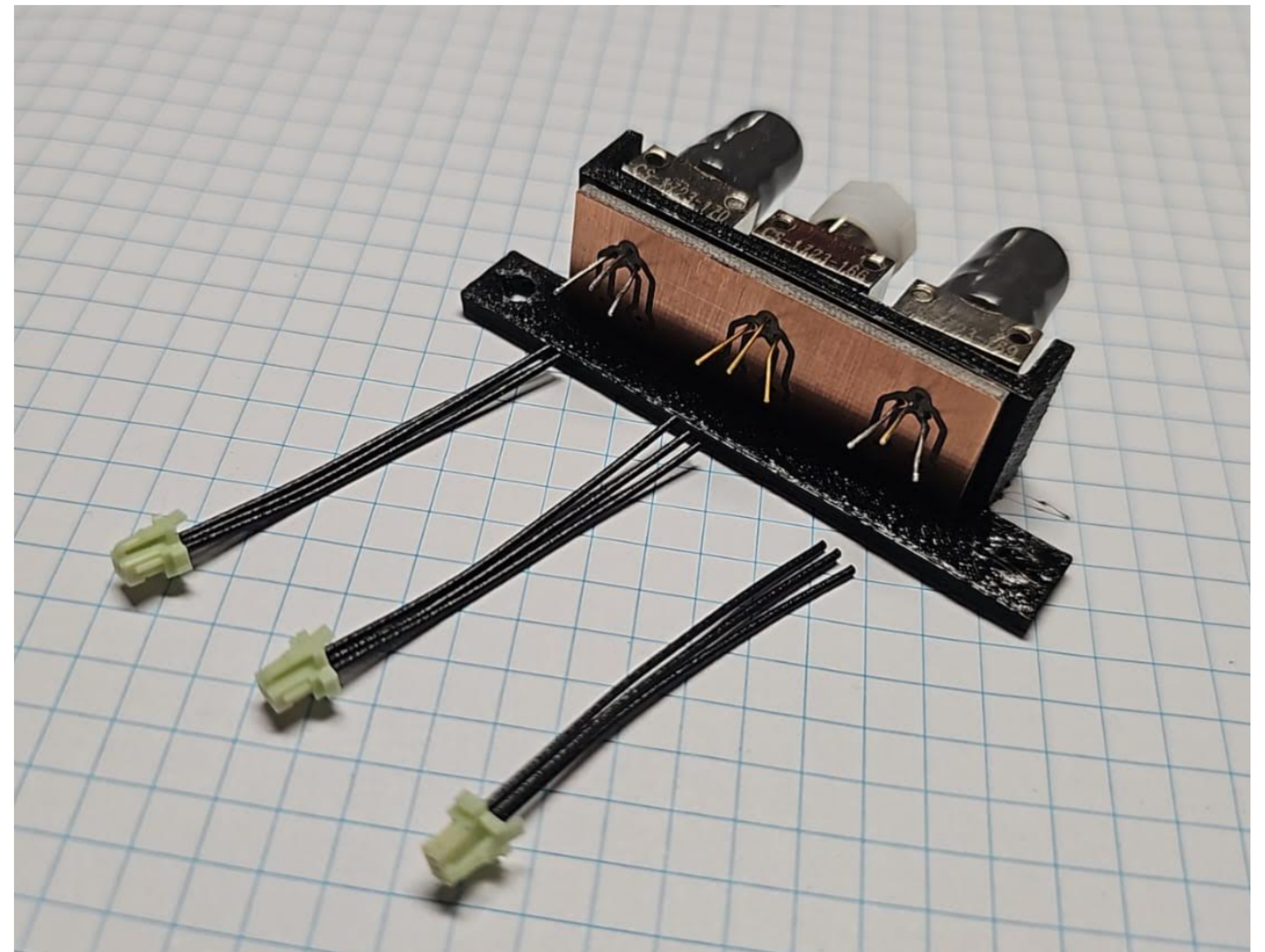


Next step: POF

One POF module is ready to be plugged in one DCEM at Fermilab.

Waiting for the laser box to power it and for more Liquid Argon for cold test.

Three OPC should be much more than needed for cold and probably two will be enough even for warm test.



Test Plan: (first warm and then cold)

Powering the full chain (DCEM + 4 HPK flex + DCDC) with 1, 2 and 3 of PoF plugged, monitoring the voltage at:

1- LDO in (=OPC out). **2-** LDO_OpAmp OUT. **3-** LDO_DCDC OUT (=DCDC in). **4-** DCDC out (SiPM in).

Boards at CERN

4x almost full set of electronics is at CERN
(OPC for PoF not there yet... Bill and Drew will carry them next week?)

- 4 DCEM
- 4 laser daughter cards
- 4 (3+1) DCDC

The 4 DCEM at CERN needs to be set in the final config (see next slides).
And tested...

X-Arapuca modules from NIU are ready to be shipped (@Kurt).

X-Arapuca modules from Spain are at CERN (@Manule).

I can prepare extra 4 DCEM boards to be carried by Bill and Drew at CERN.

I have no extra DCDC in my hands right now, (they should arrive before Bill and Drew leave).

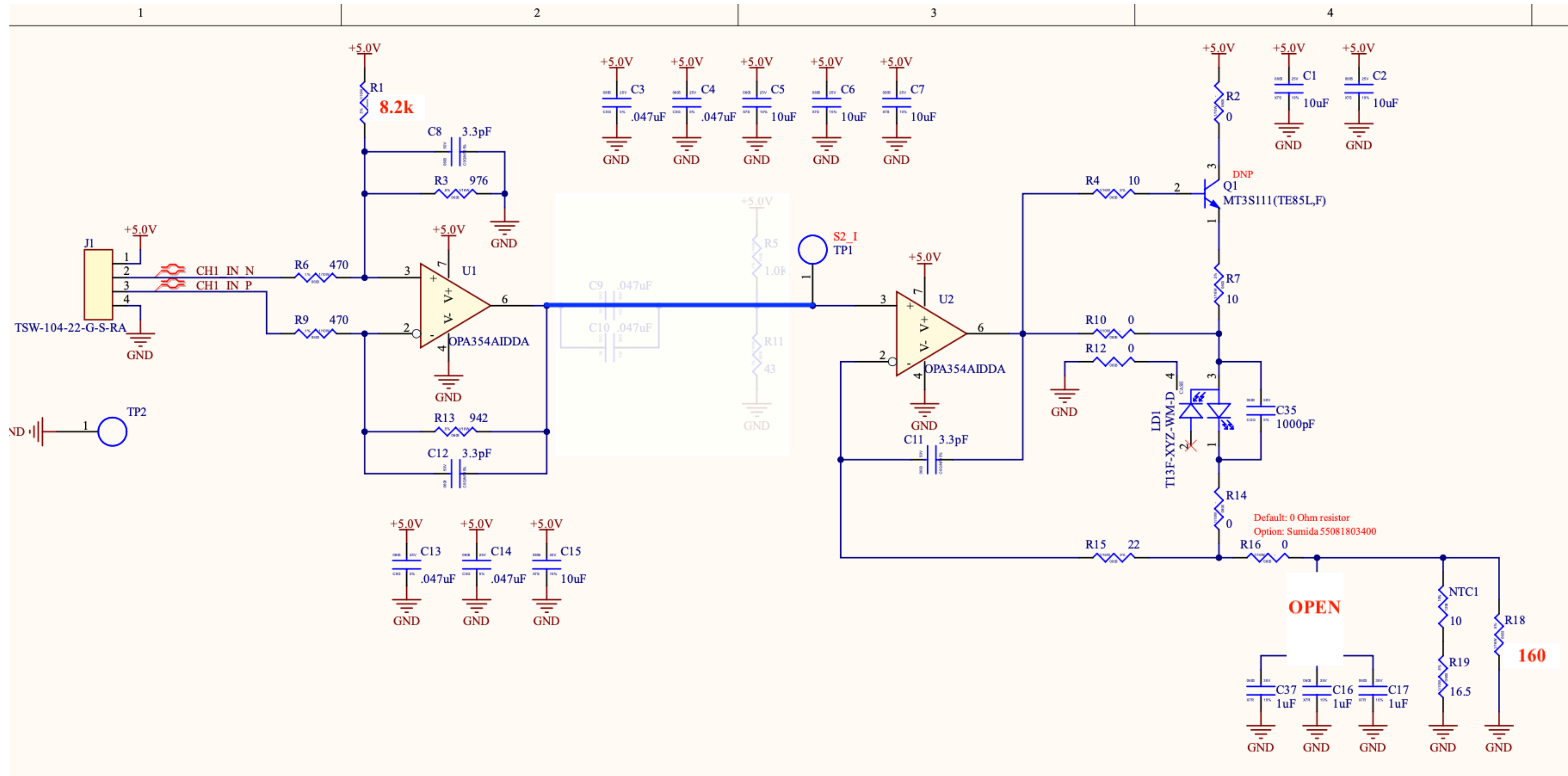
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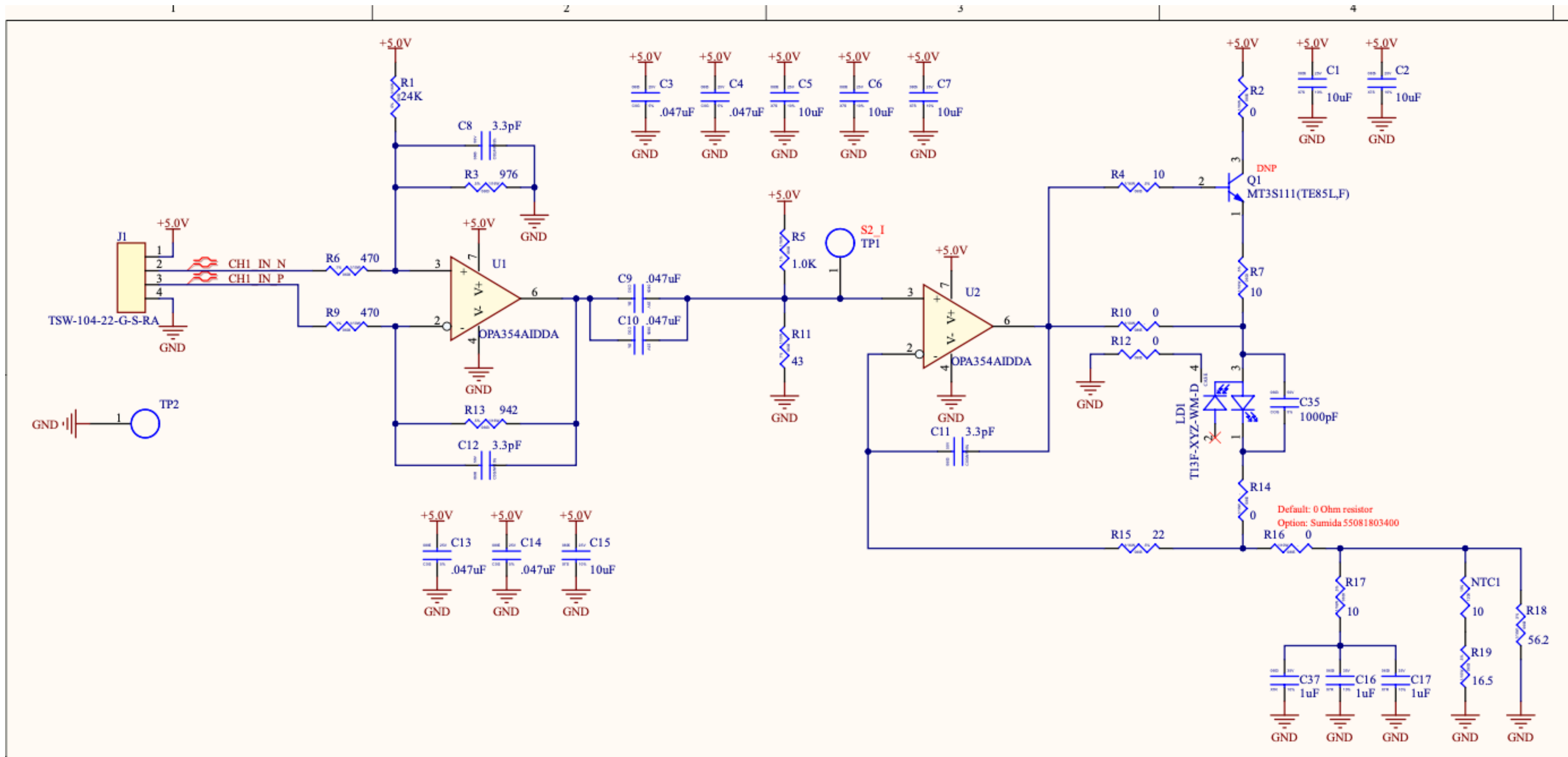


List of changes

- R1, R20 -> replace with 8.2 k Ω
- R18, R37 -> replace with 160 Ω
- R5, R11, R17, R24, R30, R36 -> remove
- C9, C10, C26, C27 -> remove and jumper (0 Ω) on C9 and C26

Channel 1

Original schematic for part number reference



Channel 2

Original schematic for part number reference

