

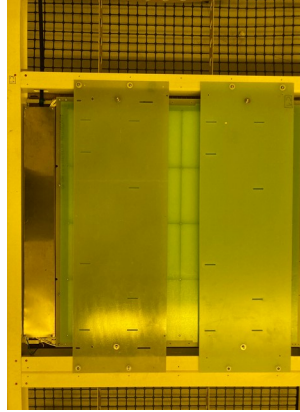
Planning

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APC - 22/03/2024

M1 SCHEDULE + RP SUPERVISED AREA!!

- Document with all the information I have + list of installation activities [is here](#)
- Estimated schedule: closing 28/03
- – weekend purging – “10cm fill” on Tuesday → if everything is OK* CB should be filled by 4/04. (holiday: 1/04)
 - Data taking for 3 weeks:
 - Starting on 08/04 (usually first days are CRP-only)
 - PNS run 2nd week→ ~11-18/04
 - stopping ~25/04
- Material:
 - SoF electronics for C3 and C4 → to be assembled and tested at Fermilab by Dante. Flavio travelling on the 18th
 - Fibers: (for cathode fibers C3 and C4): C3 and C4 shipped yesterday!! → express shipment → arrival ~18/03
 - Please ship also black silicon, kimwipes and alcohol; + screws and washers for xArapuca frame
- On-going work:
 - DAPHNE: @ Milano traveling with Eleonora on 8th of April.
 - SoF receivers prepared (powering w/ DAPHNE)
 - HD and VD matching to DAPHNE on-going (tests to avoid using attenuation etc)
 - 2nd DAPHNE could be made available by CSU (to be confirmed by Dave) to avoid using same AFE for two boards (CERN IP??)
 - VD: new warm stage and resistor change in the cold electronics to better match DAPHNE.
 - C1 turn around → C1 has a back plane → needs to be placed on the other side when flipped.
 - SoF electronics change of C3 and C4



M1 installation organization

- Tasks/team:
 - PDS test stand: Renan will train new people and be authorized to operate. Flavio is travelling. Adi and Emilie(?) btw 22-27/03.
 - Electronics testing: Jaime (19-22/03)
 - HD electronics: Eleonora
 - PNS: Ajib is coming in April.
 - Data taking: organizing btw Henrique, Sam, Ajib and Eleonora
 - Should have an more “expert” team to tune things the first week:
 - For cathode data, Henrique can be there 1 week
 - For HD: Eleonora;
 - For VD and PNS: possibly Ajib
 - Sam will stay and can run DAPHNE
 - Vitaly will be off-site but help with DAQ config of PNS run.

M0 fixup

- 8 cathode electronics to be replaced
- Electronics available → Backup solution
 - 5 boards w/CMOS
 - 2 boards w/bipolar+transistor
 - 2 boards in random config at CERN
 - 2 boards at Fermilab: 1 Iceberg, 1 for tests
 - 2 cards lost at Fermilab?
- New board production → to be attempted
 - UCSB could produce but take ~1.5 months
 - APC plans a proto-prod but needs Altium files (could be rushed to furnish M0)
- Plan: if CB results are good, $\frac{1}{2}$ and $\frac{1}{2}$ CMOS and non-CMOS. Otherwise, all CMOS

M0 fixup plan (int. meeting 13/03):

- Dismounting: 3 days, 3 ppl
- Refurbishing of cathode modules:
 - Total 2 weeks for 3-4 people
 - PD test stand: 1 module/day
- Fiber check: cleaning and verification of state. 1 day
- Installation: 1 module/day. 3 ppl.
- Team not identified yet → need dates! June is most likley.
- Boxes: high priority to replace all of them.
- PoF → only available for 4 modules (CB). But Bill possibly has enough.
- Lasers: not clear if enough available.