

Istituto Nazionale di Fisica Nucleare SEZIONE DI TORINO

Studies about the ASIC architecture: code update

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BEHAVIORAL MODEL: set of equations that capture the operation of a circuit from its terminals

<u>PURPOSE</u>: implementation of a time-based readout front end for the analysis of different cases



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for the analysis of different cases

QUENCHING RESISTANCE



MAIN PREVIOUS QUESTIONS: • HOW THE OUTPUT WAVEFORM HAS TO BE (ARCHITECTURE) • HOW DISCRIMINATOR HAS TO WORK



PYTHON CODE STRUCTURE

TBRChain_class.py

TBRChain_main.py

FUNCTIONS:

- A. Convolution of the sampled input signal with the amplifier transfer function (completed)
- **B.** Fit of the tail to provide the time constant for pole 0 cancellation (fit step completed, application of pole 0 cancellation on signal needs tuning)
- C. Construction of a spill from timestamp files (completed)
- **D.** Discrimination (with threshold and hysteresis)

PYTHON CODE STRUCTURE

FUNCTIONS:

A. FE_Amplification(I_1PE, time, params)

Regulated Common Gate Amplifier

$$\frac{V_{out}}{I_{in}} = \frac{R_L}{\left(1 + s\frac{C_T}{gm1A}\right)\left(1 + sC_LR_L\right)} \cdot G$$

Depend on the FE Transfer Function: 3 options available, selected with the class inizialization

Regulated Common Gate Amplifier with pole in the boosting fb loop

$$\frac{V_{out}}{I_{in}} = \frac{R_L gm 1A_0}{\left(C_T \tau_R s^2 + C_T s + gm 1A_0\right) \left(1 + sC_L R_L\right)} \cdot G$$

Regulated Common Gate Amplifier with pole and zero in the boosting fb loop

$$\frac{V_{out}}{I_{in}} = \frac{R_L g m 1 (A_0 - C_{gd} R_R s)}{\left(R_R C_T (C_{gs} + C_{gd}) s^2 + C_T s + g m 1 A_0\right) \left(1 + s C_L R_L\right)} \cdot G$$









PYTHON CODE STRUCTURE

FUNCTIONS:

B. POLE-ZERO CANCELLATION: modification of the FE Transfer Function

ZeroMult(num, tau)
$$T'(s) = T(s) \cdot (1 + s\tau)$$
 With: $T(s) = \frac{num}{den}$











NEXT STEPS

- Tuning of Pole 0 Cancellation
- Analysis of the different combinations of Rq and Area

BACKUP SLIDES





Bode plot

