

Planning

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APC - 22/03/2024

M1 is starting

- Document with all the information I have + list of installation activities [is here](#)
- Cathode was in by ~26th/03?
- Closed 28/03 → **stop on 25/04**
- No integrations meeting this week, some scattered info:
 - Some (small?) issue with purging required extra purging day
 - CB was filling on Wednesday (possibly the small fill?)
 - CRP6 is looking OK. CB is full.
 - Sam and Ajib taking care of connecting/testing membrane modules
- **Beam commissioning is on-going**, at least until Wednesday next week
 - Means access is restricted → people can't stay downstairs at the CB taking data
 - Need to re-plan the first days of data taking
- Reminder to request access to the trenches + get a dosimeter
 - I think one access point to look for is YDAP-PPE184

M1 data taking

- Team:
 - Sam and Ajib are at CERN until (end of run?)
 - Henrique 8-12/04 and if needed 22-26/04
 - Eleonora and Federico 8-12/04 (with Milano's DAPHNE and ArgonREC receivers)
 - Additional help at CERN: Renan, Manuel
 - Remote assistance: Vitaly, Esteban, Claudio
- 2x DAPHNEv2 at CERN (Sam is asking for extra fibers to be able to use them simultaneously)
- Data taking items → organize order
 - LED calibration with DAPHNE → tuning of DAPHNE → plot SNR and dyn range vs Vgain to find the optimal spot
 - Not dependent on LAr purity
 - But needed to know the setting of DAPHNE
 - Cosmics with DAPHNE + CRP → need good LAr purity and DAPHNE config
 - PNS run → need good LAr purity and DAPHNE config → start as soon as DAPHNE config is known
 - PNS installation to be carried out as soon as access is possible (~wednesday)
 - LED calibration with CAEN (few hours, needed for reference), no LAr purity rec

M0 fixup - electronics

- 8 cathode electronics to be replaced
- Electronics available → Backup solution
 - 5 boards w/CMOS
 - 2 boards w/bipolar+transistor
 - 2 boards in random config at CERN
 - 2 boards at Fermilab: 1 Iceberg, 1 for tests
 - 2 cards lost at Fermilab?
- New board production → to be attempted
 - UCSB production: going to CIREXX by ~12/04
 - APC: just got altium files. Need some days to make changes, then will send prod.
- Plan: if CB results are good, $\frac{1}{2}$ and $\frac{1}{2}$ CMOS and non-CMOS. Otherwise, all CMOS

M0 fixup – WLS and filters

- Tests in CIEMAT:
 - Thicker WLS: with one-sided xA at CIEMAT by the end of next week
 - No DF: need PTP-coated glass → ~1 month timescale
 - Also need double sided xA: Dave to send DF support
- Thicker WLS:
 - Need ~1 month for prod
 - Decision to produce a few (at least 5) to replace a couple modules and distribute
 - 2 already available in Milano
- No DF:
 - Need to produce PTP coated glass → Carla will send a few sets to Camipnhas to be coated, next week
 - ~3 weeks to have them at CERN?

M0 fixup plan (int. meeting 13/03):

- Dismounting: 3 days, 3 ppl
- Refurbishing of cathode modules:
 - Total 2 weeks for 3-4 people
 - PD test stand: 1 module/day
- Fiber check: cleaning and verification of state. 1 day
- Installation: 1 module/day. 3 ppl.
- Current time window: mid May to end of June
- Team not identified yet → fill in the spreadsheet!
- Boxes: high priority to replace or upgrade all of them → design is progressing.
- PoF → only available for 4 modules (CB). But Bill possibly has enough.
- Lasers: not clear if enough available.