

ICEBERG Cryostat Noise Tests

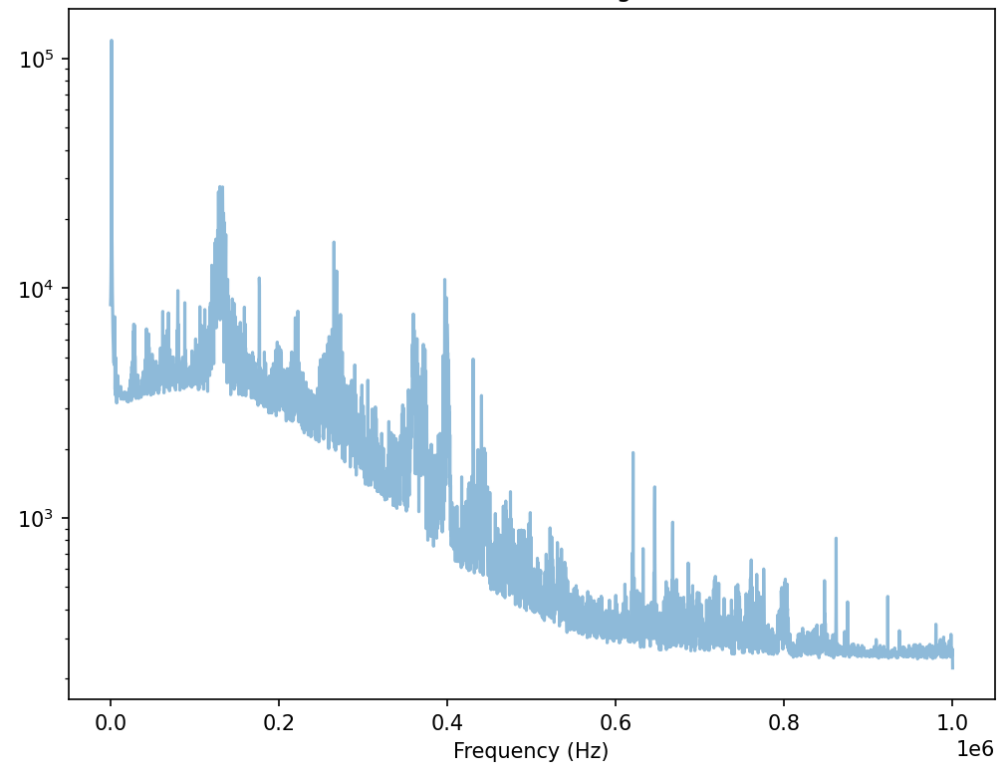
Alejandro Yankelevich

Background

- Previously reported high noise conditions after mounting FEMBs to TPC.
- Many tests to investigate nature of noise and attempt to reduce.
 - Rebundling cables, separating power/data
 - Disabling interlock and powering fans directly
 - Single WIB and single FEMB tests
 - Swapping WIBs
- Last Friday, lowered TPC into cryostat to confirm noise is environmental pickup through the anode wires.

Outside Cryostat

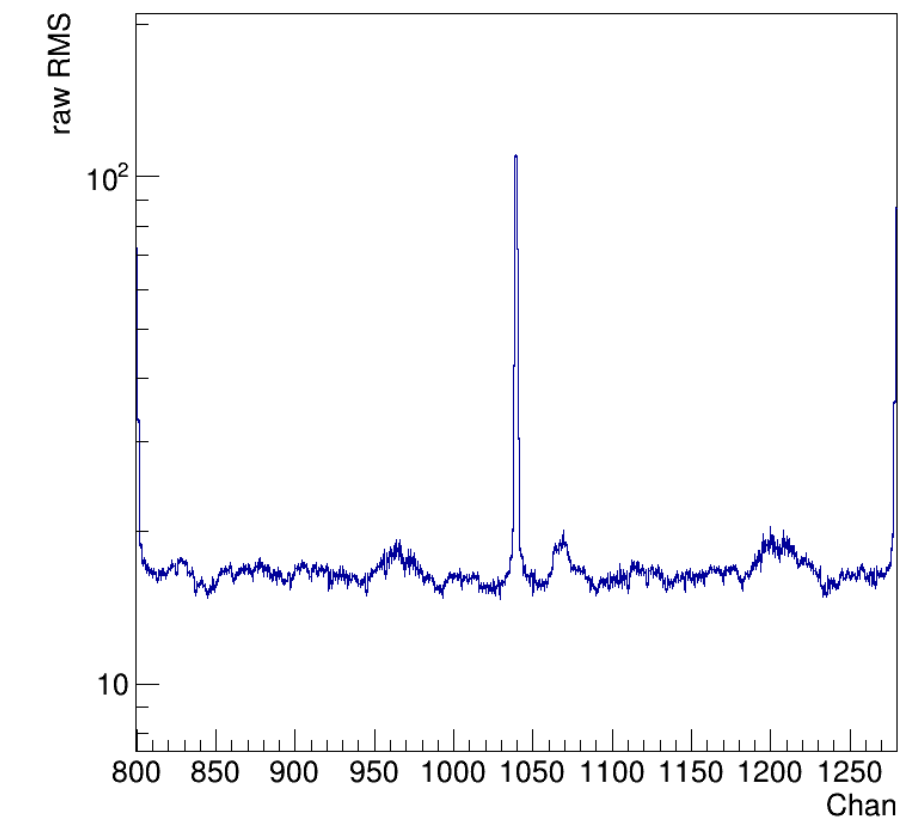
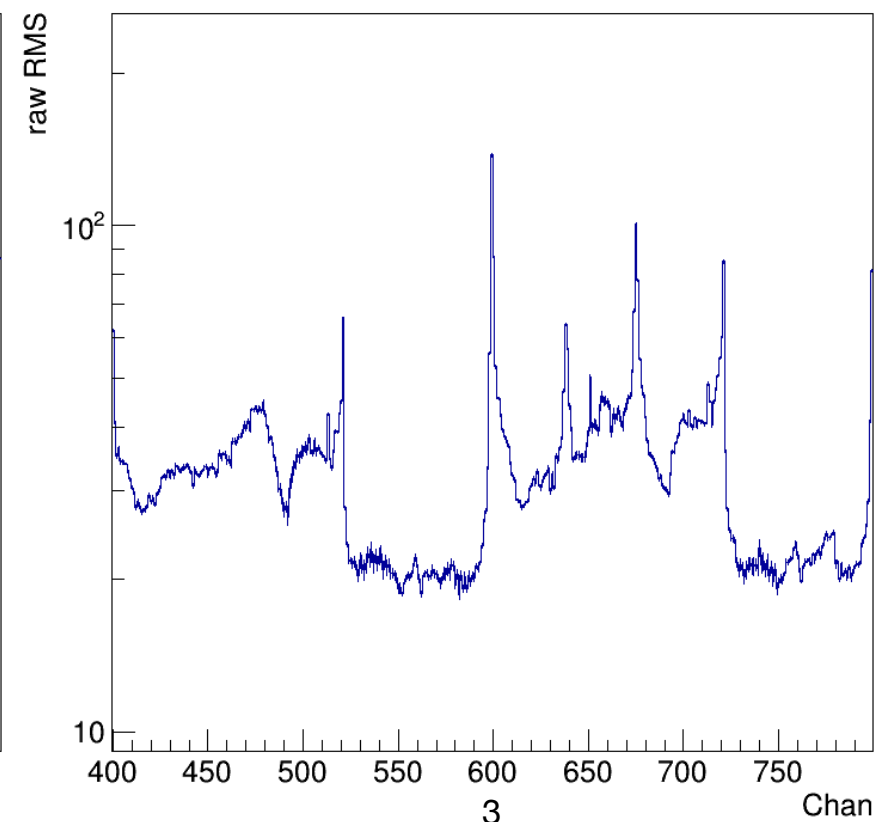
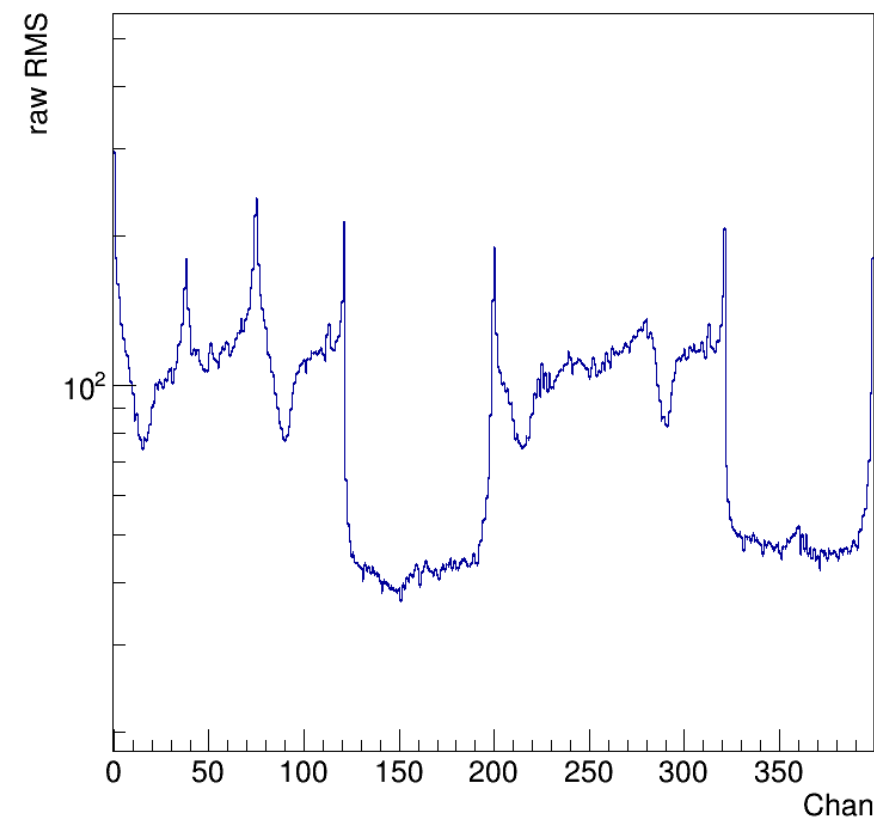
Channel 1-1280 Avg FFT



Profiled RMS vs Channel(Plane U, APA1)

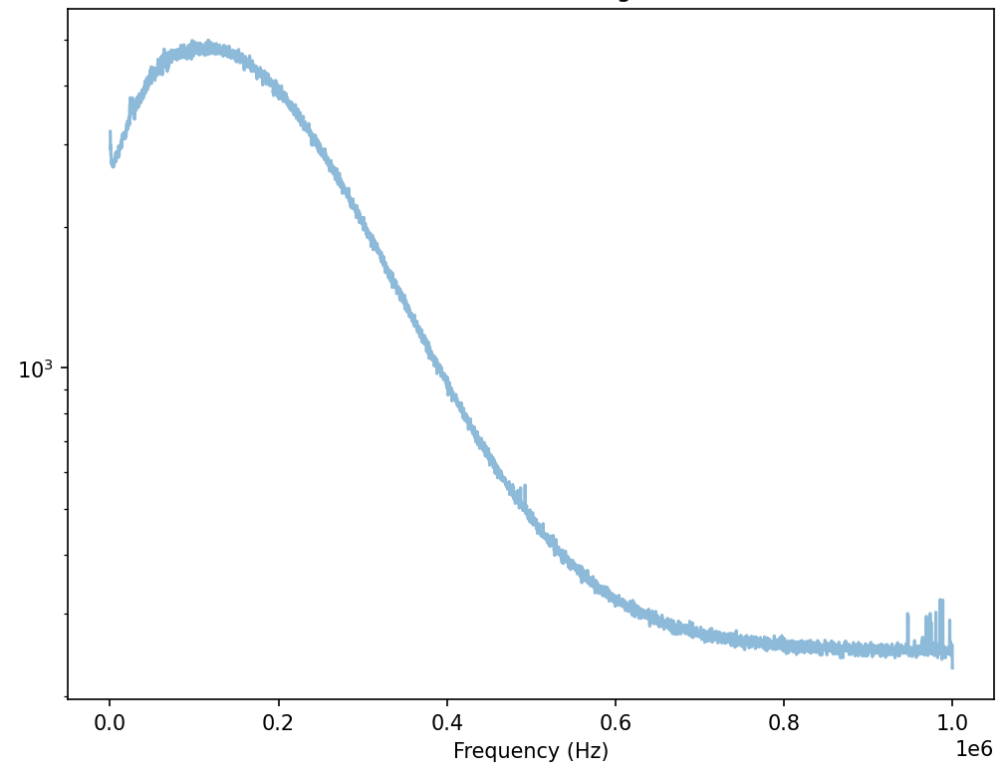
Profiled RMS vs Channel(Plane V, APA1)

Profiled RMS vs Channel(Plane Z, APA1)

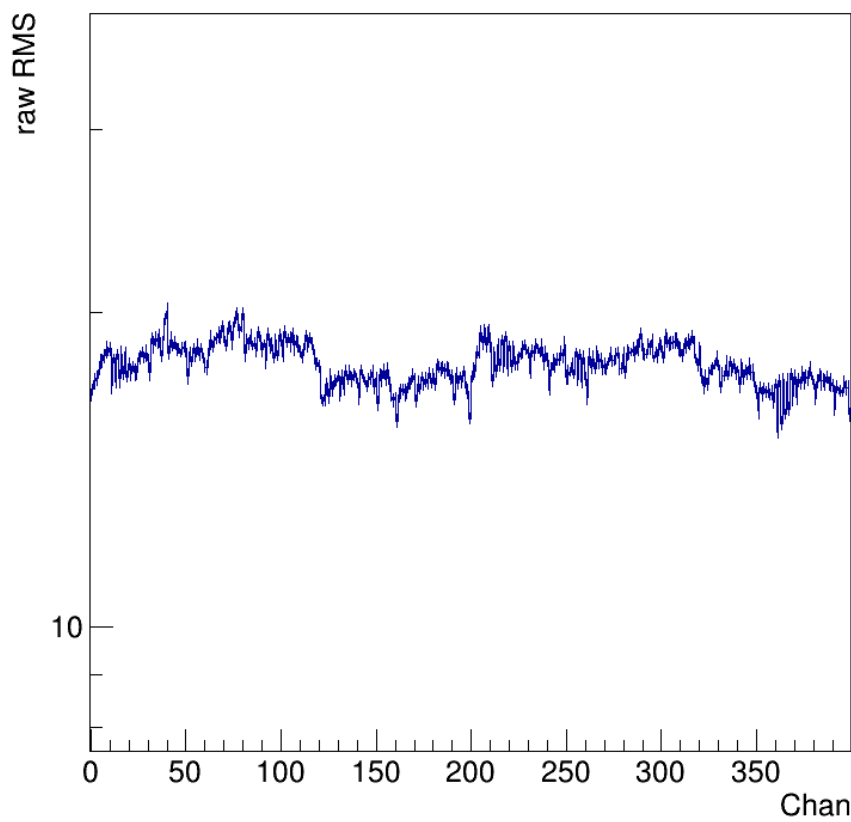


Inside Cryostat

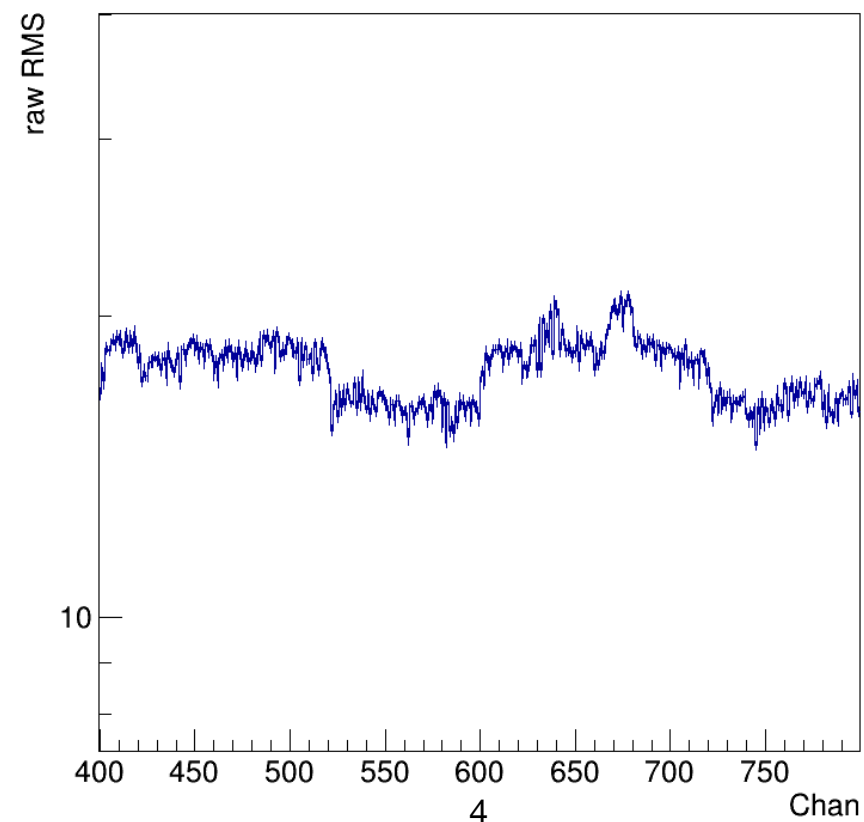
Channel 1-1280 Avg FFT



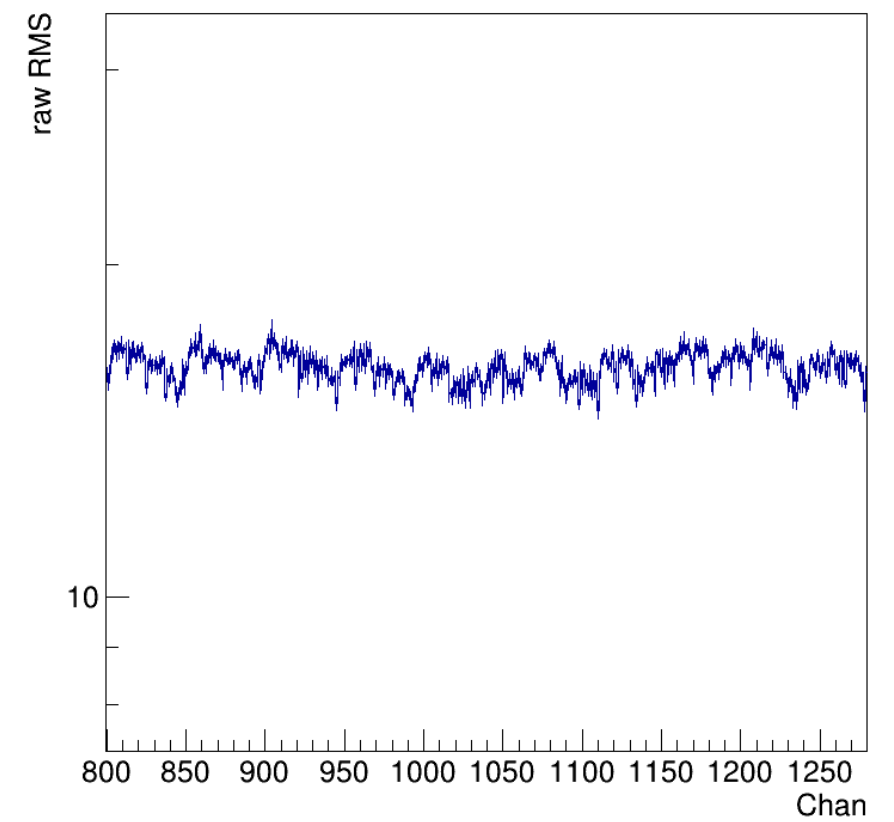
Profiled RMS vs Channel(Plane U, APA1)



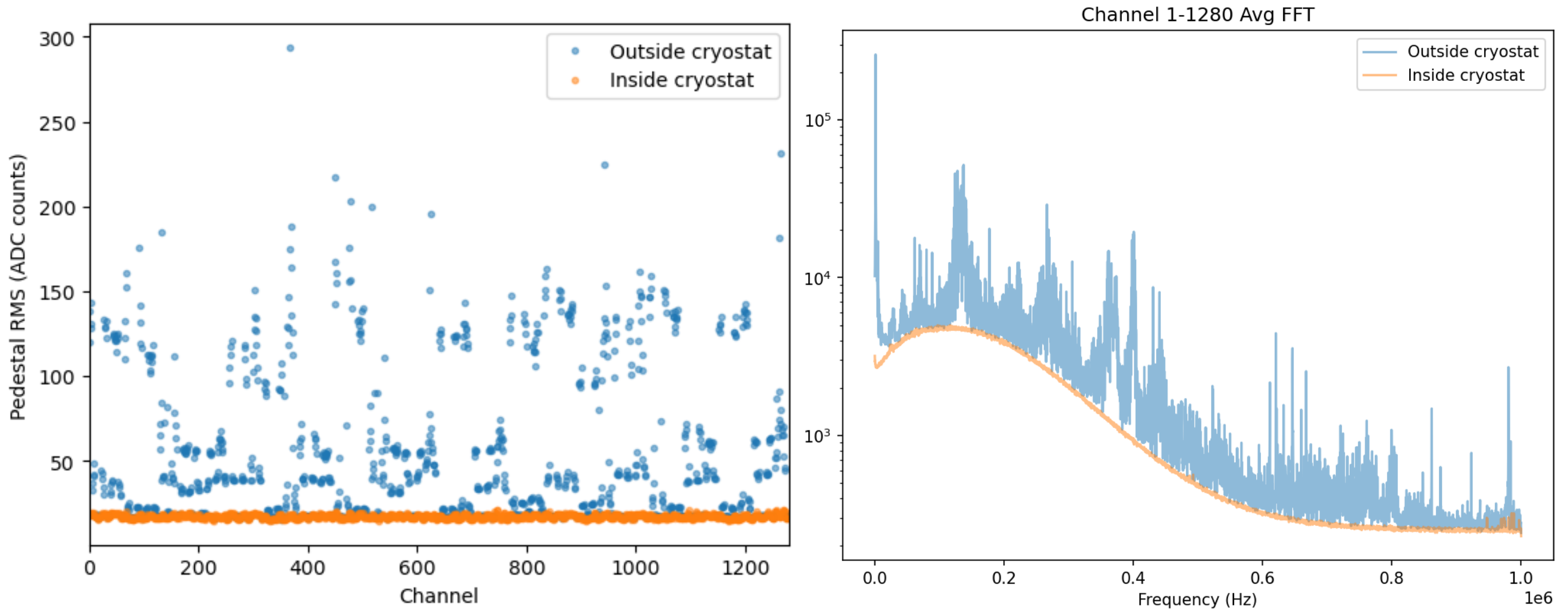
Profiled RMS vs Channel(Plane V, APA1)



Profiled RMS vs Channel(Plane Z, APA1)

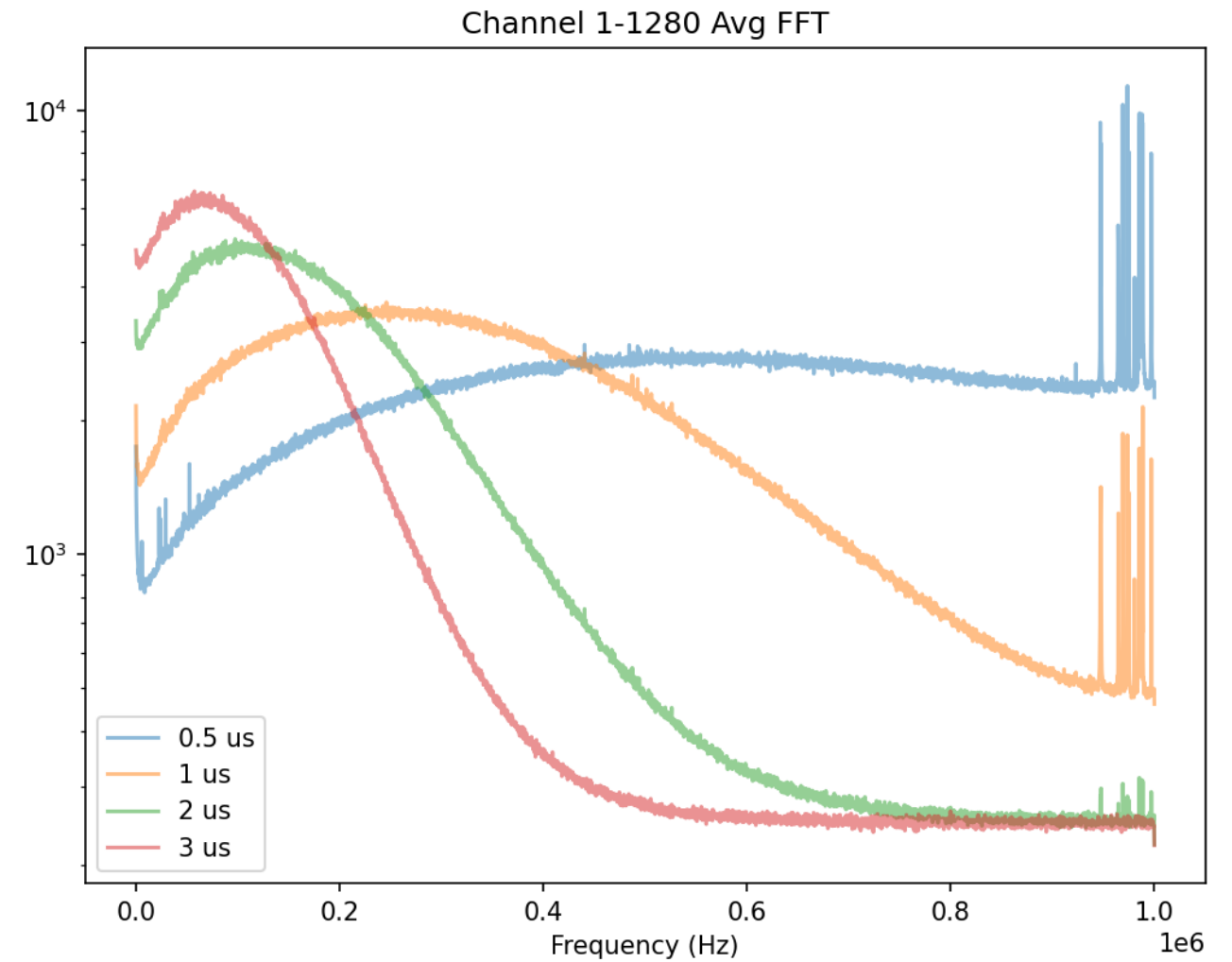
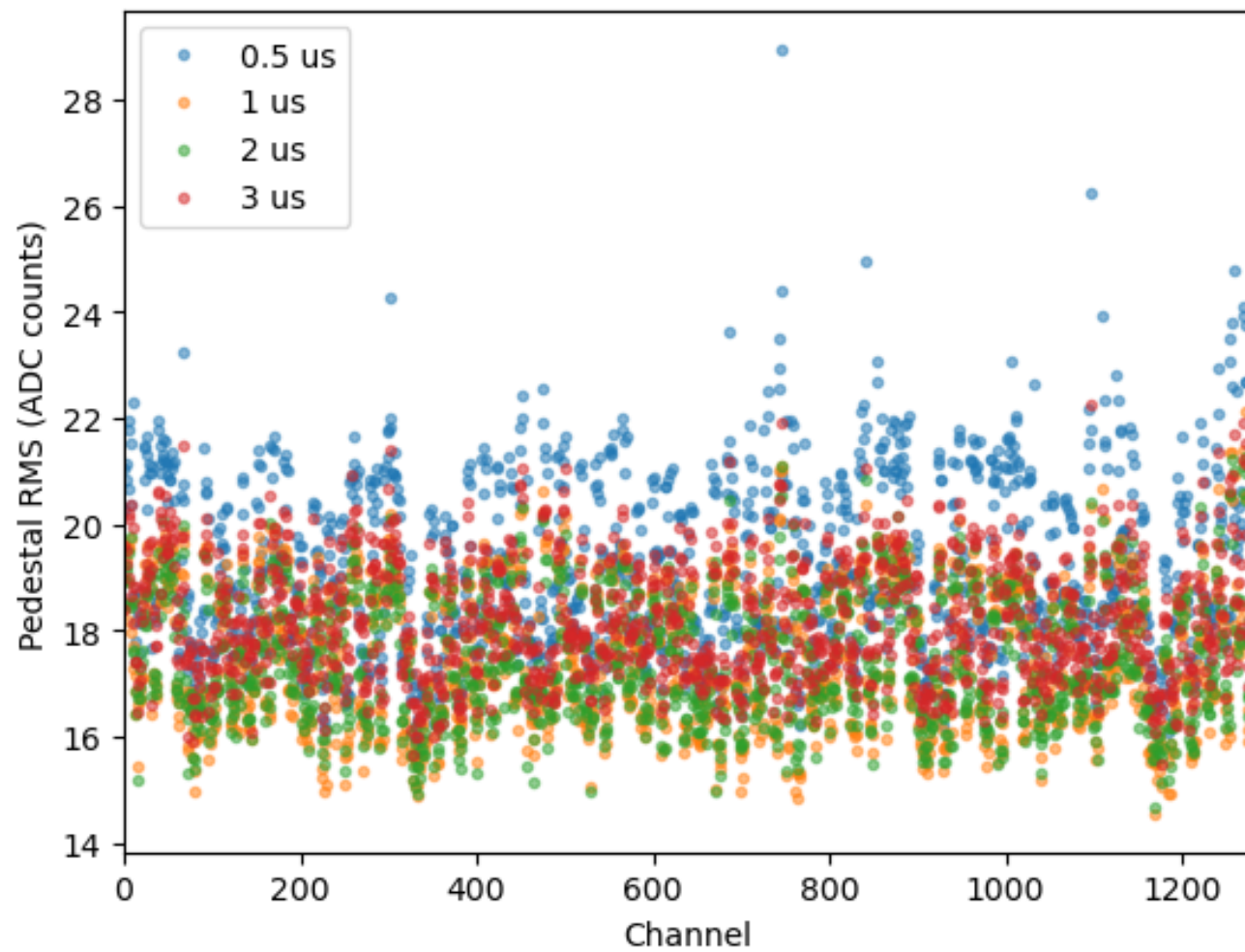


Inside vs Outside Cryostat

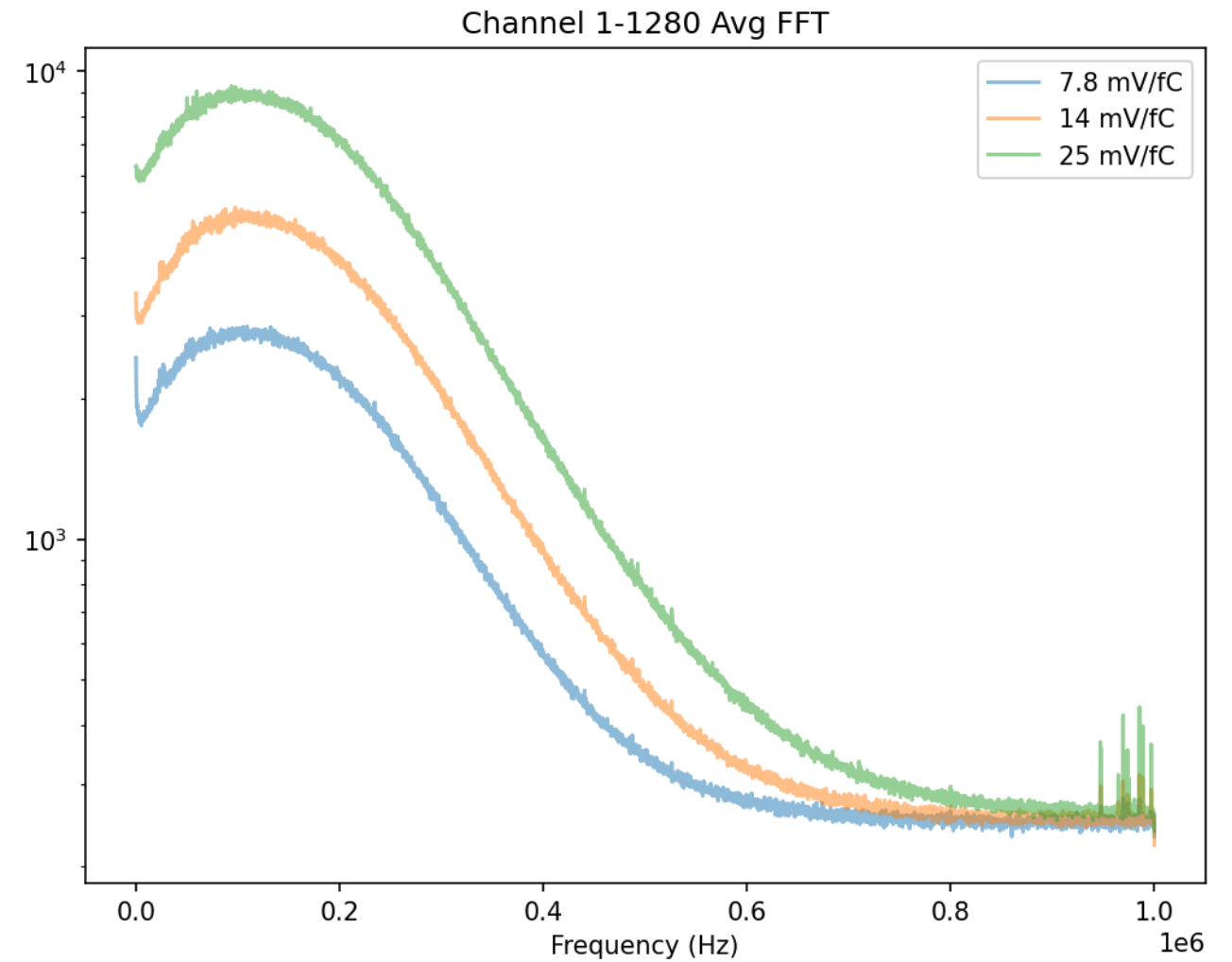
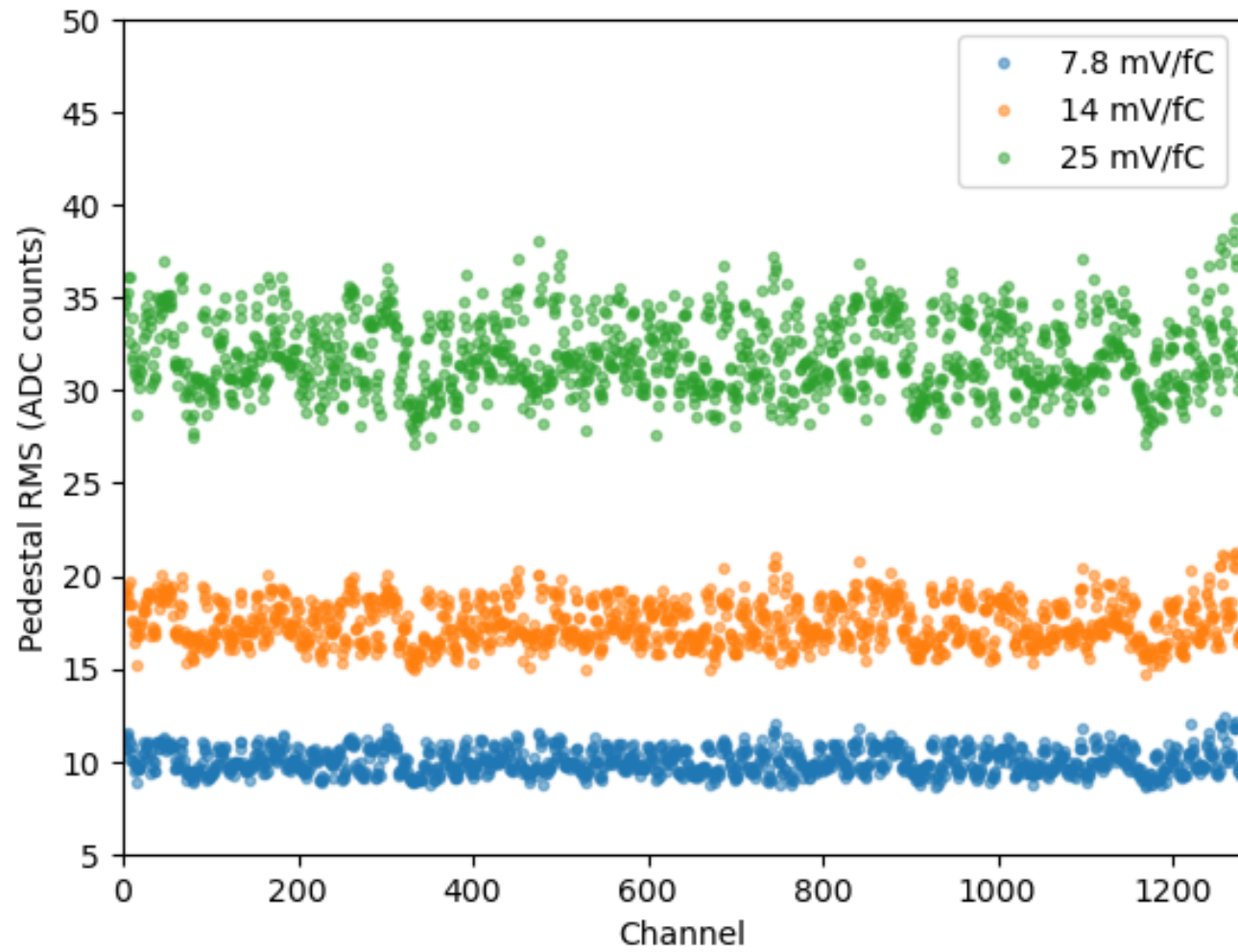


Note: Channel map is not applied in python-based analysis.

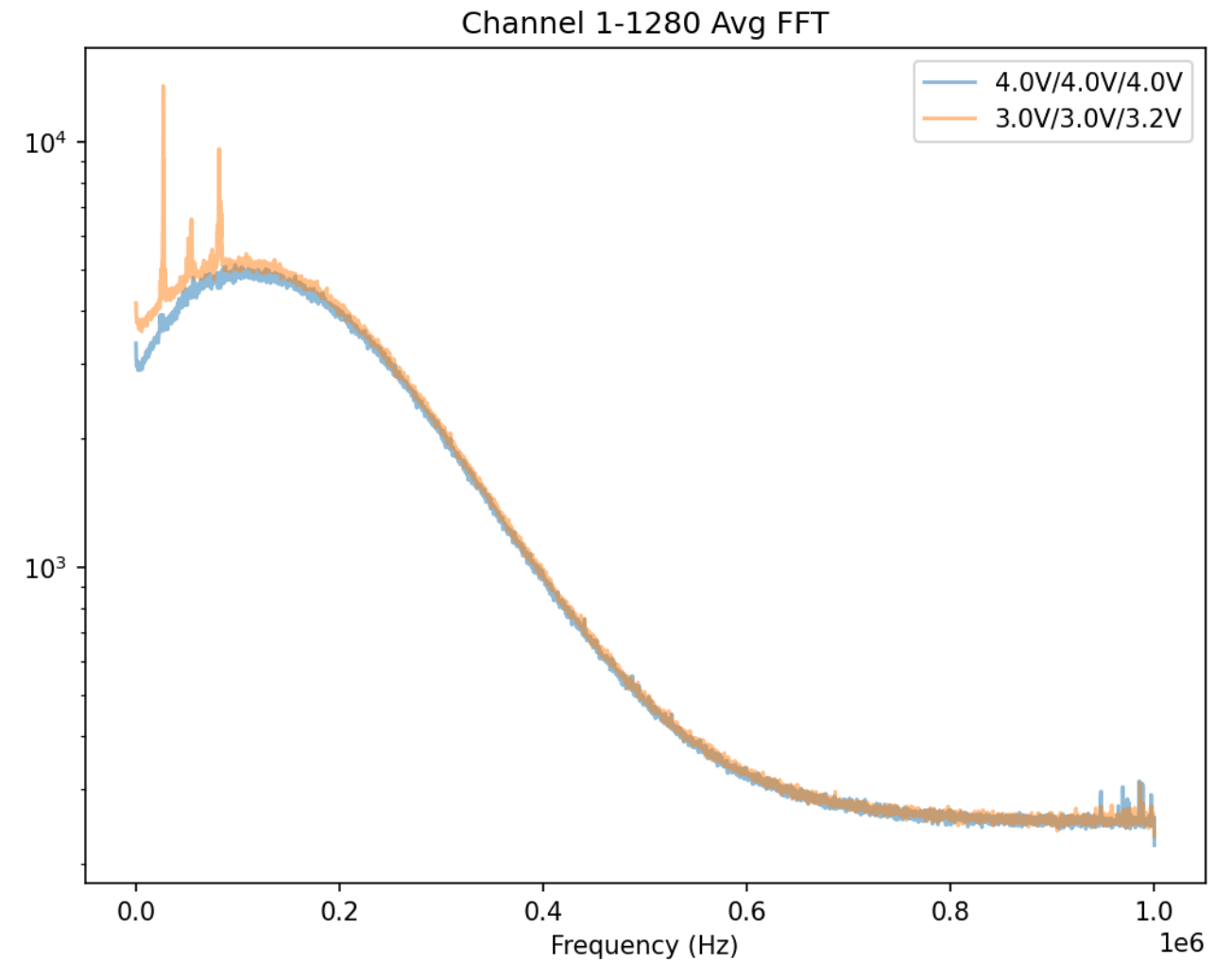
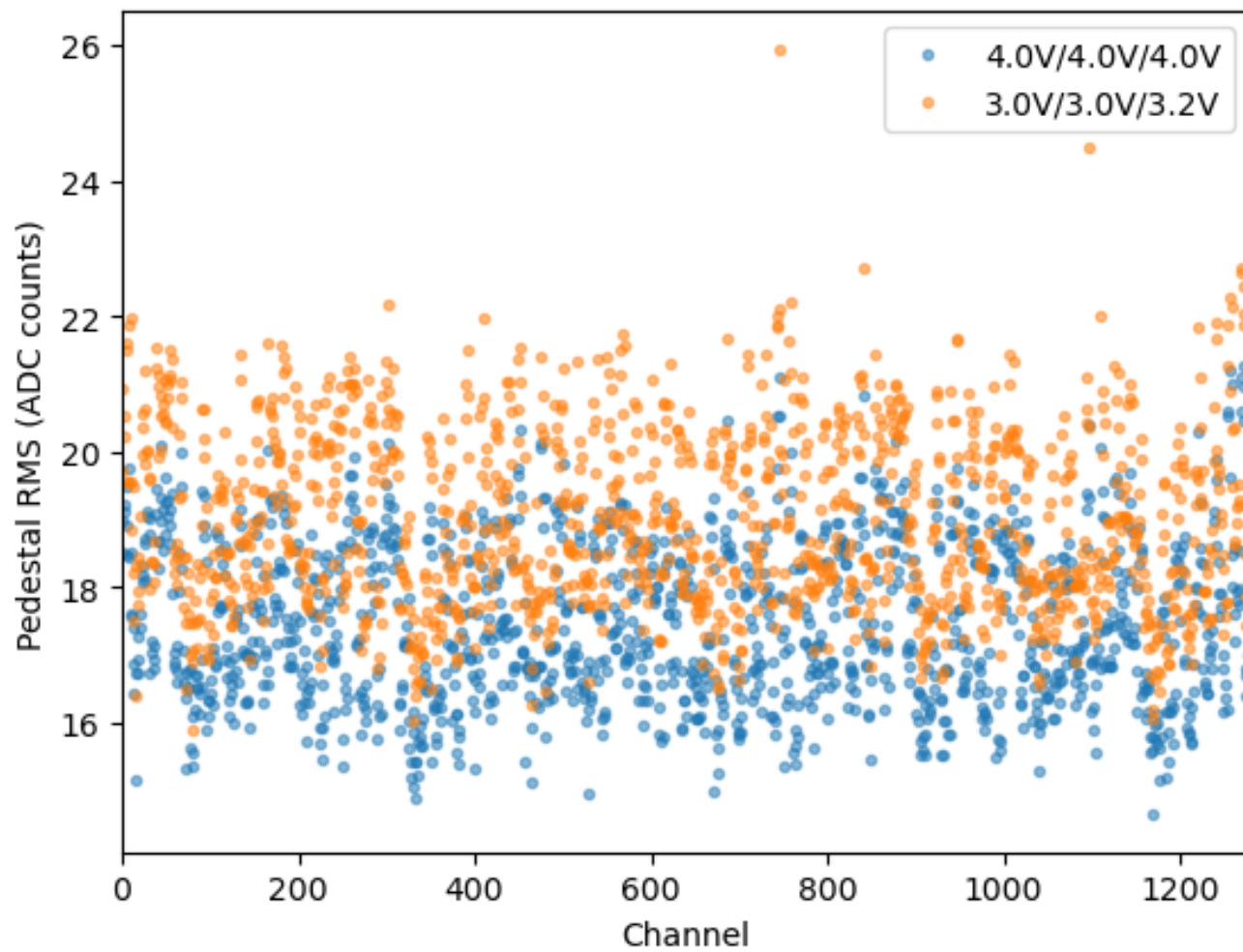
Varying Shaping Time



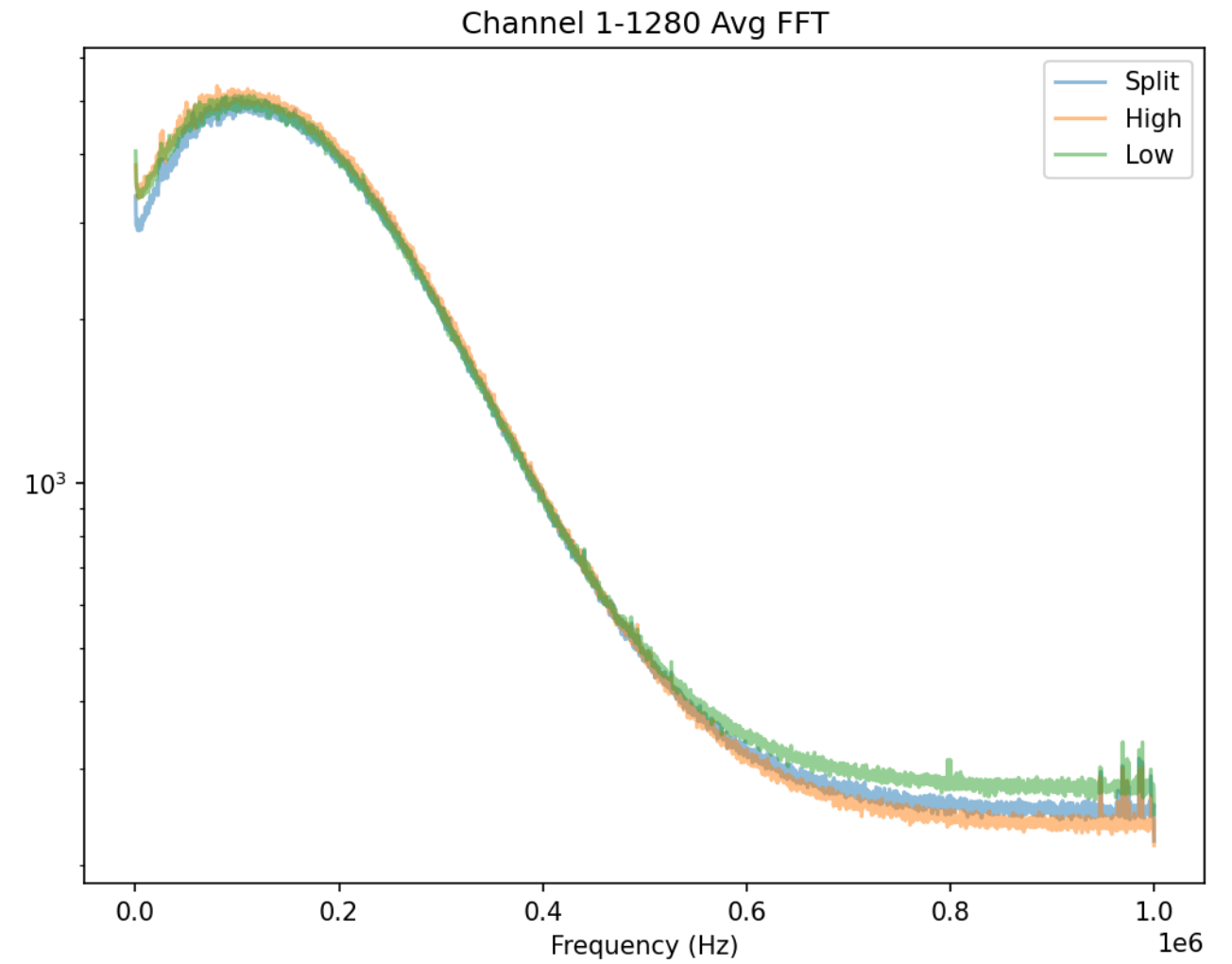
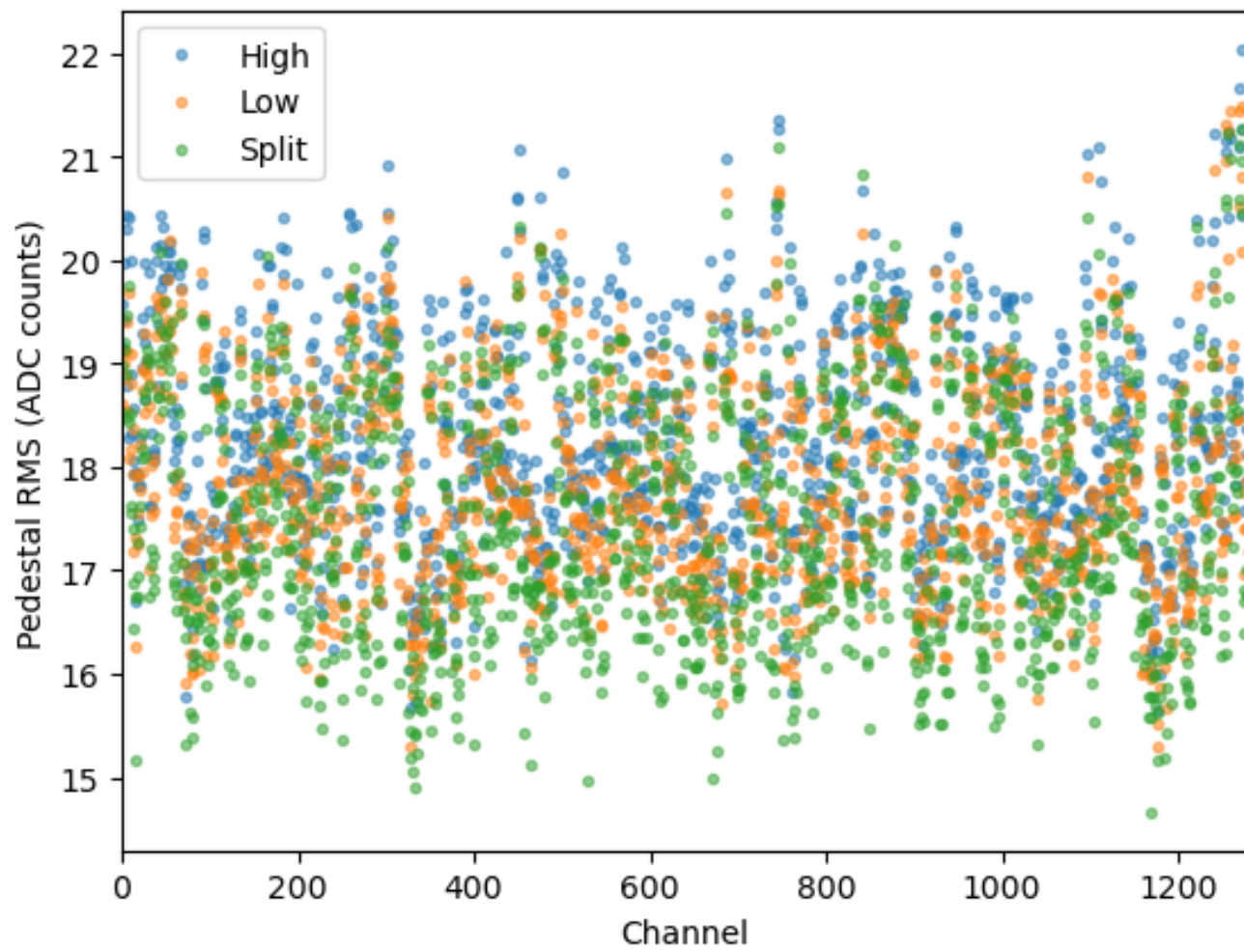
Varying Gain



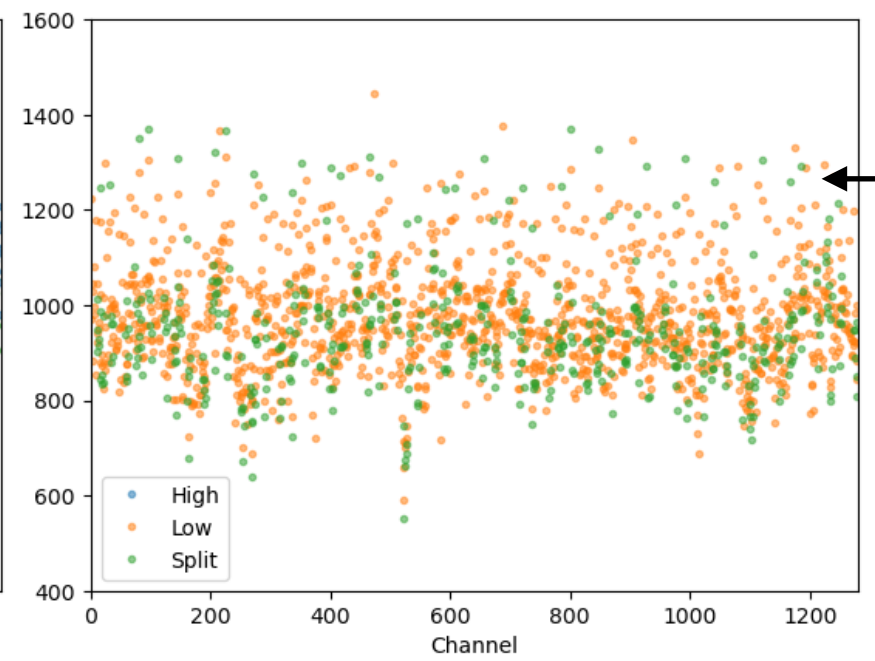
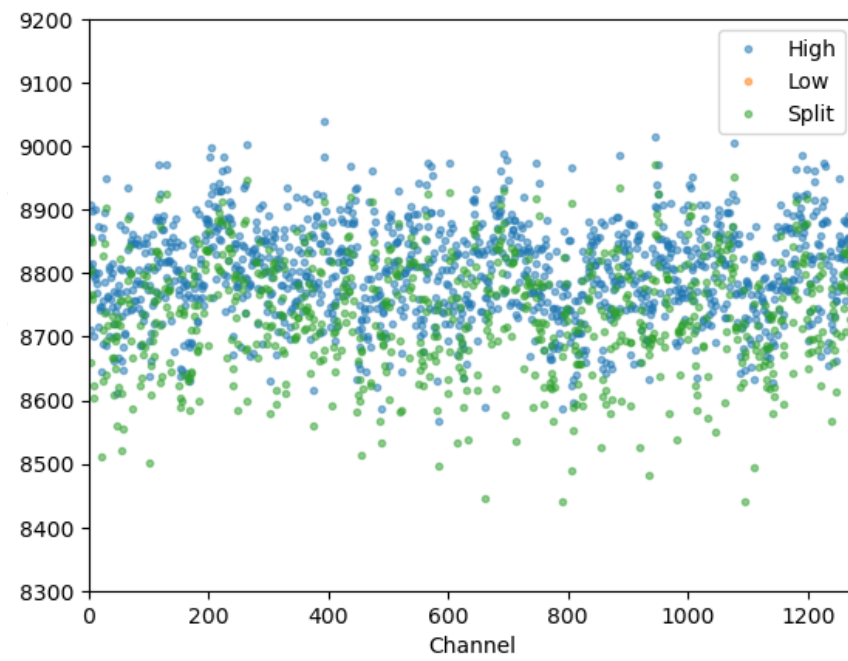
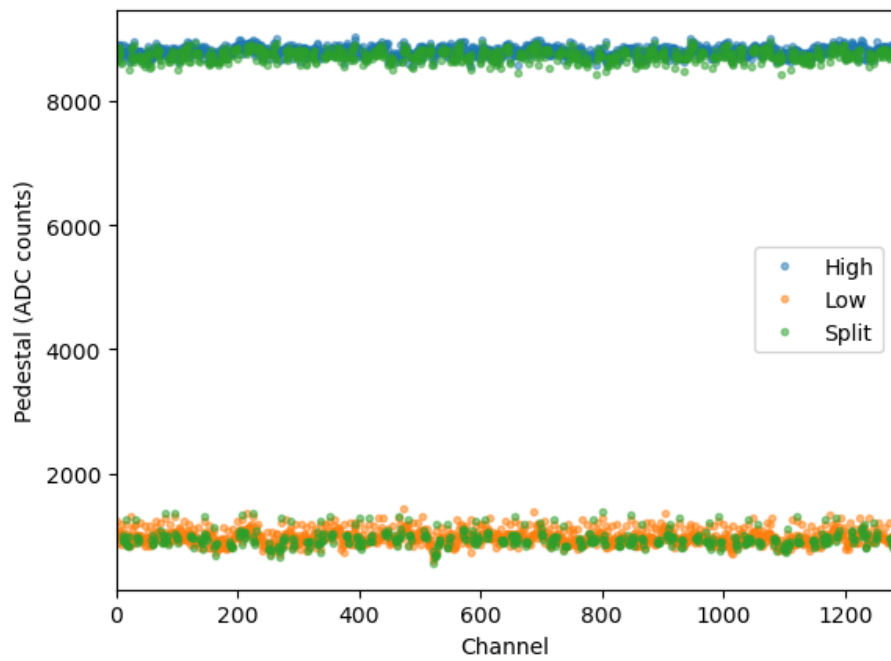
Varying Power Settings



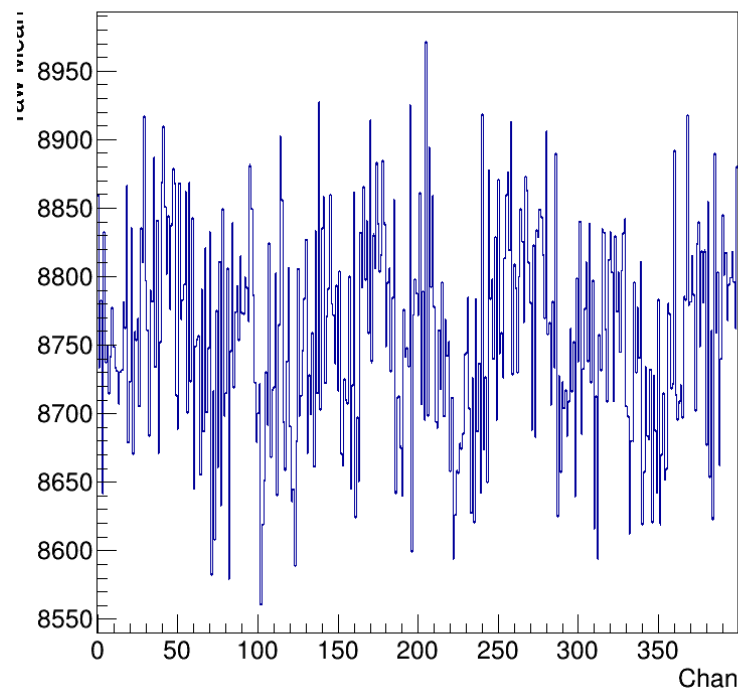
Varying Baseline



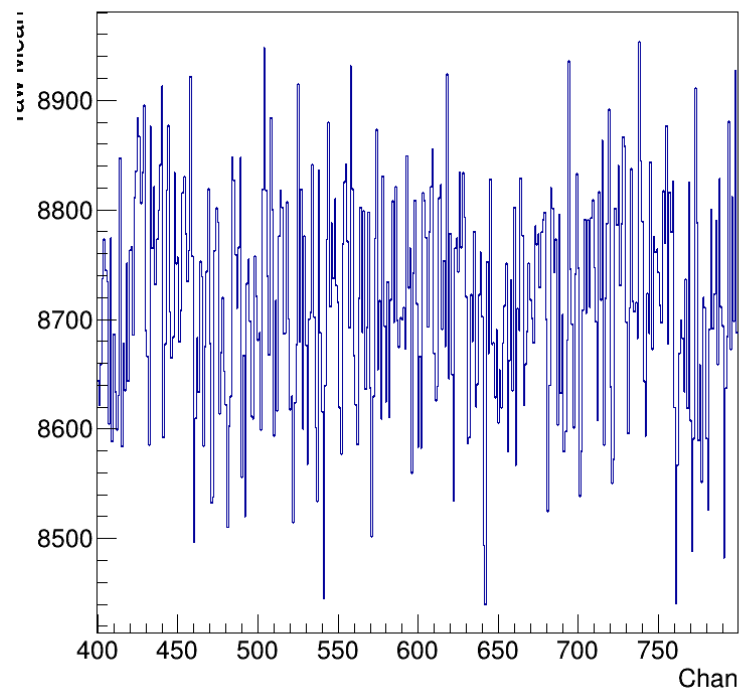
Varying Baseline Pedestals



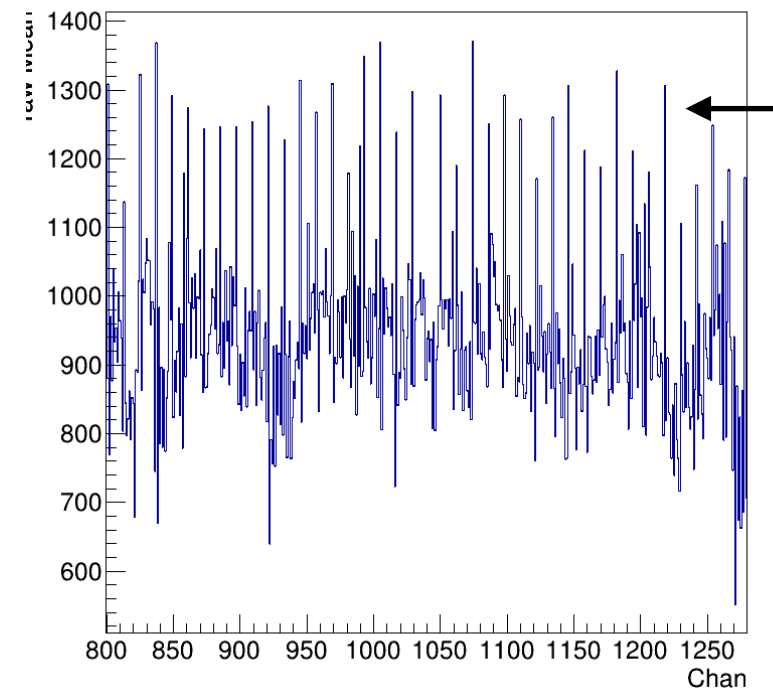
Profiled MEAN vs Channel(Plane U, APA1)



Profiled Mean vs Channel(Plane V, APA1)



Profiled Mean vs Channel(Plane Z, APA1)



Split baseline: