Test Beam Results of Planar Pixel Sensor for the CMS Phase 2 Inner Tracker Upgrade **Richa Sharma, University of Puerto Rico - Mayaguez** on behalf of CMS Tracker collaboration

Introduction

- Test beam measurements of CMS Readout Chip (CROC) were performed. These sensors will be used in the forward pixel disks of the Inner Tracker in the High Luminosity era of the Large Hadron Collider (HL-LHC).
 - HL-LHC peak instantaneous luminosity of 7.5x10³⁴ cm⁻² s⁻¹ corresponds to an average of around 200 inelastic proton-proton collisions per beam-crossing every 25 ns.



- The present CMS tracking detector will be completely replaced in order to efficiently reconstruct and track particles in these extreme and challenging conditions.
- Effect of bias voltage and charge collection threshold on efficiency, cluster charge, and cluster size are presented in this poster.

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2.5

1.5

E 1.0

0.5

The Phase-2 CMS tracker layout

Pixel modules, in orange (quad-chip modules) and green (double-chip modules), form the Inner Tracker system. The Outer Tracker is composed of two different types of modules indicated with blue (PS modules) and red (2S modules) lines



 120 GeV proton beam with 1 spill per minute

• Dimensions:

- Each spill lasts 4s
- About 50k protons per spill.
- ~1 cm width
- ~5 μm resolution



- Cluster size increases slightly with bias voltage, with primarily 2 pixel clusters.
- Cluster charge increases with bias voltage.
- Consistent behavior for 100 μ m and 225 μ m pixels.

Credit: Hsin-Wei Hsia

Private Work

Cluster size

Vbias [V]

All pixels

🔶 225 µm pixels



- Efficiency is ~99.79 % at the threshold of 1000 electrons and ~ 99.72 % at the threshold of 1200 electrons.
- Slight increase in mean value of charge distributions with decrease in threshold.

Settings

- Environment:
 - Chiller temp: 20C
 - dew point: ~13C
- Power
 - HV bias: -100 V



Normal incidence

Voltage (V)

Credit: Corrinne Mills

Acknowledgements

This document was prepared by CMS using the resources of the Fermi National Accelerator Laboratory (Fermilab), a U.S. Department of Energy, Office of Science, Office of High Energy Physics HEP User Facility. Fermilab is managed by Fermi Research Alliance, LLC (FRA), acting under Contract No. DE-AC02-07CH11359.



• Slight increase in cluster size at lower threshold.

Future Work

- Understand the effect of cross talk on cluster size and resolution.
- Validating final modules for the upgrade.

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