Field Response Simulation (Garfield ++) Update New Response File Summary

Jiangmei Yang

PhD Student, jyangdr@connect.ust.hk

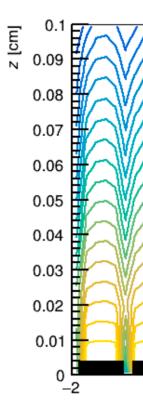
May. 10th, 2024

2x2 Sim_Cal_meeting



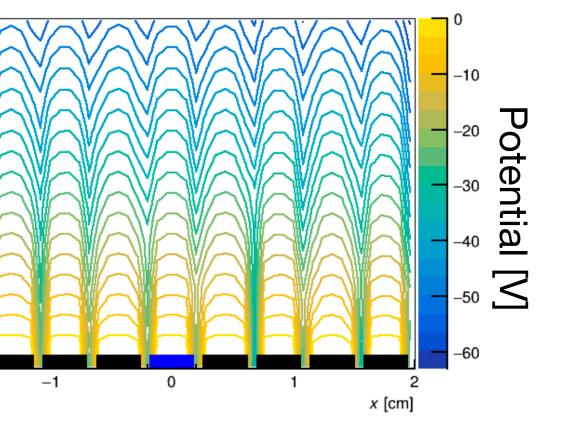
From last update (Mar. 26th)

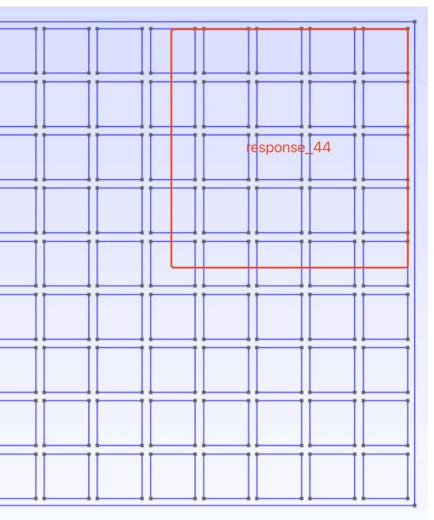
- In the previous simulation, we assigned a small offset voltage on the backing plane in order to drift electrons to the pixels
 - -50V used in generating current response_38 and response_44
 - Modify potential between pixels a lot
- New Geometry: Including a FR4 layer, with dielectric constant but not assigning voltage
 - FR4: 0.1mm thickness
 - Closer to reality
 - Able to drift all electrons to the pixels



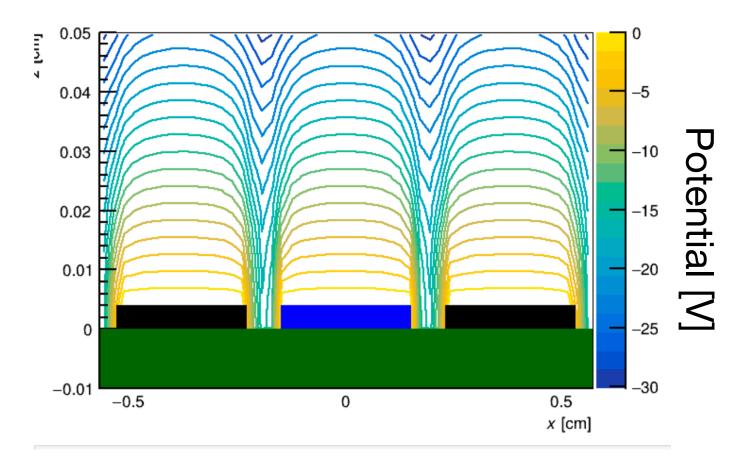
-50V as backing plane voltage



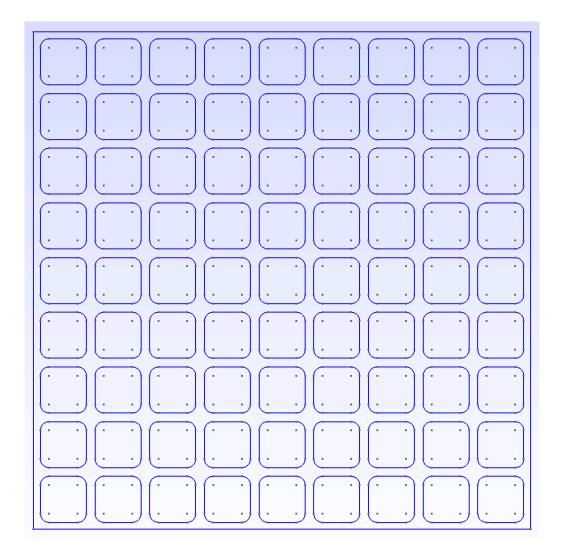




Square Corner



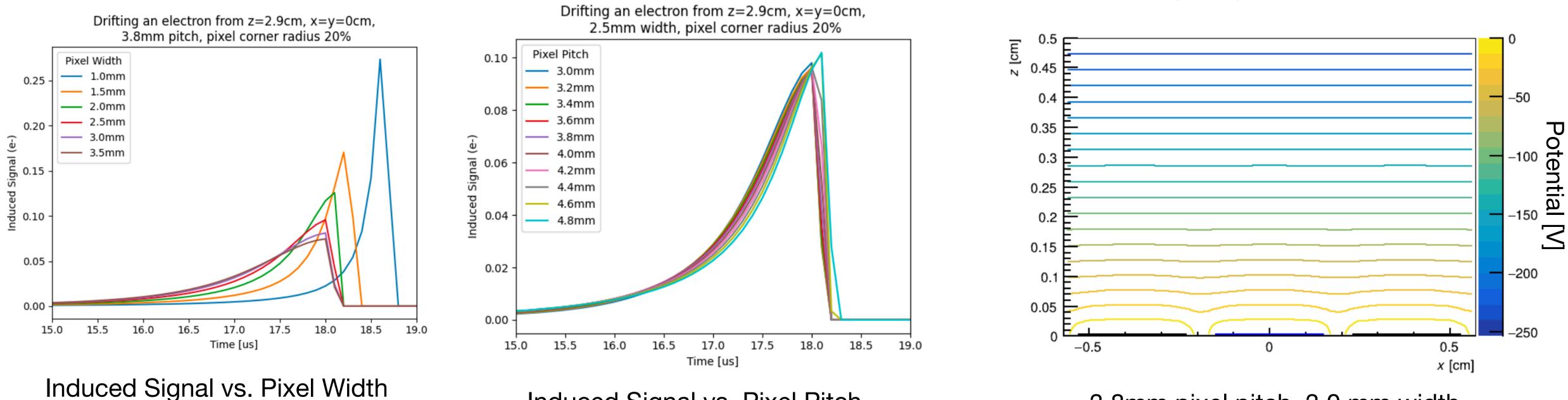
FR4 dielectric with no voltage assigned



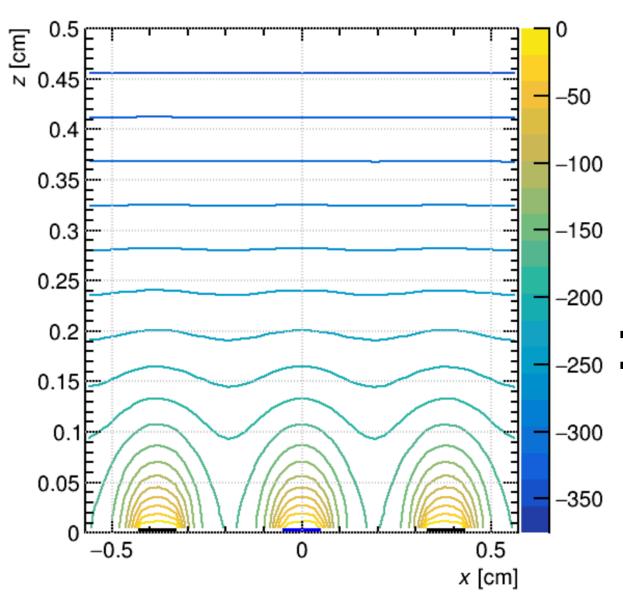
Round Corner

Induced Signal vs Pixel Size

- Given the changes in fill factor, pixel size changes
- Induced signal shape and arrival time change
- Smaller Pixel Width, less uniform field, longer e- drift time



Induced Signal vs. Pixel Pitch

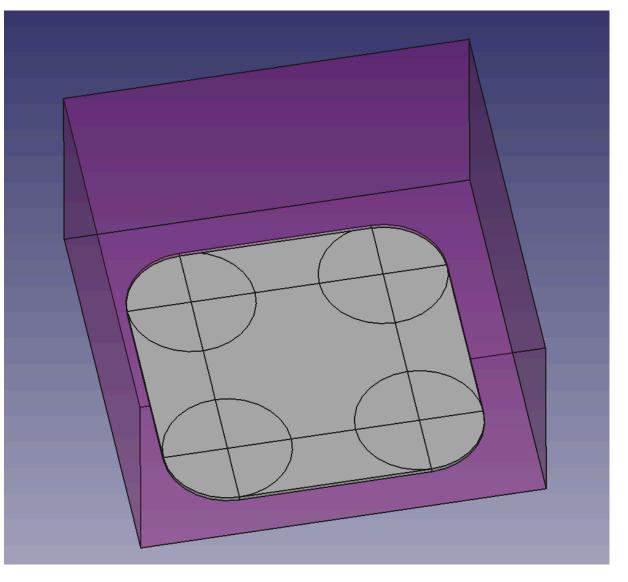


3.8mm pixel pitch, 1.0 mm width



3.8mm pixel pitch, 3.0 mm width

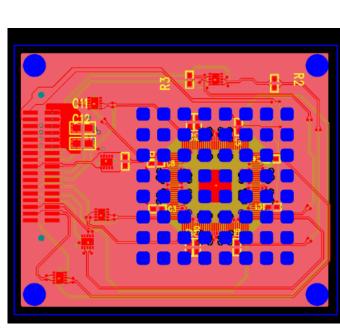




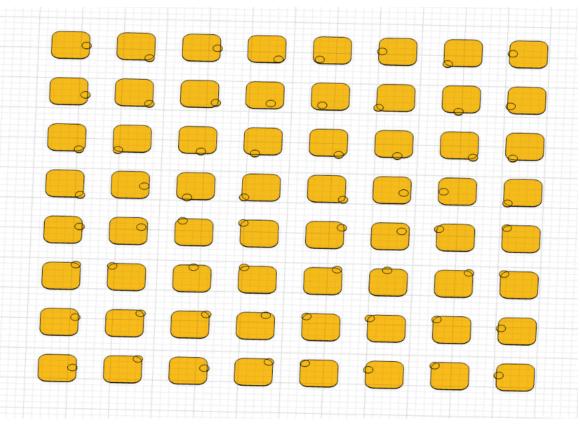
Current response_38

- In larnd-sim (module 1), 3.8mm-pitch pixel uses 3.0mm as pixel width (LArPix v2b/v3)
 - 0.6mm as corner circle radius
- In the latest development, field factor changed from 70% -> 40%
 - pitch size should be smaller

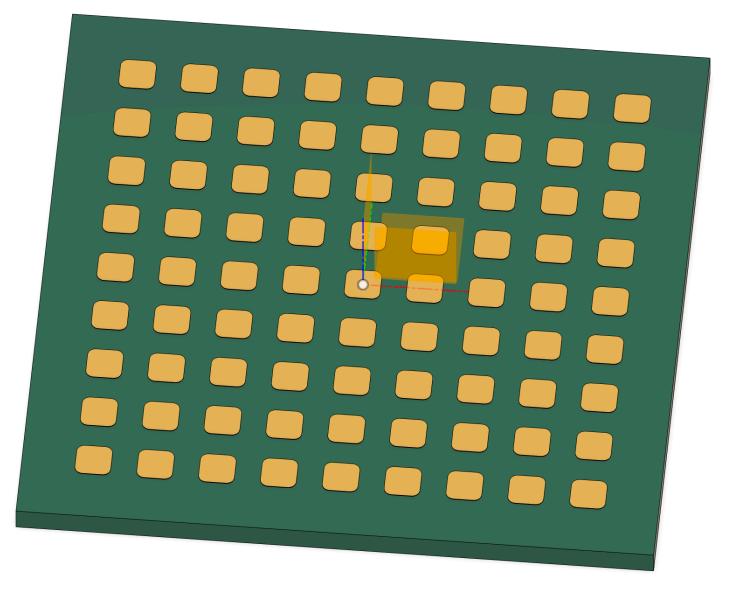
- Convert Single Chip (64) pixels) Gerber to CAD
- Since we are using single pixel induced signal as each pixel's response, remove the vias pattern.
- Current response_38 and 44 was simulated with 9x9 pixels, so expand the single chip 64 pixels to 81 pixels and using central pixel as readout
- Modified the pixel pitch to be consistent with land-sim's



Single chip gerber



Single Chip 8x8 pixels with vias



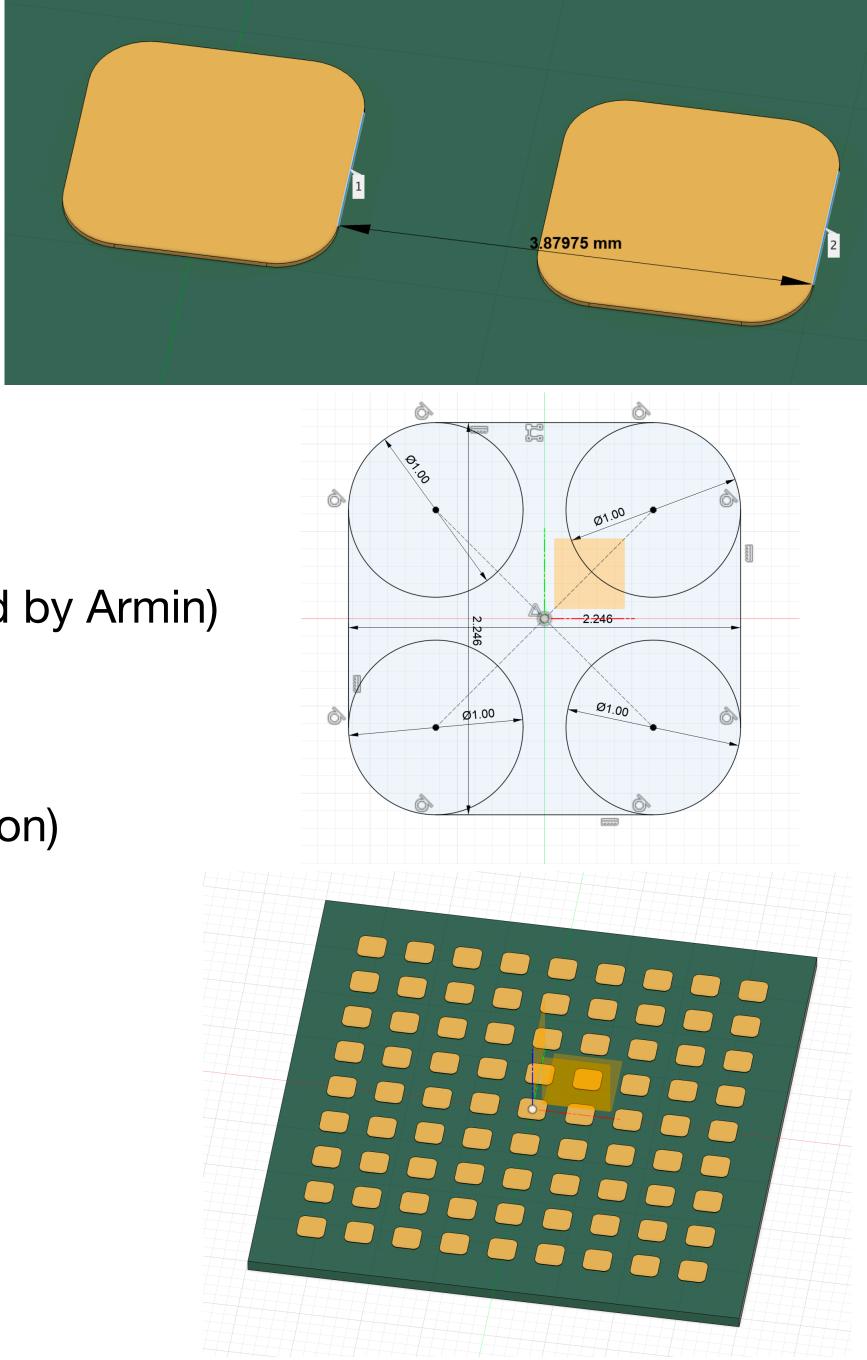
New response_38 (v2b) 9x9 pixels no vias



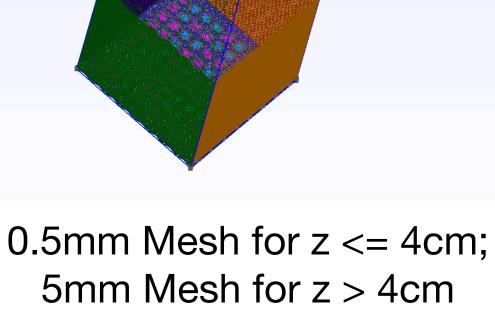


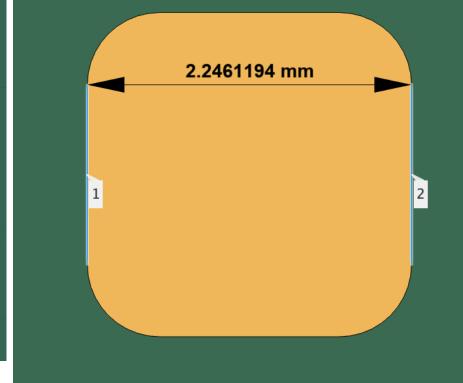
response_38_v2b dimension

- Pixel Pitch: 3.87975mm
- Pixel Width: 2.2461194mm
- Corner Circle radius: 0.5mm
- Pixel Thickness: 1oz copper, 43um (Measured by Armin)
- FR4 Thickness: 1mm
- Drift Volume: 4cm*4cm*31cm (Cathode position) ullet
 - HV: -15500V
- Electrons drift from 30.27225cm
- Time interval [0,200) us
 - response_38 was [0,189.8) us



9x9 pixels no vias

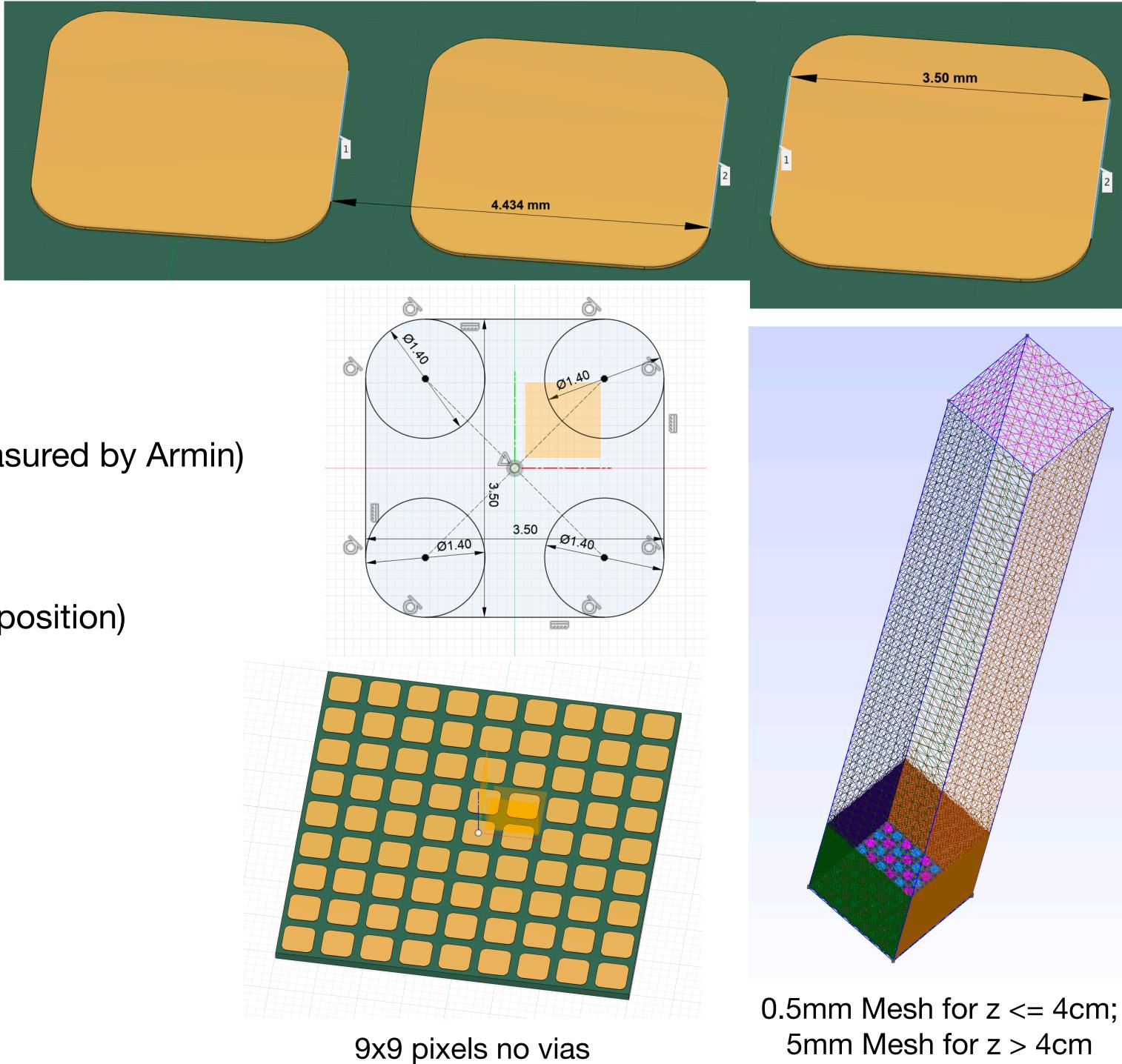






response_44_v2a dimension

- Pixel Pitch: 4.434mm
- Pixel Width: 3.50mm ullet
- Corner Circle radius: 0.7mm
- Pixel Thickness: 1oz copper, 43um (Measured by Armin)
- FR4 Thickness: 1mm
- Drift Volume: 4cm*4cm*31cm (Cathode position) ullet
 - HV: -15500V
- Electrons drift from 30.27225cm
- Time interval [0,200) us
 - response_44 was [0,189.1) us

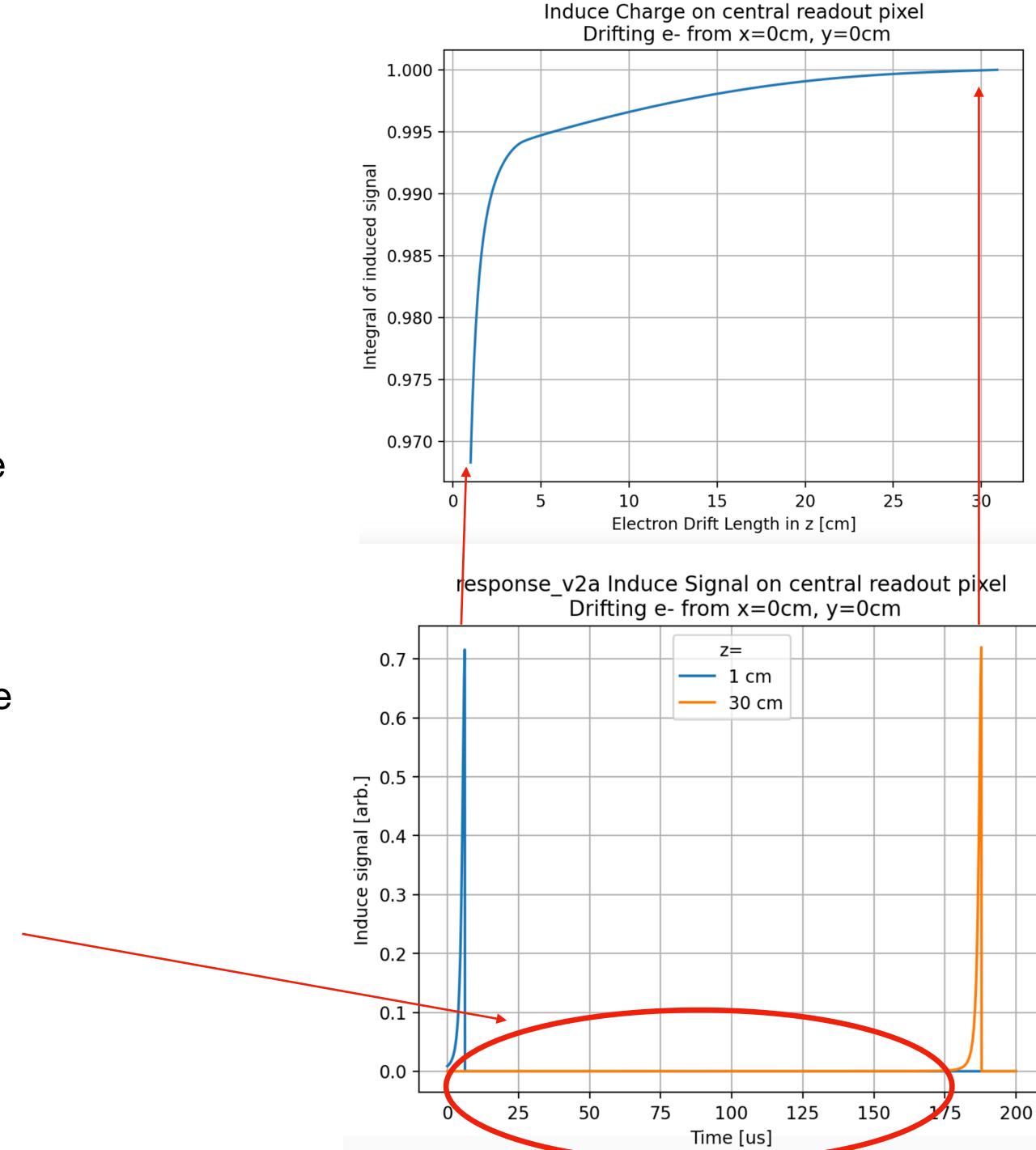


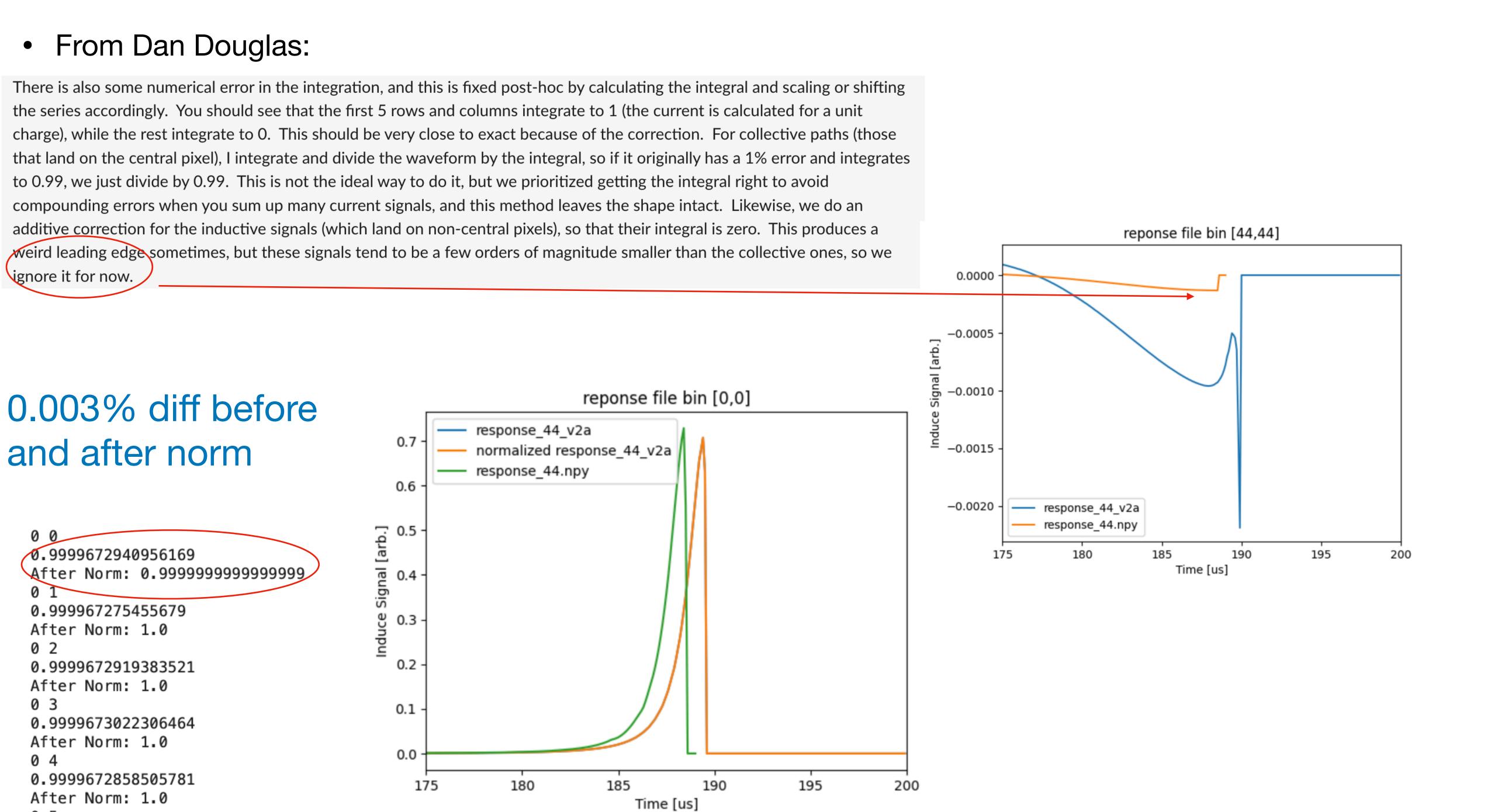




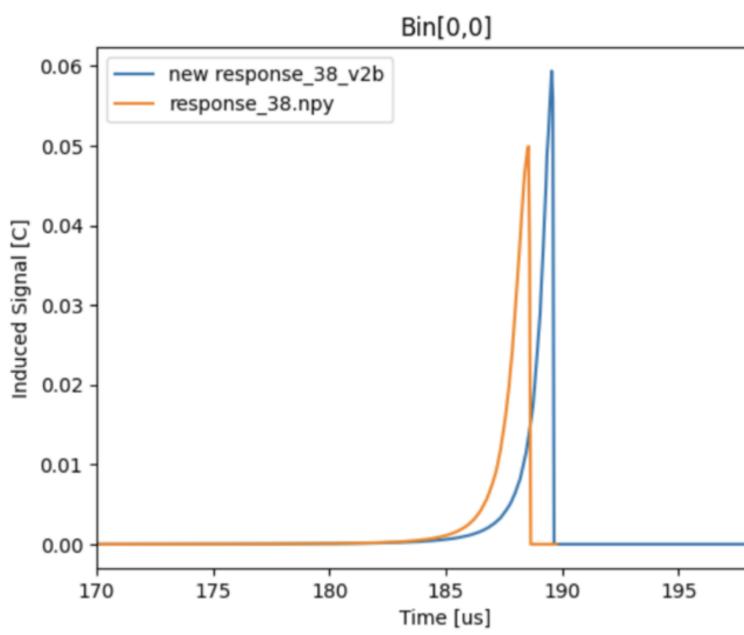
Normalization?

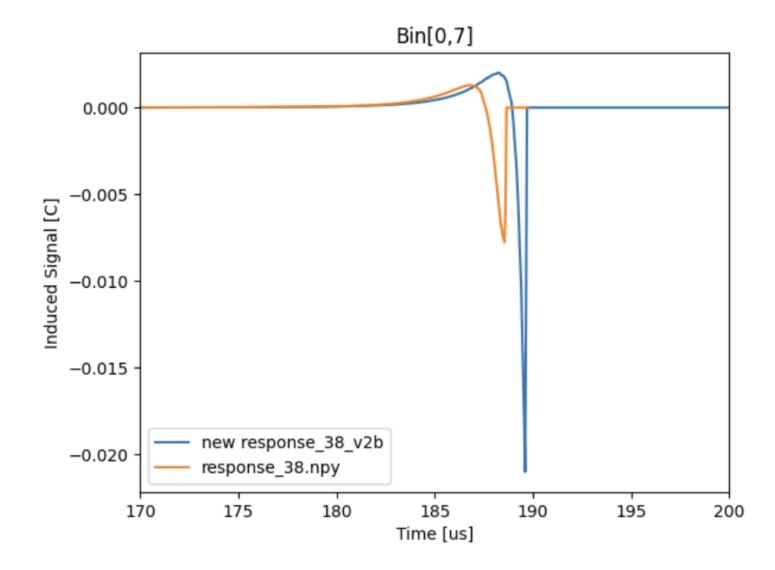
- Previous response signal was normalized to unity if e- arrives on readout pixel, to 0 if earrives on nearby pixels
- Thought:
 - The integration of induce signal may not be unity charge
 - But position dependent
 - As the e- formation position closer to the cathode, the induce signal integration closer to 1 e-
 - e.g. integrated charge from z=1 cm and z=30 differ by ~3.5%, which given by the far-field induce signal tail
- So maybe normalization is not necessary?





- response_44_v2b.npy and response_38_v2a.npy are on land-sim GitHub
 - Not normalized
- To do
 - Compare larnd-sim/ndlar_flow with the old and new response files







Backup