# **HLS4ML Integration with QICK** Mohamud Ali, Northwestern University I Advisors: Nhan Tran, Giuseppe G, Javi C, Fermilab

#### **I-Introduction**

The QICK (Quantum Instrumentation Control Kit) integration aims to enhance the readout of superconducting qubits by Train the HLS4ML leveraging machine learning (ML) techniques. These techniques Model offer high accuracy, increased speed, and better state preservation for qubit readouts. The integration process employs neural network algorithms to optimize system performance and achieve high accuracy rates. The QICK board uses an FPGA, allowing for Generate efficient real-time processing and high-performance execution of **FPGA** C++ Code machine learning algorithms. RTL Vivado HLS Synthesis

# **II- Purpose of the Project**

The goal of the "ML-based Qubit readout with QICK" is to develop and implement a machine learning-driven approach for the efficient and accurate readout of qubits using the Quantum Instrumentation Control Kit (QICK).

# **III- Tools and Material**

**Software**: Vivado, HLS4ML, Python, VNCserver, and C++





**Figure 1: QICK Board Hardware** 

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### **IV-Design Flow**

Figure 2: QICK Firmware Design Flow

# V-ML Model Results



- In validation data the Categorical Cross-Entropy is quite sensitive to weight swing over first 10 epochs.
- Loss difference between training data and validation data decreases as epochs increase, two lines merge close together.



Three different clusters of distribution

- Significant spike at zero
- Two small clusters at -0.4 & 0.4

Effect of Pruning: majority of weights have been set to zero, which is typical in model pruning to reduce model complexity and size. The remaining weights are clustered around -0.4 and 0.4, suggesting that these weights are more significant and have been retained through the pruning process.

#### VI- Keras vs. HLS Model





- The data points are closely aligned along the diagonal line, showing that both the fully-connected layer (fc1) and batch normalization outputs are very similar
- The data points clustering along the diagonal line in both graphs indicate that the performance across different layers.



Figure 6: Dual layer NN Architecture Flow

**Fixed-Point Arithmetic**: Ensures efficient hardware implementation with specific truncation (TRN) and wraparound (WRAP) handling for overflow.

#### **VIII- References**

- learning/hls4ml





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between the two models. This indicates strong correlation between the two models.

HLS model effectively replicates the Qkeras model's behavior, ensuring consistent

	Keras	HLS
ccuracy	95.405%	95.407%

Figure 7: Accuracy measure of model training

#### VII – Future work

Future work will include RTL synthesis, implementation on FPGA and QICK board to ensure Qbit readout data is correct with the expected accuracy level, and further verification to validate the design's real-world performance.

1. HLS4ML Tutorials access to this link "Document" HLS4ML GitHub <a href="https://github.com/hls-fpga-machine-">https://github.com/hls-fpga-machine-</a>

#### 3. QICK Reference "https://arxiv.org/pdf/2110.00557"

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