



My TARGET Internship

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Supervisor: Fred Lewis

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Miguel Mendez

Walter Payton College Preparatory
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Class of 2026

Areas of Interest:

Physics, Mechanical Engineering, Quantum
Engineering, & Aerospace Engineering

Introduction to the Test & Instrumentation CIS Team



Fred Lewis
Supervisor



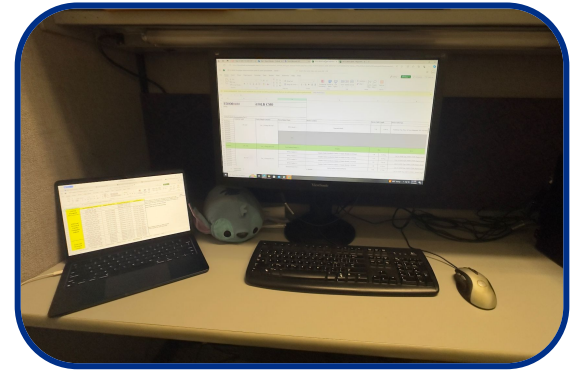
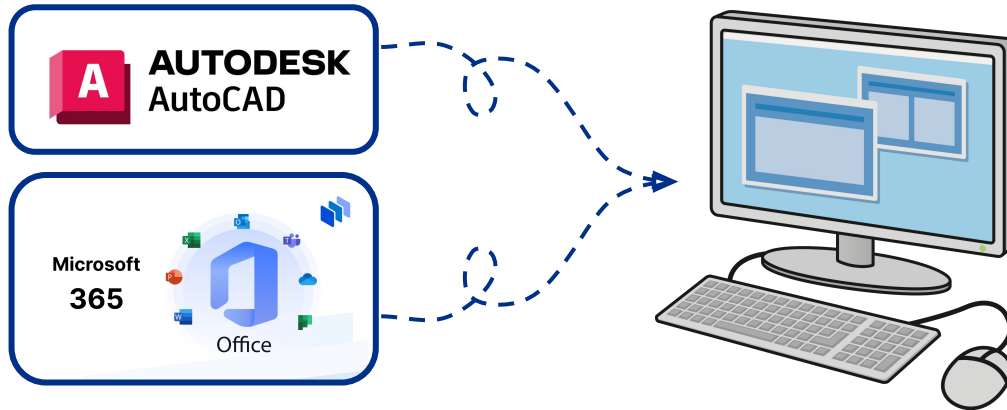
Miguel Mendez
TARGET Intern



Cubicle Buddies
David, Bill, Alexander, Brianna, & Junko

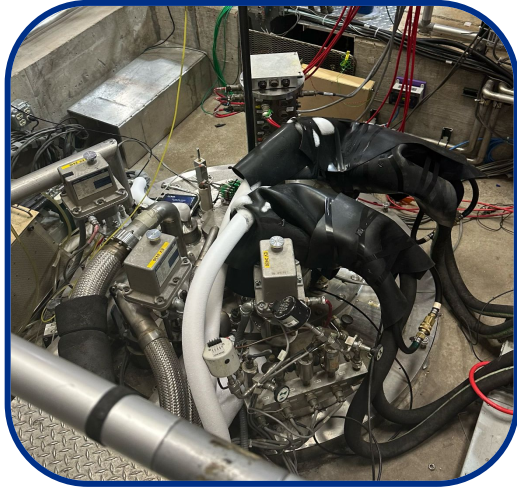
Week 1 - Settling In

My very first task of the internship was to get myself settled in by installing the necessary software I would need for the following 5 weeks.



My workspace in Industrial Building 4

Week 2 - Connection of Test Stand 7 to VMTF



Vertical Magnet Test Facility (VMTF)



Test Stand 7

Week 2 - Connection of Test Stand 7 to VMTF

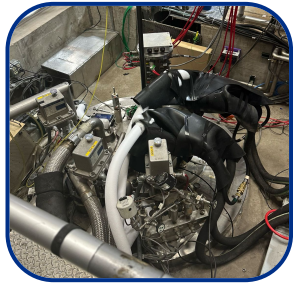
VMTF vs. Test Stand 7

Similarities

Both test the performance of **superconducting magnets**. Unlike conventional magnets which work in room temperature, superconducting magnets must be at a temperature of about 4° Kelvin (-452° F) to function.

VMTF

- Uses liquid helium
- Dewar is 20 ft deep.
- Ability to test large magnets.



Test Stand 7

- Uses cryocooling
- Dewar is 6 ft deep.
- Allows for small magnets to be tested without the need of VMTF



Week 2 - Connection of Test Stand 7 to VMTF

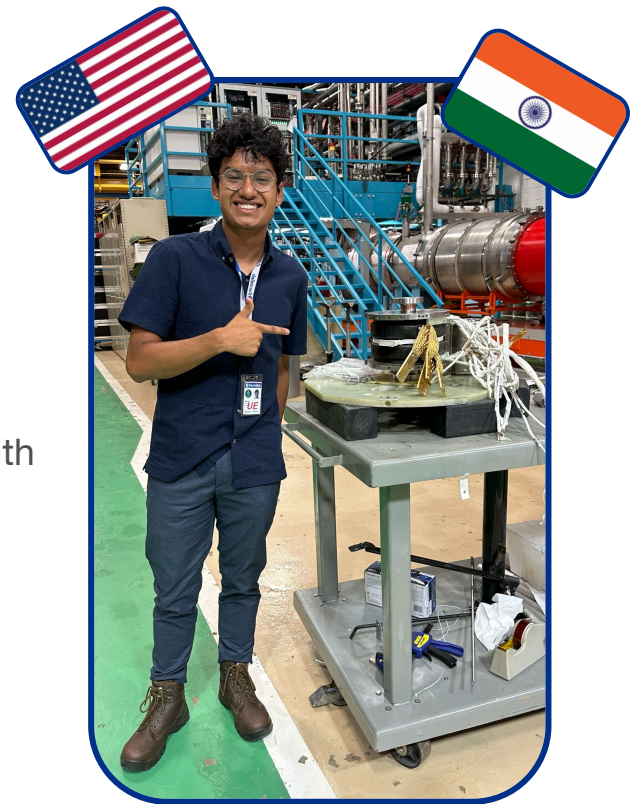
Outlining the task:

Provide a physical connection of the instrumentation & power supply from Test Stand 7 to VMTF in order to use VMTF with Test Stand 7 infrastructure

In short, this would involve creating **electrical schematics** for a visual block diagram, cable specifications, and power supply specifications with the use of **AutoCAD**.

The goals:

- Allow for the flexibility of utilizing VMTF with Test Stand 7 infrastructure.
- Conduct a performance test of India's BARC 1 and 007 superconducting magnets with this new available option.

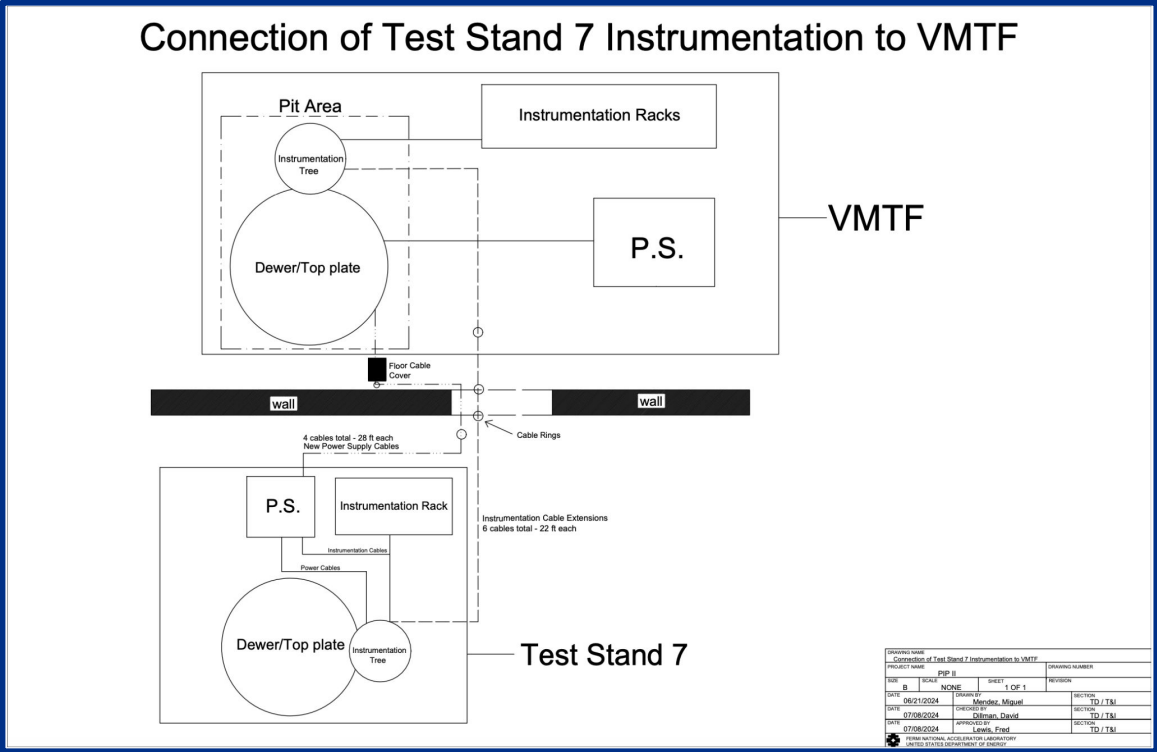


Me standing beside the BARC magnets. India will use these in one of their BARC facility by submerging them in liquid helium and thus removing any temperature concerns.

Week 2 - Connection of Test Stand 7 to VMTF

Delving into the details:

- 6 instrumentation extension cables connect stand 7 to VMTF
- 4 new power supply cables from stand 7 provide power to VMTF
- Visualizing floor layout and possible obstacles



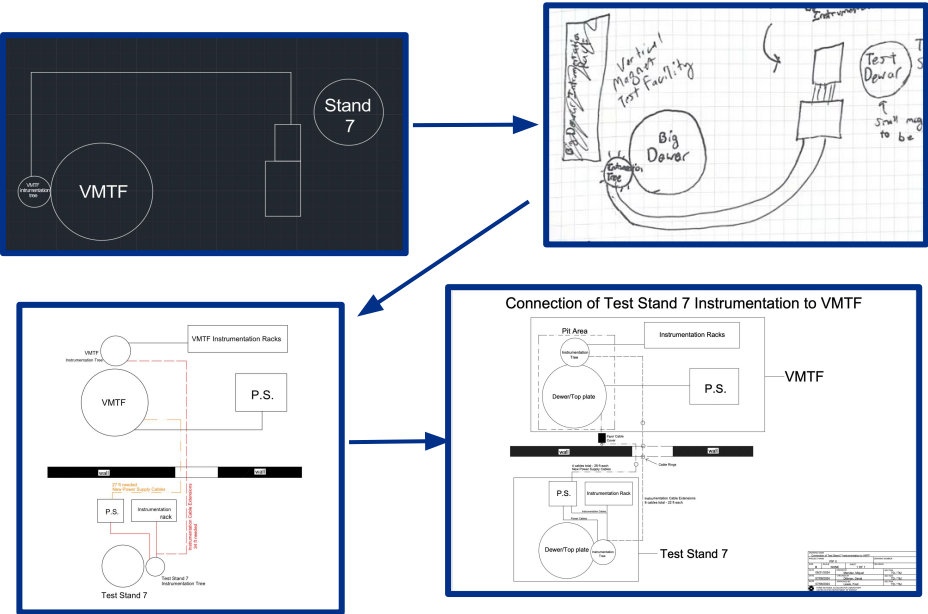
The Final Block Diagram I Would Make

Week 3/4 - Schematics

Visual Block Diagram Timeline

Although making schematics sounds fairly simple... It is truly a complicated and time consuming endeavor.

Shown on this slide is only a small portion of the many versions I made of only the block diagram.



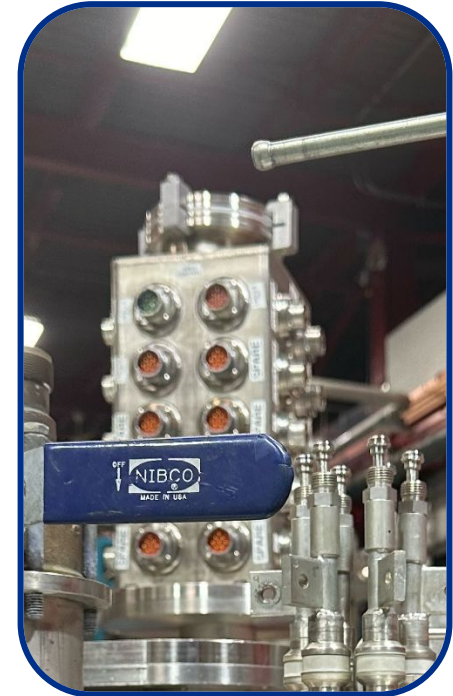
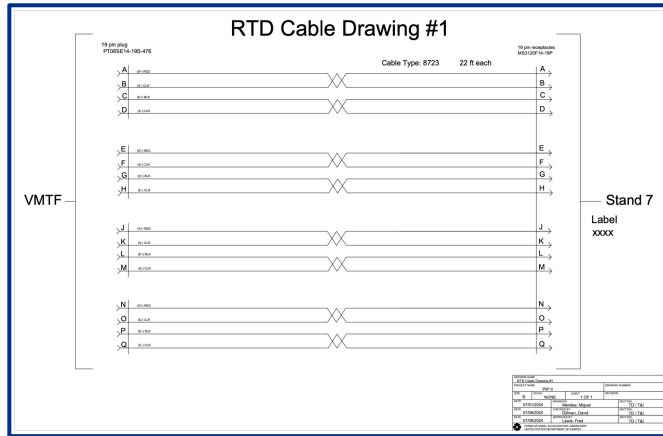
Week 3/4 - Schematics

Instrumentation Cable Specification Schematics

Why are they needed?

These ensure that an electrical technician can correctly make a cable with a proper length, wiring, and connectors.

Here's what one looks like!



The block with many connection ports is the instrumentation tree. That is where all instrumentation cables connect to

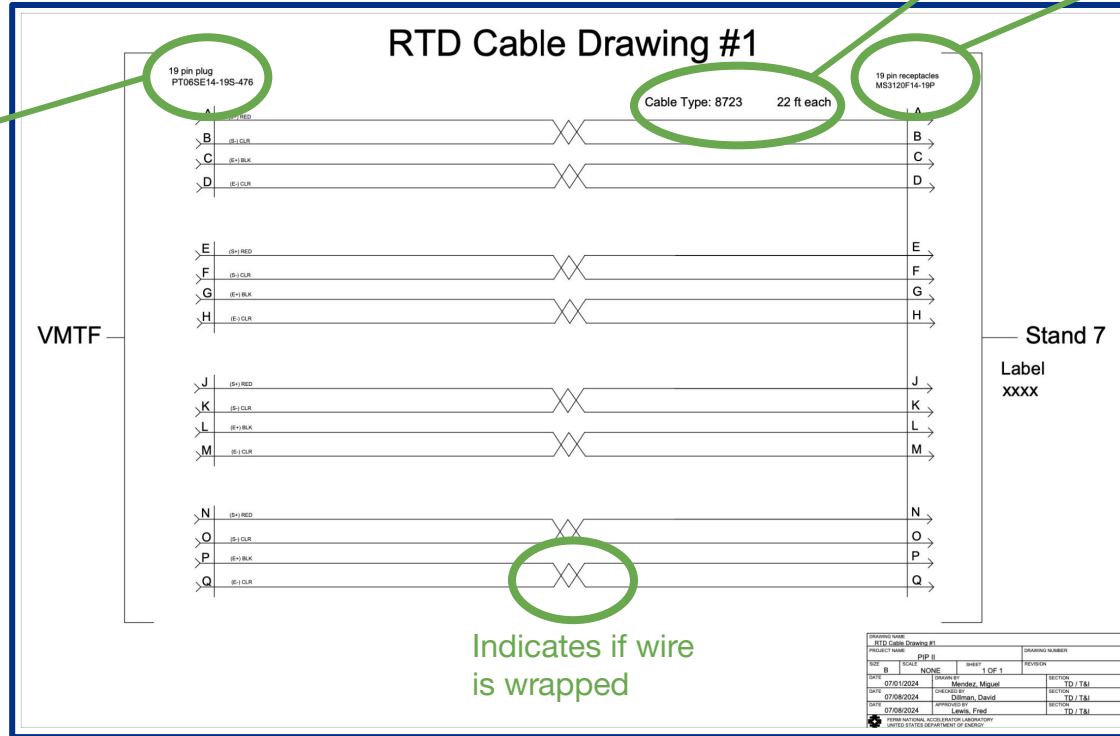
Week 3/4 - Schematics

Overview: Instrumentation Cable Specification Schematics

One side of connector specifications

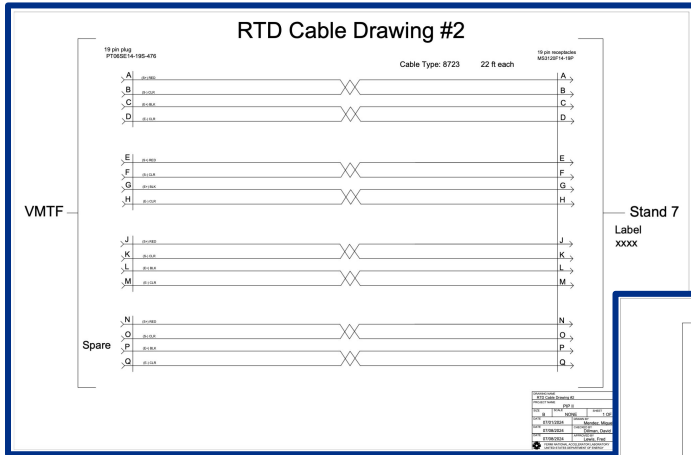
Cable type and length

Other side of connector specifications

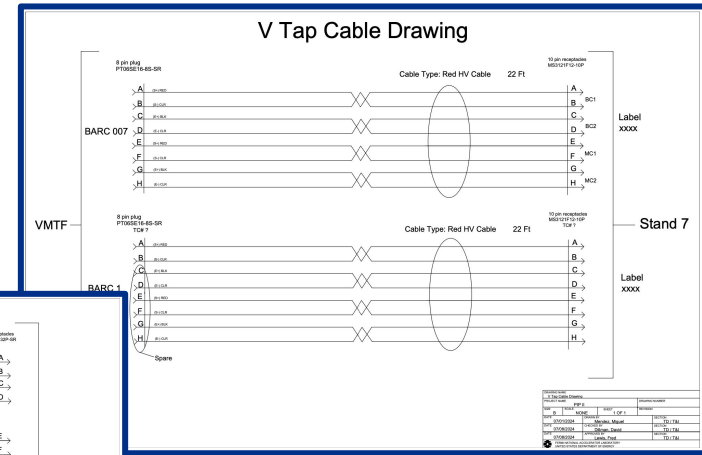


Week 3/4 - Schematics

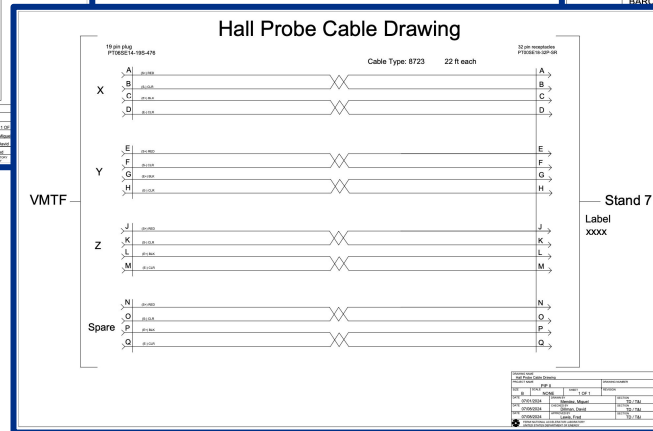
The Rest of Instrumentation Cable Specification Schematics



Resistance Thermal Device (RTD) measure the resistance of the magnet and thus correlate with the temperature



Detects drops in voltage within a magnet, thus stopping the run of electricity through so that there is no magnet damage

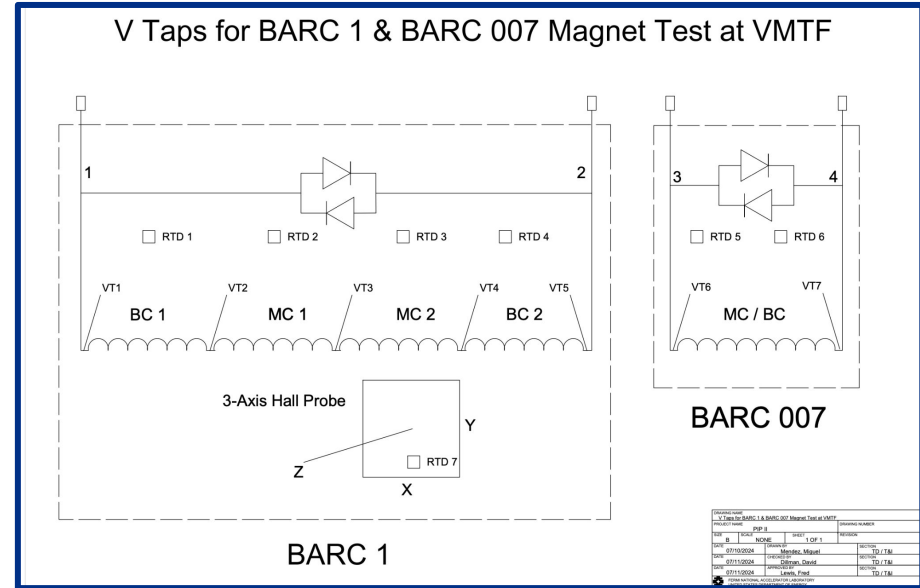
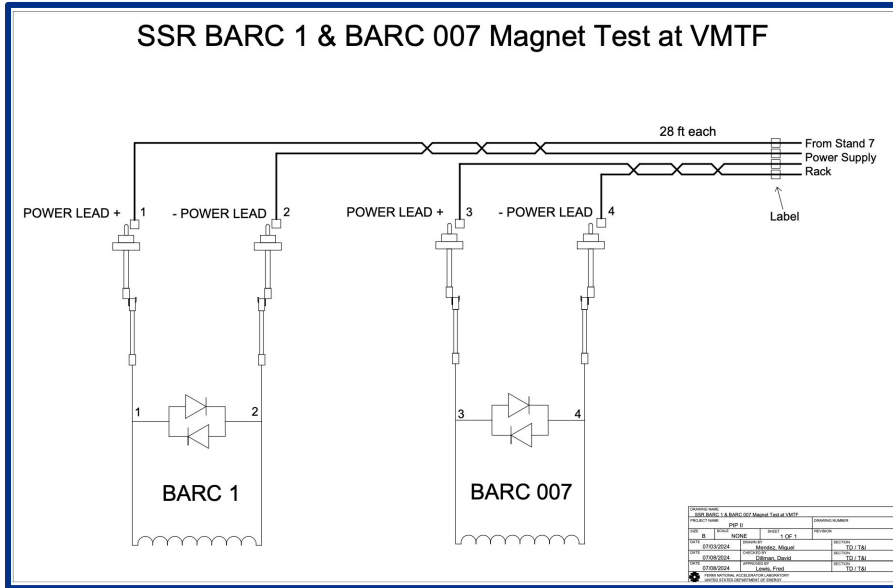


Hall probe tests the strength of the magnet's magnetic field

Week 3/4 - Schematics

The Power Supply Schematics For The BARC Magnets

These provide an electrical technician the sufficient information necessary to link up magnets with electricity to conduct a performance test at VMTF.



Week 5/6 - Labeling PIP-II Cryomodules LB650

What is a Cryomodule?

A section of a particle accelerator composed of superconducting RF cavities. Those RF cavities take in radio-frequency power and convert it into acceleration for the particle beam.



A cryomodule being shipped to California



LCLS-II Cryomodule

Week 5/6 - Labeling PIP-II Cyromodules LB650

Why is labeling Important?

Although it is quite a small task, labeling is extremely critical and important to do correctly. If anything of the smallest scale goes wrong, it can have very long and expensive consequences. Labeling tells a technician exactly where a device must be connected. Without those labels, it would be impossible to know where connection is meant to take place.







An RTD cable with an extension cable beside it. Labeling is shown.



Week 5/6 - Labeling PIP-II Cryomodules LB650

Reviewing Labeling Procedures

I was given procedures used by all instrumentation employees here at Fermilab and with all of our foreign partners. Upon reading over every detail, I was encouraged to recommend any changes I deemed necessary.

 FERMILAB APS-TD&I Department	CIS PROCEDURE GENERAL PROCEDURE CREATING CABLE LABELS	Doc. No. TID-N-1135 Rev. No. 1.3 Date: 12/29/2016 Page 1 of 8 TC# ED0007529	
 FERMILAB APS-TD Test and Instrumentation Department CIS PROCEDURE FOR GENERAL PROCEDURE CREATING CABLE LABELS			
Prepared by: Jeremy Brown	Date: 12/29/2016 T&I&DAQ	Organization APS-TD&I	Extension X8058
Reviewed by: Tom Cummings	Date: 8/29/2018 T&I&DAQ	Organization APS-TD&I	Extension X3168
Approved by: Fred Lewis	Date: 8/30/2018 T&I&CIS	Organization APS-TD&I	Extension X3975

 FERMILAB APS-TD&I Department	CIS PROCEDURE MISC PROCEDURE APPLYING DESCRIPTION CABLE LABELS TO INSTRUMENTATION PROCEDURE FOR LCLS-II 1.3GHz, 3.9GHz AND HE PRODUCTION CRYOMODULES	Doc. No. TID-N-866 Rev. No. 1.11 Date: 1/23/2017 Page 1 of 11 TC# ED0007516
 FERMILAB APS-TD Test and Instrumentation Department CIS PROCEDURE MISC PROCEDURE APPLYING DESCRIPTION & SERIAL NUMBER CABLE LABEL TO INSTRUMENTATION FOR LCLS-II 1.3GHz AND 3.9GHz AND HE PRODUCTION CRYOMODULES		

 FERMILAB APS-TD&I Department	CIS PROCEDURE GENERAL PROCEDURE PRINTING LABELS USING BRADY WORKSTATION LITE	Doc. No. TID-N-1502 Rev. No. 1.5 Date: 6/18/2021 Page 1 of 13 TC# ED0014078
 FERMILAB APS-TD Test and Instrumentation Department CIS PROCEDURE GENERAL PROCEDURE PRINTING LABELS USING BRADY WORKSTATION LITE		

These are the front covers of the procedures which I received

Reflections

- Learning to utilize AutoDesk AutoCAD
- Exposure to electrical engineering
- Importance of taking time for quality control
- Learning is always a part of the job
- Everyone of all levels make mistakes

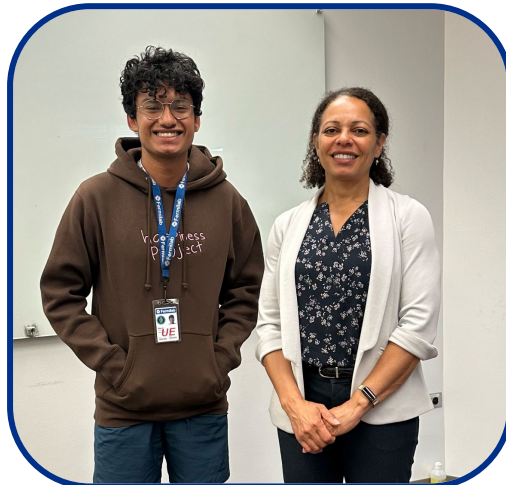
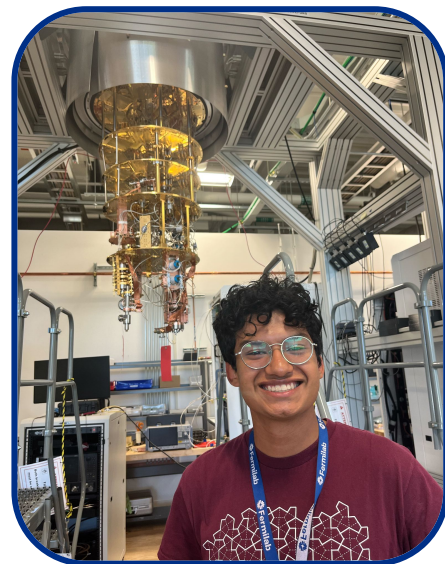


My Favorite Tour

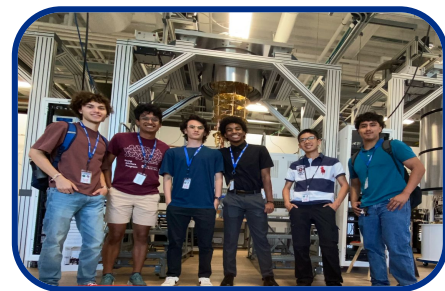
While we got tour many amazing facilities at the Fermilab site, SQMS caught my attention the most.

What I loved most about SQMS is their Quantum Garage. We got to see several dilution fridges!

As a result of this tour, I am now more heavily interested in pursuing a career in the Quantum field.



Not with the tour. I got to meet the uChicago Pritzker School of Molecular Engineering Dean Nadya Mason.



My Favorite Workshop

Python Workshop

Through this workshop, I was able to significantly improve my coding skillset which included learning the topics such as:

- Variables
- Functions
- Tuples
- Classes & Methods

Going forward, I will follow Mr. Mambelli's advice on continuing to learn python with Zed Shaw's *Learn Python The Hard Way* book.

```
class counter_class:
    def __init__(self, initial_value=0): # Optional argument with default at 0
        self.counter_var = initial_value
    def increment(self, increment_value):
        self.counter_var += increment_value
        print("The counter has been incremented to ", self.counter_var)
        return self.counter_var
    def double_increment(self, increment_value):
        multiplier = 2 # this is a local variable
        self.counter_var += multiplier * increment_value
        print("The counter has been incremented to ", self.counter_var)
        return self.counter_var
```





Acknowledgments

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Recommenders

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Ms. Dao

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THANK YOU FERMILAB!

