

Integration of GET electronics on TPC for HIC program at RIBF

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A Time Projection Chamber (TPC) for the heavy ion collision experiments has been produced as the main detector of SAMURAI-SPIRIT project for the study of nuclear equation of state.

As the readout system for the SPIRIT-TPC, we are integrating the GET system, which stands for the General Electronics for Tpc and was developed mainly by France and USA collaboration.

For the integration of GET electronics, development of interfaces in terms of both hardware and software are necessary.

For example, the interface of the electronics of GET to TPC depending on the each detector specifications, such as type of connector and characteristics of detector signal, has to be developed.

We call such interface ZAP board, which is for adapting connector on TPC and for protection of electrical circuit.

The board was designed to fit in the space on the TPC which is supposed to be installed in SAMURAI chamber, to reduce the noise to gain the dynamic range, and to reduce the distortion of gain among the different channel.

The achieved noise level with our ZAP is 4~ADC under the configuration of dynamic range of 120~fC and shaping time of 233~nsec.

It can be reduced to be 2~3~ADC after the subtraction of fixed noise pattern line which is not connected to TPC pads.

In addition to the report of the status of development and intergration of electronics, the benchmark test data of GET system on Brahm's-TPC performed at HIMAC at 2014 Nov will be presented.

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